

June 2013

# **FJP5554**

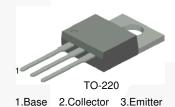
# **NPN Silicon Transistor**

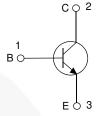
### **Features**

- · Fast Speed Switching
- Wide Safe Operating Area
- · High Voltage Capability

### **Application**

- · Electronic Ballast
- · Switch Mode Power Supplies





# **Ordering Information**

Part Number	Marking	Package	Packing Method	
FJP5554TU	J5554	TO-220	Rail	

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_{\Delta} = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units
BV <sub>CBO</sub>	Collector-Base Voltage	1050	V
BV <sub>CEO</sub>	Collector-Emitter Voltage	400	V
BV <sub>EBO</sub> Emitter-Base Voltage		15	V
I <sub>C</sub>	Collector Current (DC)	4	Α
I <sub>CP</sub>	Collector Current (Pulse)	8	Α
Ι <sub>Β</sub>	Base Current (DC)	2	Α
I <sub>BP</sub>	Base Current (Pulse)	4	Α
TJ	Junction Temperature	150	°C
T <sub>STG</sub> Storage Junction Temperature Range		- 55 to +150	°C

### **Thermal Characteristics**

Values are at T<sub>A</sub> = 25°C unless otherwise noted.

Symbol	Parameter		Value	Units
P <sub>D</sub>	Total Device Dissipation $T_C = 25^{\circ}$		70	W
$R_{\theta jc}^{(1)}$	Thermal Resistance, Junction to Case		1.78	°C/W

1

#### Note:

1.  $R_{\theta ic}$  test fixture under infinite cooling condition.

# Electrical Characteristics(2)

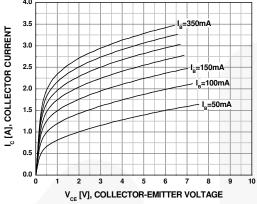
Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 500  \mu A, I_E = 0$	1050			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 5 \text{ mA}, I_B = 0$	400			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 1 \text{ mA}, I_C = 0$	15		23	V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 1050 \text{ V}, I_{E} = 0$			1	mA
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CB} = 400 \text{ V}, I_{B} = 0$			250	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 15 \text{ V}, I_{C} = 0$			1	mA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ A}$	45		100	
		$V_{CE} = 3 \text{ V}, I_{C} = 0.8 \text{ A}$	20		50	
V <sub>CE(sat)</sub> Collec	Collector-Emitter Saturation Voltage	$I_C = 1 A, I_B = 0.2 A$			0.5	V
		$I_C = 3.5 \text{ A}, I_B = 1.0 \text{ A}$			1.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_C = 3.5 \text{ A}, I_B = 1.0 \text{ A}$			1.5	V
t <sub>ON</sub>	Turn-On Time	V <sub>CC</sub> =125 V, I <sub>C</sub> =0.5 A,			1.0	μs
t <sub>STG</sub>	Storage Time	$I_{B1} = 45 \text{ mA}, I_{B2} = 0.5 \text{ A},$			1.2	μs
t <sub>F</sub>	Fall Time	$R_L = 250 \Omega$			0.3	μs
EAS	Avalanche Energy	L = 2 mH	6			mJ

### Note:

2. Pulse test: pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2\%.$ 

# **Typical Performance Characteristics**



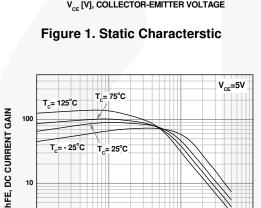


Figure 3. DC Current Gain

I<sub>c</sub> [A], COLLECTOR CURRENT

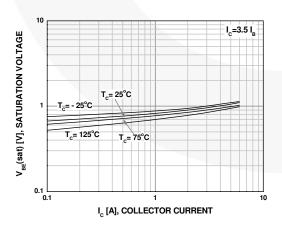


Figure 5. Base-Emitter Saturation Voltage

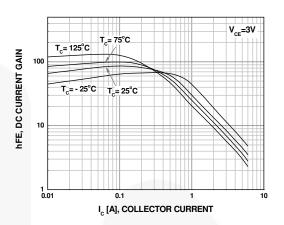


Figure 2. DC Current Gain

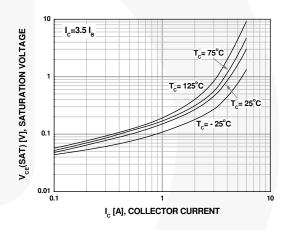


Figure 4. Collector-Emitter Saturation Voltage

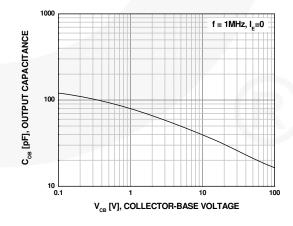
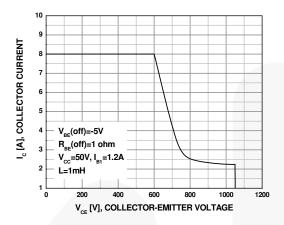


Figure 6. Output Capacitance

1 L 0.01

# **Typical Performance Characteristics** (Continued)



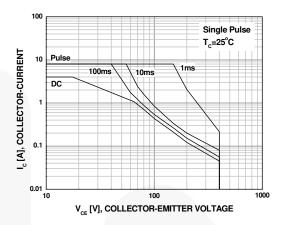


Figure 7. Reverse Biased Safe Operating Area

Figure 8. Forward Biased Safe Operating Area

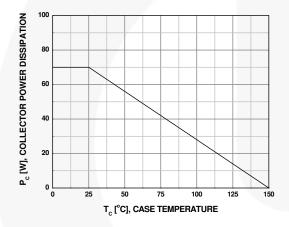
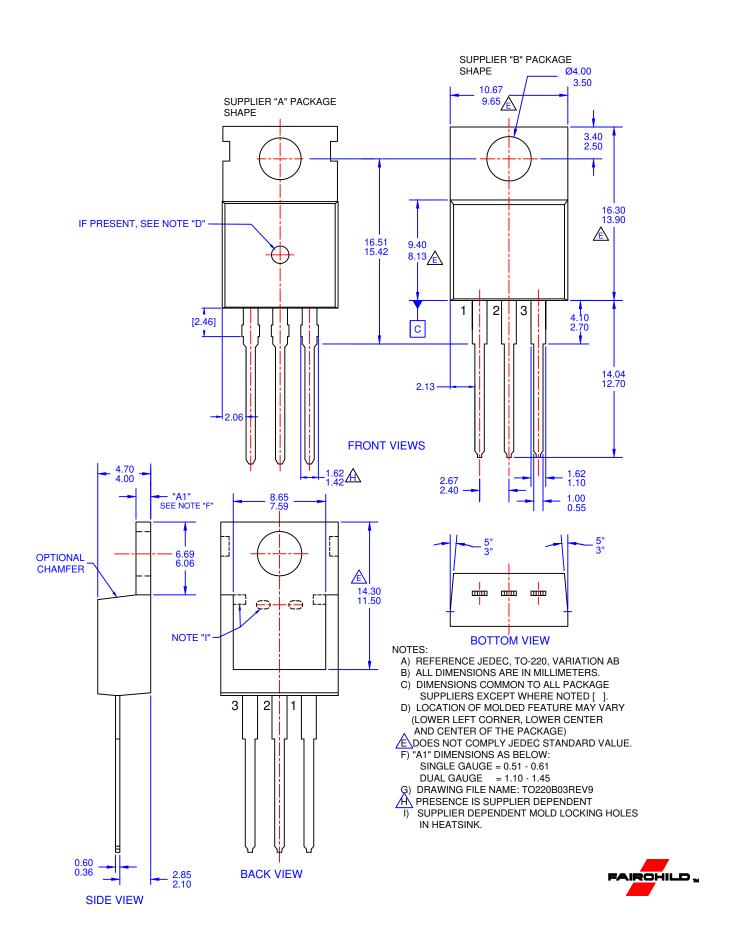


Figure 9. Power Derating Curve







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