

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
20V	0.99Ω @ V <sub>GS</sub> = 4.5V	0.54A
	1.2Ω @ V <sub>GS</sub> = 2.5V	0.49A
	1.8Ω @ V <sub>GS</sub> = 1.8V	0.4A
	2.4Ω @ V <sub>GS</sub> = 1.5V	0.35A

## Description and Applications

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

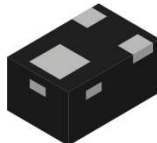
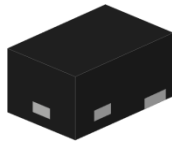
## Features and Benefits

- Low Package Profile
- 0.6mm x 0.4mm Package Footprint
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**  
<https://www.diodes.com/quality/product-definitions/>

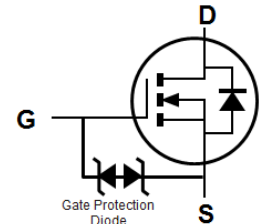
## Mechanical Data

- Case: X2-DFN0604-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)

X2-DFN0604-3



Top View  
Package Pin Configuration



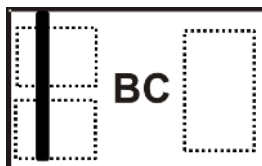
Equivalent Circuit

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2991UFO-7B	X2-DFN0604-3	10k/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



Top View

BC= Product Type Marking Code  
Bar Denotes Gate and Source Side

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	0.54	A
		T <sub>A</sub> = +85°C		0.43	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	1.2	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	P <sub>D</sub>	0.44	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>θJA</sub>	281	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±5V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	—	1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	0.5	0.99	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 100mA
		—	0.6	1.2		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 50mA
		—	0.8	1.8		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 20mA
		—	1.0	2.4		V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 10mA
		—	—	—		—
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	21.5	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	4.9	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	3.7	—	pF	
Gate Resistance	R <sub>g</sub>	—	0.94	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Q <sub>g</sub>	—	0.35	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Gate-Source Charge	Q <sub>gs</sub>	—	0.07	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	0.08	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.6	—	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 47Ω, R <sub>g</sub> = 10Ω, I <sub>D</sub> = 200mA
Turn-On Rise Time	t <sub>R</sub>	—	4.9	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	60.6	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	27.6	—	ns	

- Notes:
5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
  6. Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.

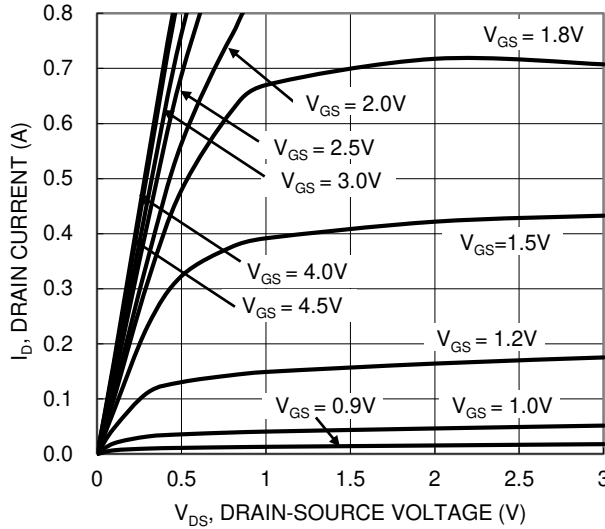


Figure 1. Typical Output Characteristic

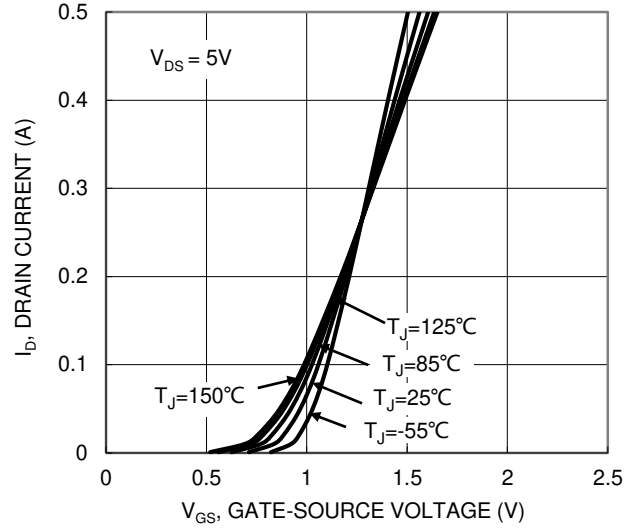


Figure 2. Typical Transfer Characteristic

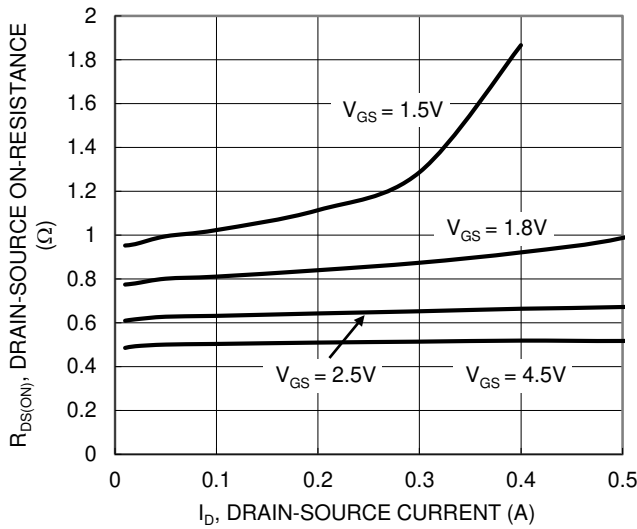


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

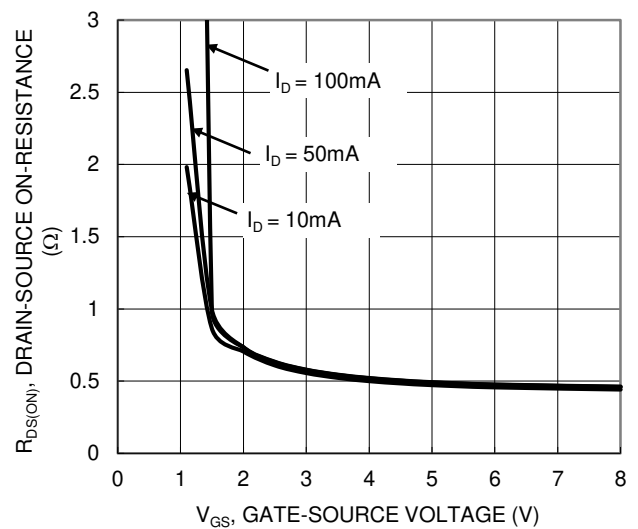


Figure 4. Typical Transfer Characteristic

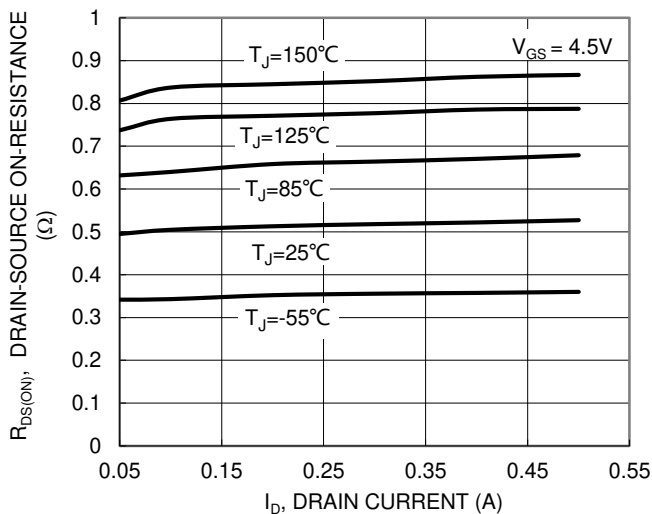


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

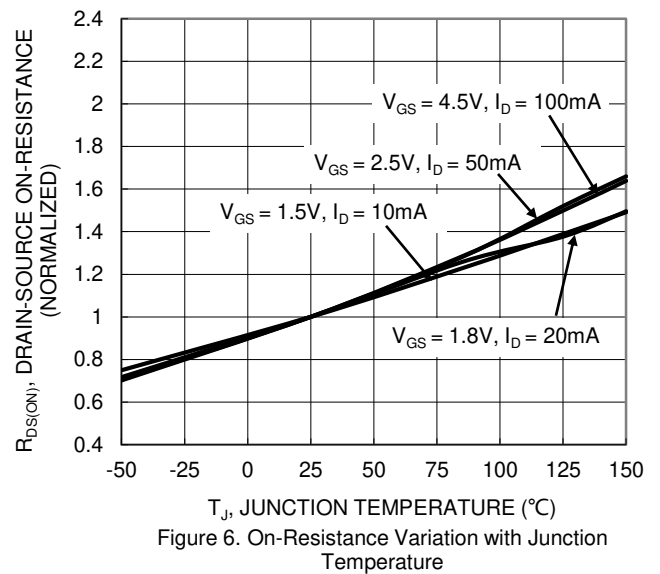


Figure 6. On-Resistance Variation with Junction Temperature

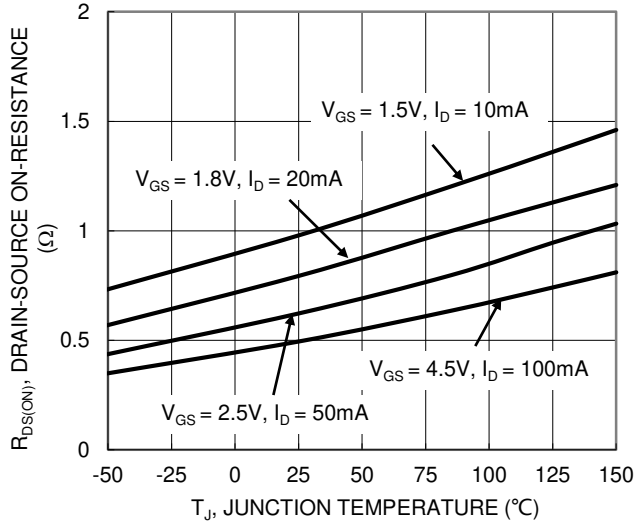


Figure 7. On-Resistance Variation with Junction Temperature

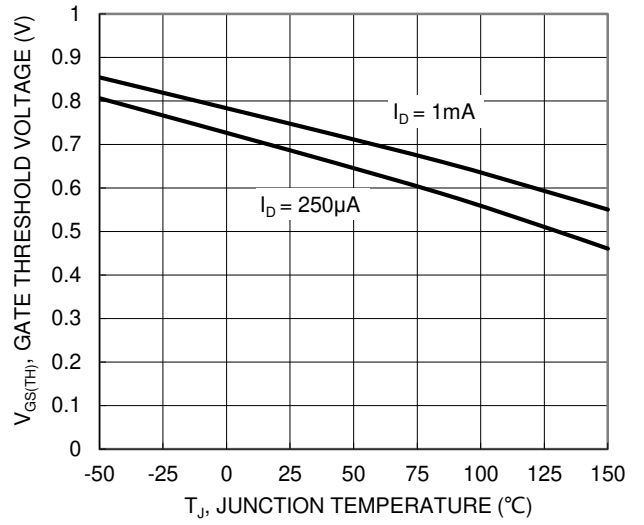


Figure 8. Gate Threshold Variation vs. Junction Temperature

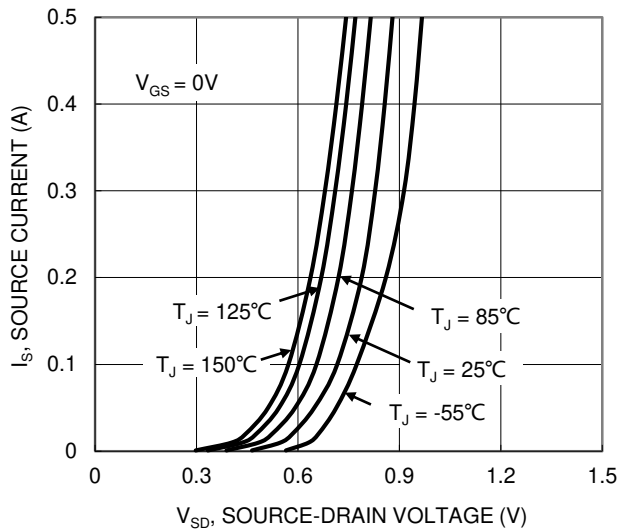


Figure 9. Diode Forward Voltage vs. Current

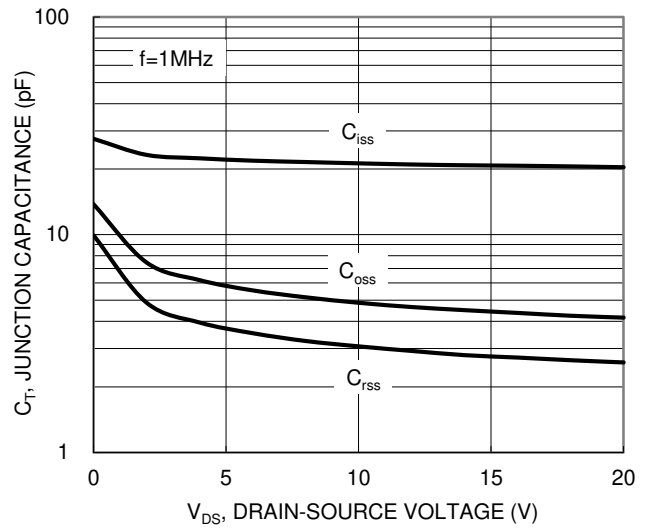


Figure 10. Typical Junction Capacitance

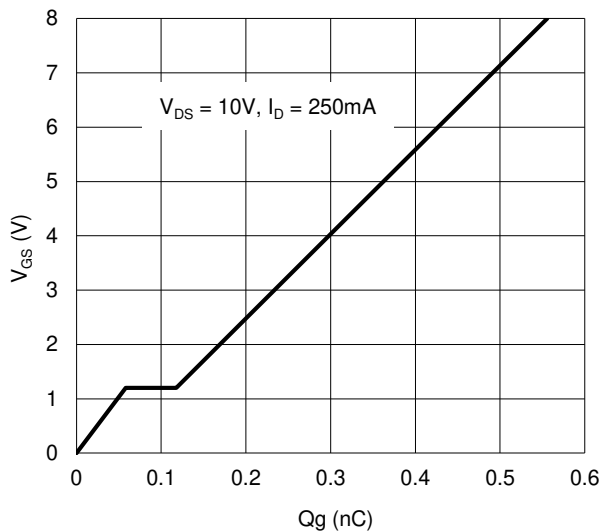


Figure 11. Gate Charge

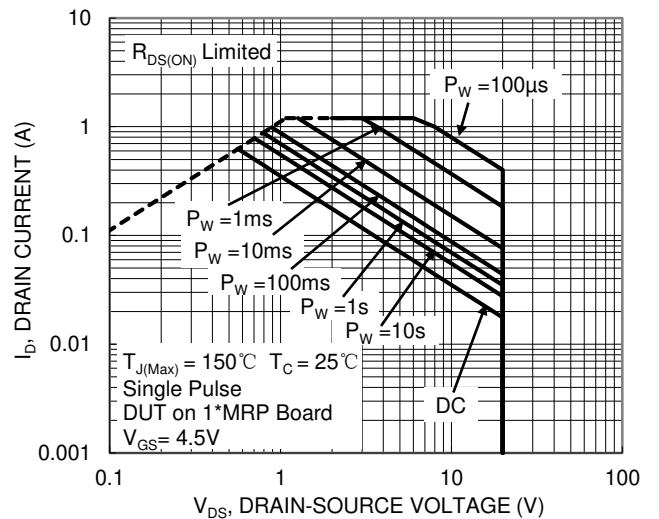


Figure 12. SOA, Safe Operation Area

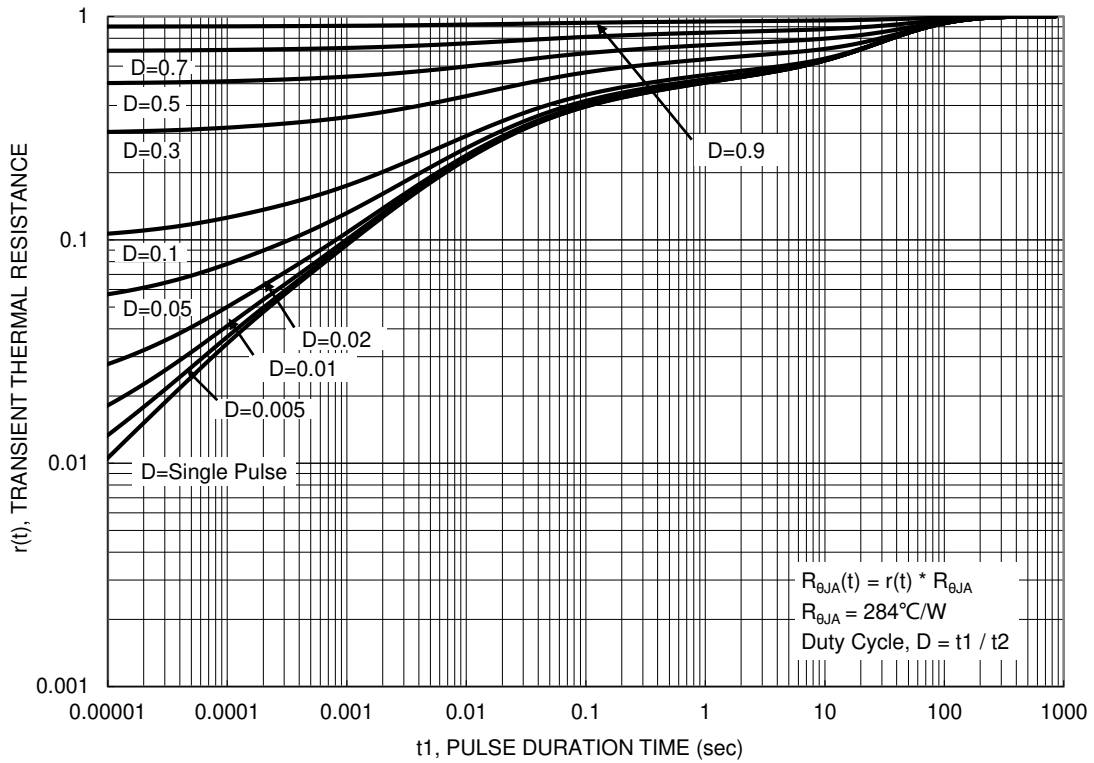
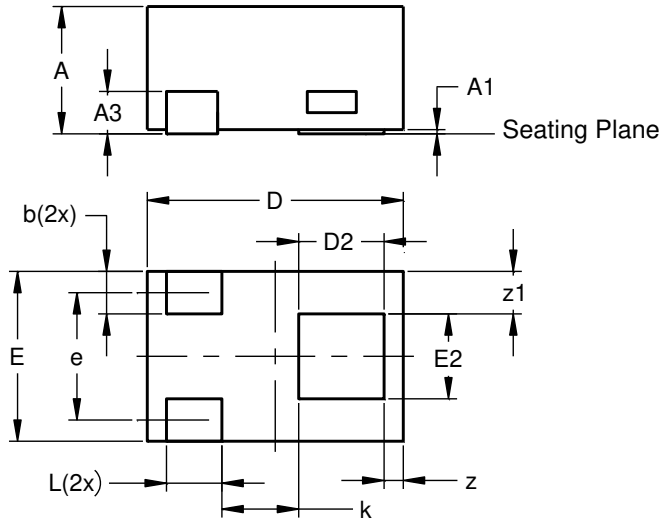


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X2-DFN0604-3**

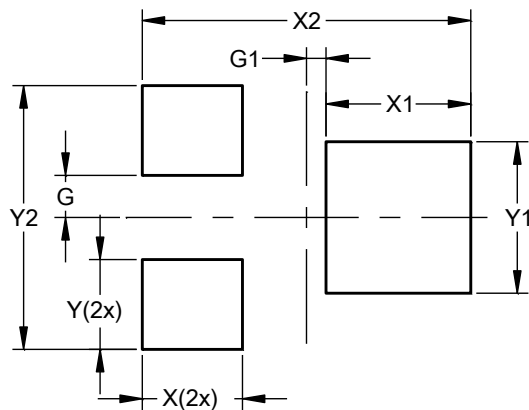


X2-DFN0604-3			
Dim	Min	Max	Typ
A	--	0.40	0.36
A1	0.00	0.03	0.02
A3	--	--	0.10
b	0.07	0.15	0.10
D	0.55	0.65	0.60
D2	0.15	0.25	0.20
E	0.35	0.45	0.40
E2	0.15	0.25	0.20
e	--	--	0.30
k	0.15	--	--
L	0.10	0.18	0.13
z	--	--	0.045
z1	--	--	0.10
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X2-DFN0604-3**



Dimensions	Value (in mm)
G	0.075
G1	0.035
X	0.180
X1	0.260
X2	0.590
Y	0.160
Y1	0.270
Y2	0.470

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