



**DMN6075S** 

### **60V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
60V	$85m\Omega @ V_{GS} = 10V$	2.5A
	120mΩ @ V <sub>GS</sub> = 4.5V	2.0A

### **Description**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

- DC-DC Converters
- Power Management Functions
- Backlighting

## **Features and Benefits**

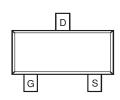
- N MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

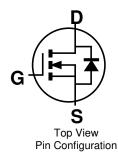
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound;
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)







Top View



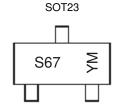
## Ordering Information (Note 4)

Product	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DMN6075S-7	7	8	3,000
DMN6075S-13	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



S67 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	201	4	~	2017	2018	2019	2020	202	1 20	22   2	2023	2024	2025
Code	В		~	E	F	G	Н	ı	,	J	K	L	М
Month	ı	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	)	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	60	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	2.0 1.5	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	I <sub>D</sub>	2.5 2.0	А		
Maximum Body Diode Forward Current (Note 5)		I <sub>S</sub>	2.0	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%	6)	I <sub>DM</sub>	12	Α	

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	D	0.8	W	
Total Fower Dissipation (Note 5)	$T_A = +70^{\circ}C$	$P_D$	0.5		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	157	°C/W	
Total Pawer Discipation (Note C)	T <sub>A</sub> = +25°C	В	1.15	W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	$P_D$	0.7		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	110	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)	OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	1	_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	1	1	1.0	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	I <sub>GSS</sub>	1	1	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	$V_{GS(TH)}$	1		3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$		
Static Drain-Source On-Resistance	D		69	85	mΩ	$V_{GS} = 10V, I_D = 3.2A$		
Static Diani-Source On-nesistance	R <sub>DS(ON)</sub>		75	120	MΩ	$V_{GS} = 4.5V, I_D = 2.8A$		
Diode Forward Voltage	$V_{SD}$	1	1	1.2	V	$V_{GS} = 0V, I_{S} = 2.5A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C <sub>iss</sub>	1	606	_	pF	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
Output Capacitance	Coss	1	32.6	_	pF	$V_{DS} = 20V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	Crss	1	24.6	_	pF	1 = 1.0WH12		
Gate Resistance	$R_g$	1	1.5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	1	12.3	_	nC			
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	1	5.6	_	nC	V <sub>DS</sub> = 30V. In = 3A		
Gate-Source Charge	Qgs	1	1.7	_	nC	$V_{DS} = 30V, I_{D} = 3A$		
Gate-Drain Charge	$Q_{gd}$		1.9	_	nC			
Turn-On Delay Time	t <sub>D(ON)</sub>	1	3.5	_	ns			
Turn-On Rise Time	t <sub>R</sub>	1	4.1	_	ns	$V_{GS} = 10V, V_{DS} = 30V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	1	35	_	ns	$R_g = 20\Omega, R_L = 50\Omega$		
Turn-Off Fall Time	t <sub>F</sub>	_	11	_	ns			

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

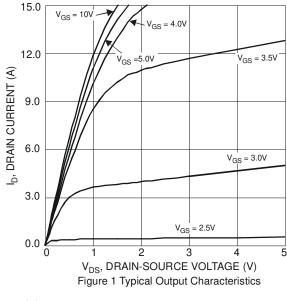
<sup>6.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.

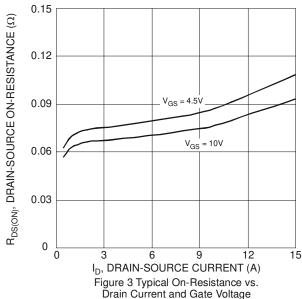
7. Short duration pulse test used to minimize self-heating effect.

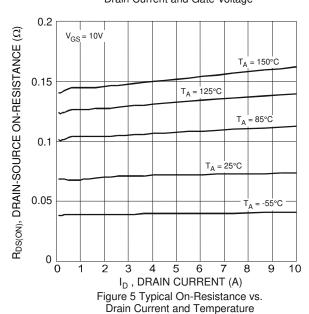
<sup>8.</sup> Guaranteed by design. Not subject to product testing.

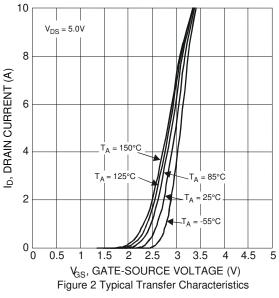


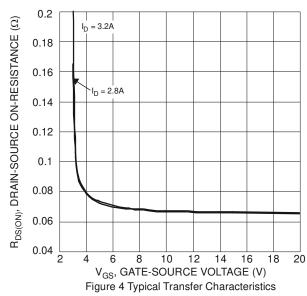












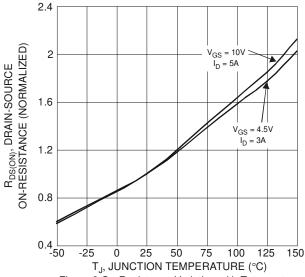
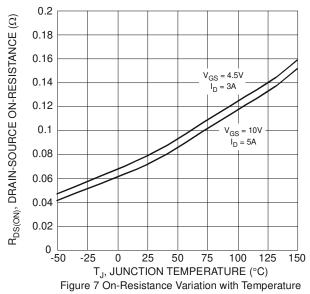
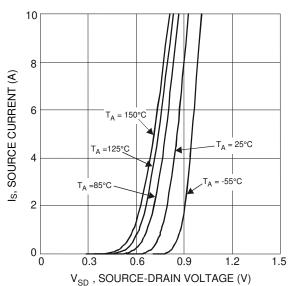


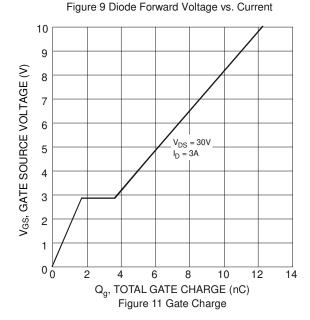
Figure 6 On-Resistance Variation with Temperature

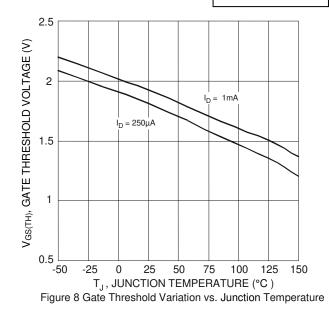


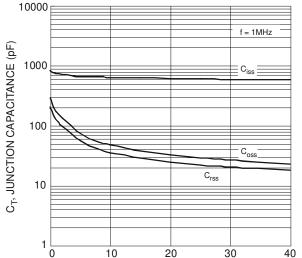




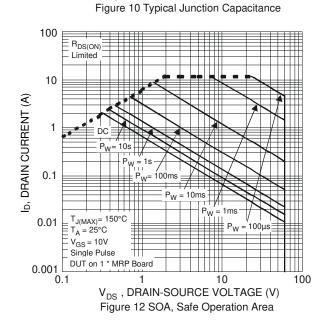








V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V)





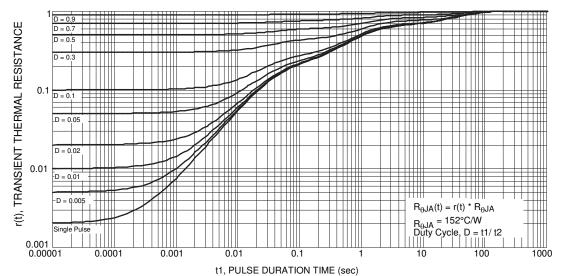
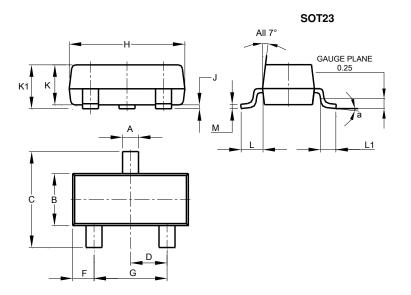


Figure 13 Transient Thermal Resistance



## **Package Outline Dimensions**

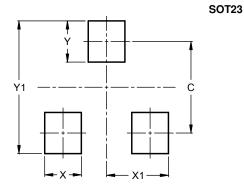
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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