TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

# 2SK3068

Chopper Regulator DC–DC Converter, and Motor Drive Applications

- Low drain-source ON-resistance : R<sub>DS (ON)</sub> = 0.4 Ω (typ.)
- High forward transfer admittance : |Y<sub>fs</sub>| = 9.0 S (typ.)
  - Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode : V<sub>th</sub> = 2.0 to 4.0 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	500	$\langle v \rangle$
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	500	V
Gate-source voltage		V <sub>GSS</sub>	±30	$\checkmark$
Drain current	DC (Note 1)	I <sub>D</sub>	12	∼ А
	Pulse (Note 1)	I <sub>DP</sub>	48	А
Drain power dissipation	n (Tc = 25°C)	PD	100	W
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	364	mJ
Avalanche current		IAR	)) 12	A
Repetitive avalanche e	nergy (Note 3)	EAR	10	mJ
Channel temperature		(T <sub>ch</sub> )	150	°C
Storage temperature ra	ange	Tstg	-55 to 150	~c

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### Thermal Characteristics

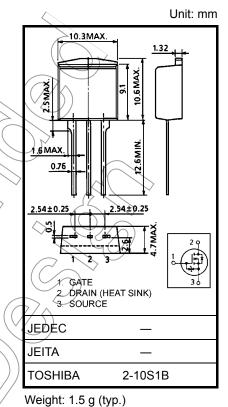
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch−c)	1.25	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

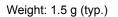
Note 2: 
$$V_{DD}$$
 = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.3 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.



1. GATE 2. DRAIN (HEAT SINK) 3. SOURCE JEDEC — JEITA — TOSHIBA 2-10S2B



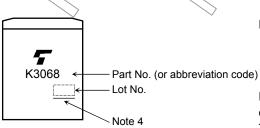
**Electrical Characteristics (Ta = 25°C)** 

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_	—	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	—	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	X	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	-	_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	-7(	4.0	V
Drain-source O	N-resistance	R <sub>DS (ON)</sub>	$V_{GS}$ = 10 V, I <sub>D</sub> = 6 A		0.4	0.52	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	4.0	9.0		S
Input capacitance	e	C <sub>iss</sub>			2040	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		200	_	pF
Output capacitance		Coss			640		
Switching time	Rise time	tr	$v_{\rm GS}^{10V}_{0V}$	- (	22	$\geq$	- ns
	Turn-on time	t <sub>on</sub>		M C	58	) _	
	Fall time	t <sub>f</sub>	= 3322	$\langle n \rangle$	> ) 36	_	115
	Turn-off time	t <sub>off</sub>	$Duty \leq 1\%, t_{W} = 10 \mu s$	) -	180	_	
Total gate charg plus gate−drain)		Qg		_	45	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400$ V, $V_{GS} = 10$ V, $I_D = 10$ A	_	25	_	nC
Gate-drain ("mil	ller") charge	Q <sub>gd</sub>		_	20	—	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			_	-	12	А
Pulse drain reverse current (Note 1)		-	_		48	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V		1200	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 Å / μs	_	16	_	μC

#### Marking



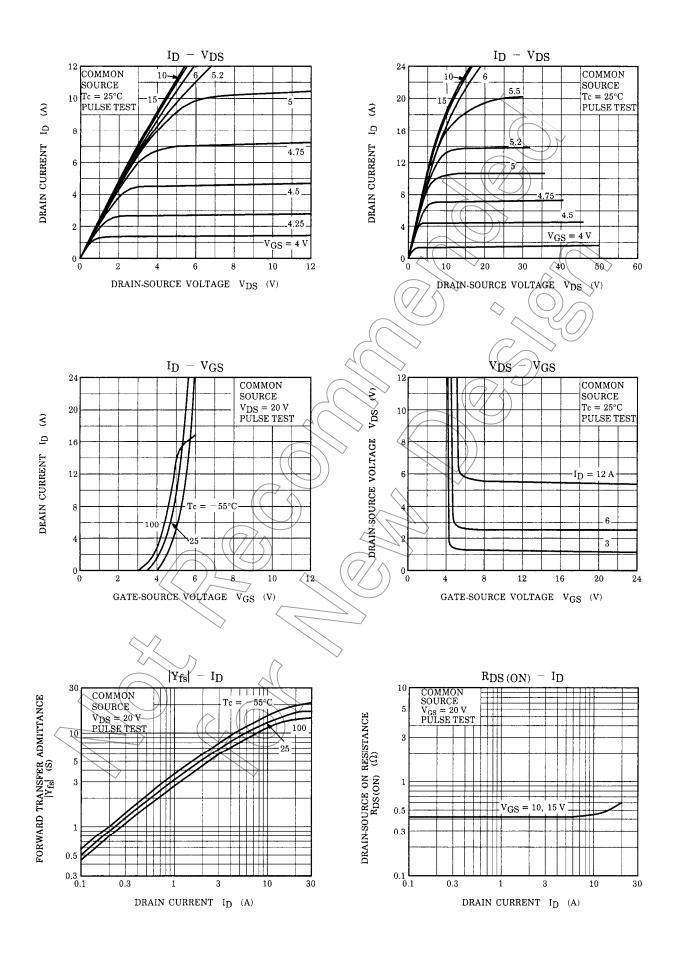
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

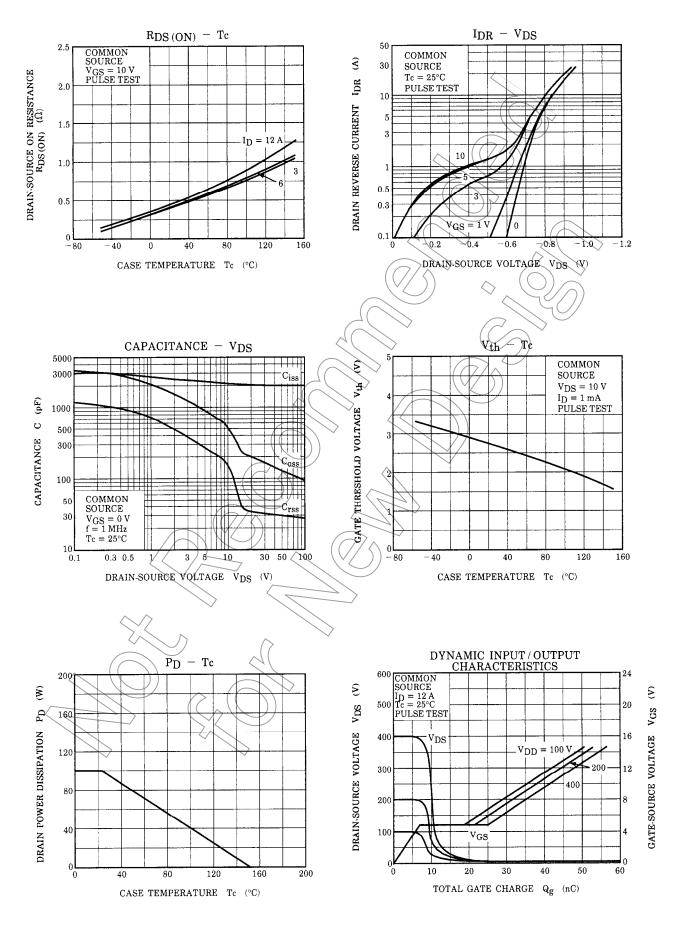
Please contact your TOSHIBA sales representative for details as to

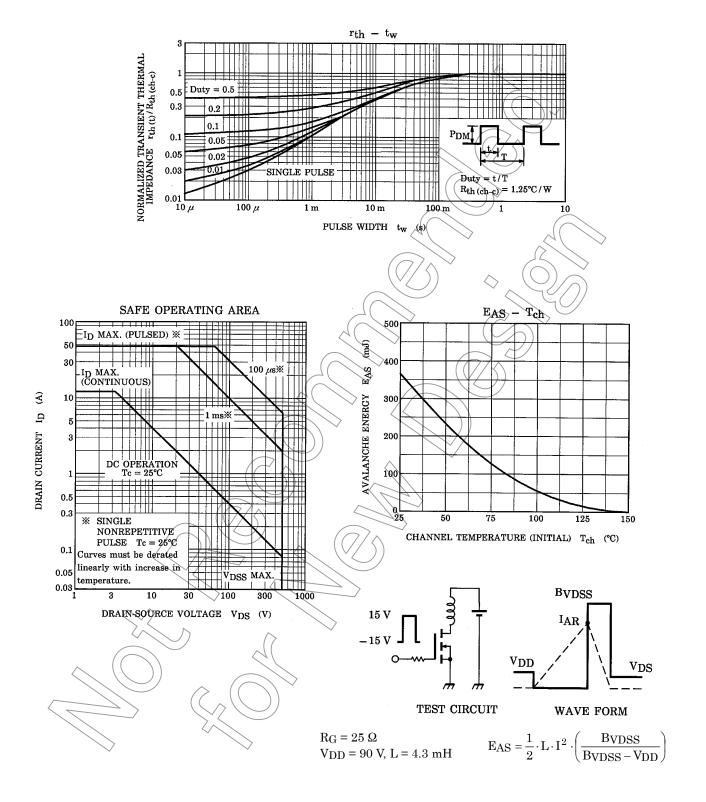
environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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