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BUK7507-30B



N-channel TrenchMOS standard level FET Rev. 02 — 22 February 2011

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

1.3 Applications

- 12 V loads
- Automotive systems

- General purpose power switching
- Motors, lamps and solenoids

1.4 Quick reference data

Quick reference data Table 1.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}$		-	-	30	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; see <u>Figure 1</u> ; see <u>Figure 3</u>	[1]	-	-	75	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see Figure 2		-	-	157	W
Static cha	racteristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 11}}{\text{Figure 12}};$ see Figure 12		-	5.9	7	mΩ
Avalanche	ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$I_D = 75 \text{ A}; V_{sup} \le 30 \text{ V};$ $R_{GS} = 50 \Omega; V_{GS} = 10 \text{ V};$ $T_{j(init)} = 25 ^{\circ}C; unclamped$		-	-	329	mJ
Dynamic o	characteristics						
Q _{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $V_{DS} = 24 \text{ V}; T_j = 25 \text{ °C};$ see Figure 13		-	12	-	nC

^[1] Continuous current is limited by package.



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain	mb	D
3	S	source		
mb	D	mounting base; connected to drain	1 2 3	mbb076 S
			SOT78A (TO-220AB)	

3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BUK7507-30B	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

		,				
Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	30	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$		-	30	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	T_{mb} = 100 °C; V_{GS} = 10 V; see <u>Figure 1</u>	<u>[1]</u>	-	75	Α
		$T_{mb} = 25 ^{\circ}C; V_{GS} = 10 V; \text{see } \frac{\text{Figure 1}}{};$	[2]	-	108	Α
		see <u>Figure 3</u>	[1]	-	75	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \mu s$; see <u>Figure 3</u>		-	435	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	157	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drai	n diode					
Is	source current	T _{mb} = 25 °C	[1]	-	75	Α
			[2]	-	108	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	435	Α
Avalanche r	ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 75 A; $V_{sup} \le 30$ V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped		-	329	mJ

- [1] Continuous current is limited by package.
- [2] Current is limited by power dissipation chip rating.

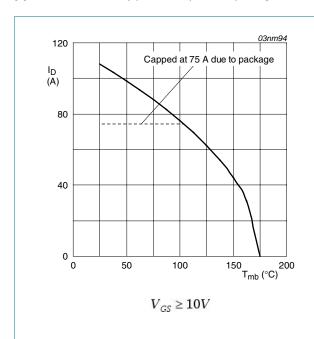


Fig 1. Normalized continuous drain current as a function of mounting base temperature

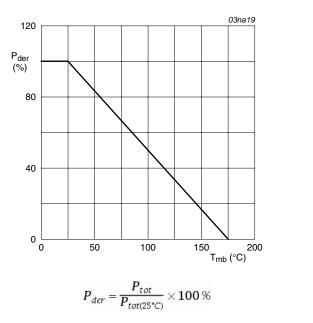
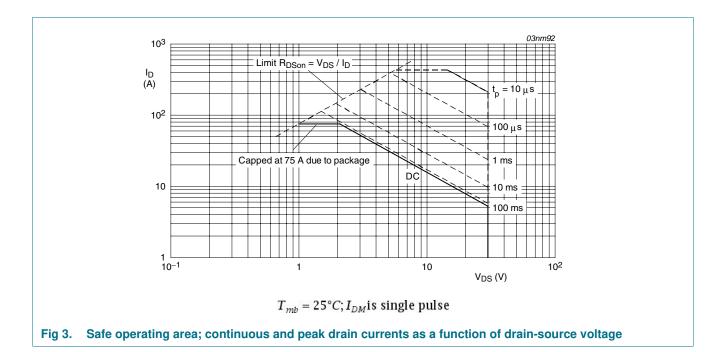


Fig 2. Normalized total power dissipation as a function of mounting base temperature



5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j\text{-}mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	-	0.95	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in still air	-	-	60	K/W

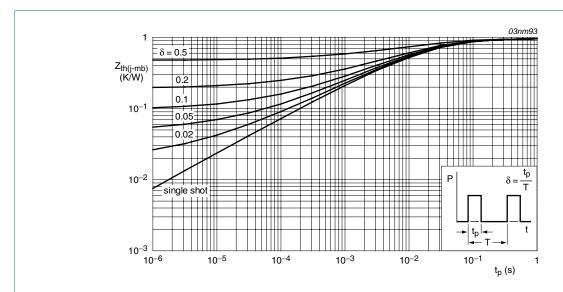


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Characteristics

Table 6. Characteristics

Symbol	Characteristics	Conditions	Min	Tvn	Mov	lini+
Symbol Statio abo	Parameter	Conditions	WIIN	Тур	Max	Unit
	racteristics	1 0.05 A. V. 0.V. T 0.5 °C	20			\/
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	30	-	-	V
	<u>-</u>	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	27	-	-	V
$V_{GS(th)}$		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 175 \text{ °C}$; see Figure 10	1	-	-	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ see <u>Figure 10</u>	2	3	4	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see Figure 10	-	-	4.4	V
I _{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μΑ
		$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175 °C;$ see Figure 11; see Figure 12	-	-	13.3	mΩ
		V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C; see Figure 11; see Figure 12	-	5.9	7	mΩ
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 24 \text{ V}; V_{GS} = 10 \text{ V};$	-	36	-	nC
Q _{GS}	gate-source charge	$T_j = 25$ °C; see <u>Figure 13</u>	-	9	-	nC
Q _{GD}	gate-drain charge		-	12	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 \text{ V; } V_{DS} = 25 \text{ V; } f = 1 \text{ MHz;}$ $T_j = 25 \text{ °C; see } \frac{\text{Figure 14}}{\text{Figure 14}}$	-	1820	2427	рF
C _{oss}	output capacitance		-	632	758	рF
C _{rss}	reverse transfer capacitance		-	256	351	рF
t _{d(on)}	turn-on delay time	$V_{DS} = 25 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	20	-	ns
t _r	rise time	$R_{G(ext)} = 10 \Omega; T_j = 25 \degree C$	-	51	-	ns
t _{d(off)}	turn-off delay time		-	51	-	ns
t _f	fall time		-	44	-	ns
L _D	internal drain inductance	from drain lead 6 mm from package to centre of die; $T_j = 25$ °C	-	4.5	-	nΗ
		from contact screw on mounting base to centre of die ; $T_j = 25$ °C	-	3.5	-	nΗ
L _S	internal source inductance	from source lead 6 mm from package to source bond pad ; $T_j = 25 ^{\circ}\text{C}$	-	7.5	-	nΗ
Source-di	rain diode					
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 15	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s};$	-	46	-	ns
Qr	recovered charge	$V_{GS} = -10 \text{ V}; V_{DS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	28	-	nC

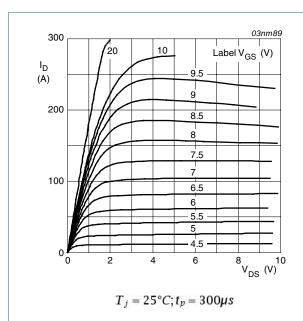


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

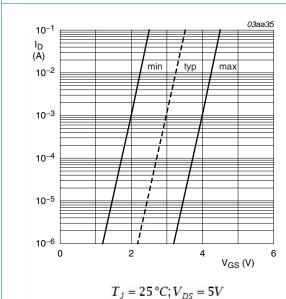


Fig 7. Sub-threshold drain current as a function of gate-source voltage

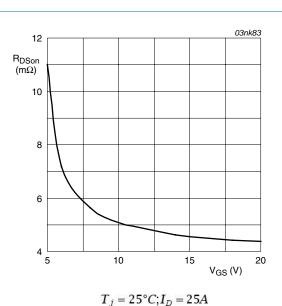


Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

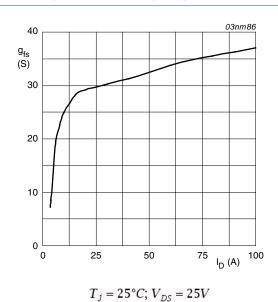


Fig 8. Forward transconductance as a function of drain current; typical values

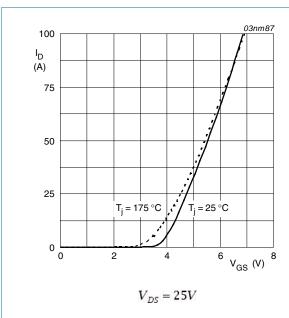


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

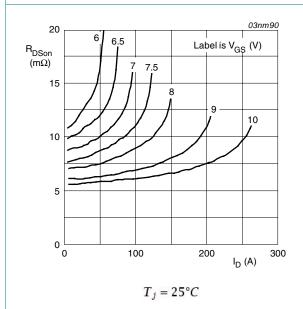


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

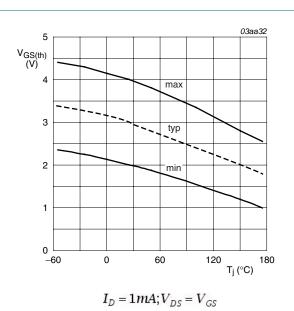


Fig 10. Gate-source threshold voltage as a function of junction temperature

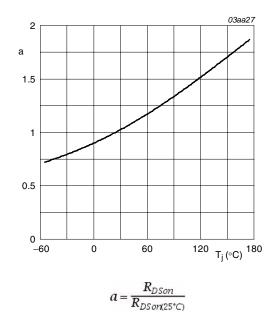


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature

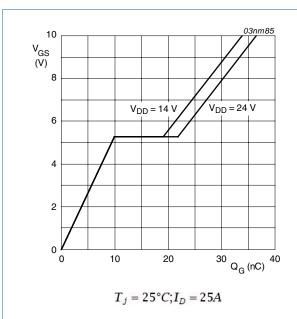
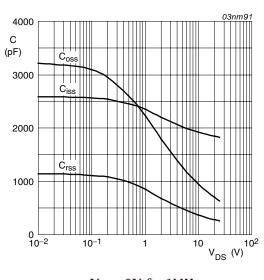


Fig 13. Gate-source voltage as a function of gate charge; typical values



 $V_{GS} = 0V; f = 1MHz$

Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

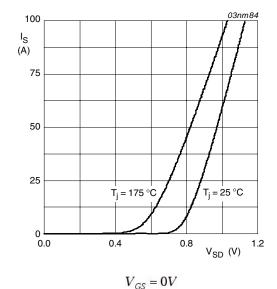
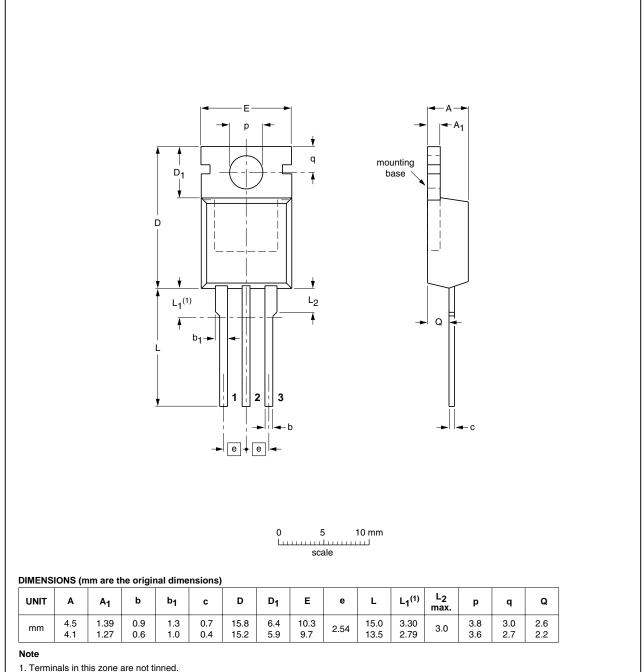


Fig 15. Source current as a function of source-drain voltage; typical values

Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78A



VERSION IEC JEDEC JEITA PROJECTION		. ISSUE DA	EUROPEAN	RENCES	KEFEK		OUTLINE
201704 2 lead TO 2004D 50 40	- DAIE	1 ISSUE DA	PROJECTION	JEITA	JEDEC	IEC	VERSION
S0176A 3-lead 10-220AB SC-46 05-		- 03-01-22 05-03-14		SC-46	3-lead TO-220AB		SOT78A

Fig 16. Package outline SOT78A (TO-220AB)

8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK7507-30B v.2	20110222	Product data sheet	-	BUK75_7607_30B v.1
Modifications:	 The format of this of NXP Semicondu 	data sheet has been rede uctors.	signed to comply with the	e new identity guidelines
	 Legal texts have b 	een adapted to the new o	ompany name where app	oropriate.
	 Type number BUK 	(7507-30B separated fron	n data sheet BUK75_760	7_30B v.1.
BUK75_7607_30B v.1	20030407	Product data	-	-

9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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N-channel TrenchMOS standard level FET

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