



## Surge arrester

### 3-electrode arrester

**Series/Type:** TG20-C420SMD6  
**Ordering code:** B88069X5433T203  
**Date:** 2019-09-03  
**Version:** 02

**Features**

- Very small size
- Very fast response time
- Stable performance over life
- Very low capacitance
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

**Applications**

- DSL line card
- Telecommunication
- Applications with limited space

**Electrical specifications**

DC spark-over voltage <sup>1) 2) 3)</sup>	420	V
Tolerance	-15 ... +33	%
Min.	360	V
Max.	560	V
Impulse spark-over voltage <sup>3)</sup>		
at 100 V/μs - for 99% of measured values	< 850	V
- typical values of distribution	< 750	V
at 1 kV/μs - for 99% of measured values	< 1000	V
- typical values of distribution	< 920	V
Service life		
10 operations	50 Hz, 1 s <sup>4)</sup>	2
10 operations [5x (+) & 5x (-)]	8/20 μs <sup>4)</sup>	2
10 operations [5x (+) & 5x (-)]	5/320 μs <sup>5) 6)</sup>	150
Insulation resistance at 100 V <sub>DC</sub> <sup>3)</sup>	> 1	GΩ
Capacitance at 1 MHz <sup>3)</sup>	< 1.0	pF
Arc voltage at 1 A	~ 10	V
Glow to arc transition current	< 0.5	A
Glow voltage at 0.1 A	~ 60	V
Weight	~ 0.3	g
Operation and storage temperature	-40 ... +125	°C
Climatic category (IEC 60068-1)	40/125/21	
Marking	without	

<sup>1)</sup> At delivery AQL 0.65 level II, DIN ISO 2859

<sup>2)</sup> In ionized mode

<sup>3)</sup> Tip or ring electrode to center electrodes

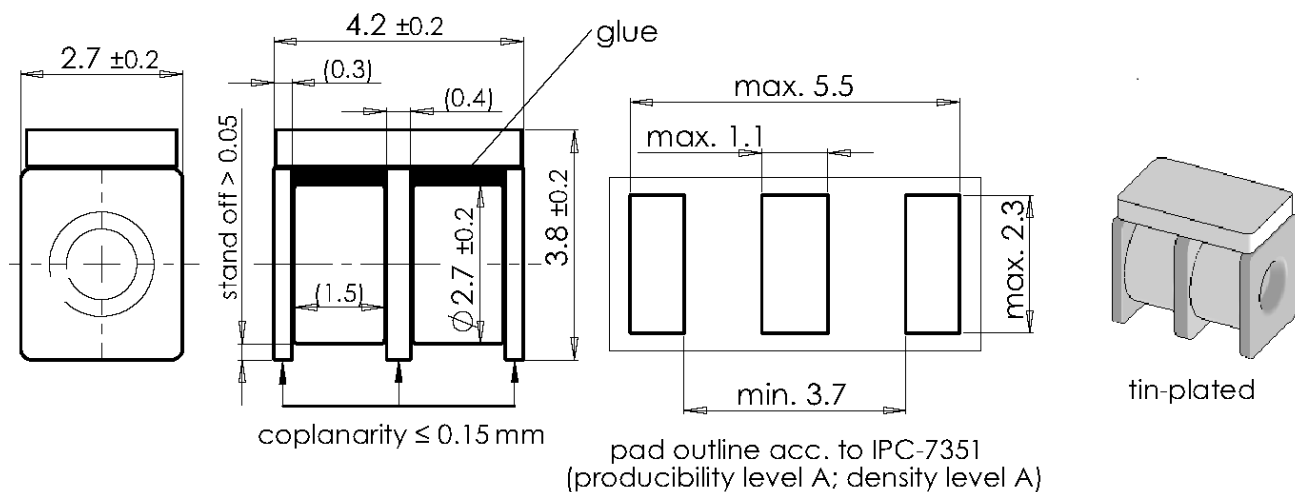
<sup>4)</sup> Total current through center electrodes, half value through tip respectively ring electrode.

<sup>5)</sup> Tip to center electrode additional ring to center electrode

<sup>6)</sup> Test generator 6 kV, 10/700 μs, 40 Ω

Terms in accordance with ITU-T Rec. K.12 and IEC 61643-311.

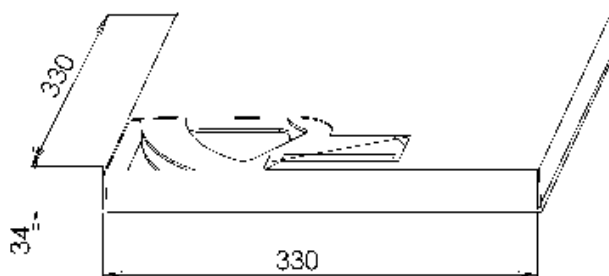
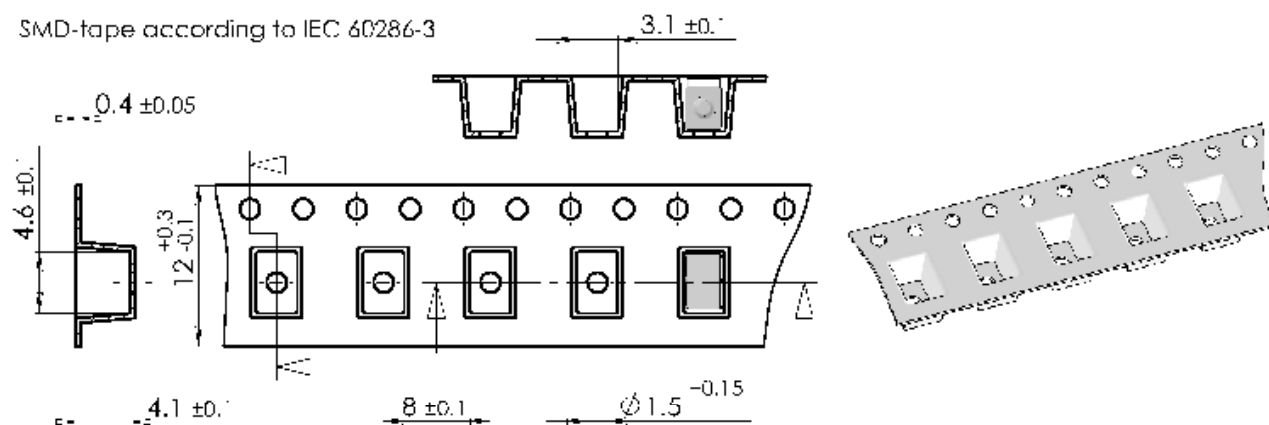
Dimensional drawing in mm



Ordering code and packing advice

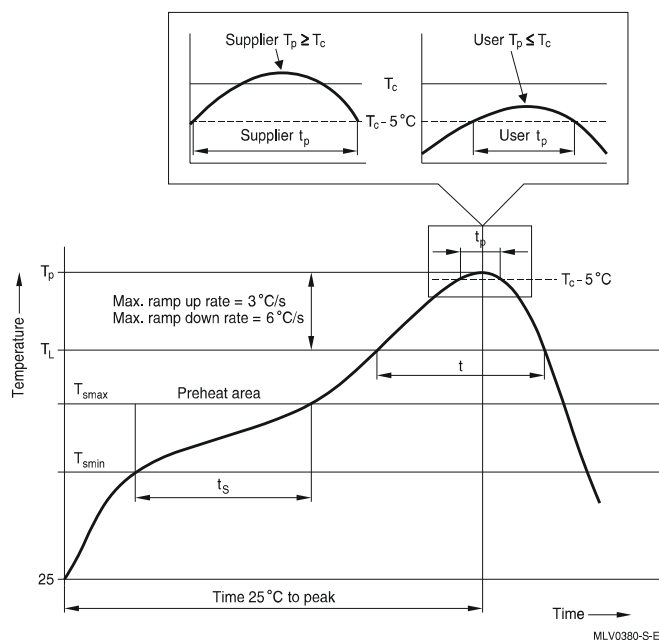
B88069X5433T203 = SMD-tape with 2000 pcs.

SMD-tape according to IEC 60286-3



## Soldering parameter

### Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	$T_{smin}$ $T_{smax}$ $t_{smin}$ to $t_{smax}$	100 °C 150 °C 60 ... 120 s	150 °C 200 °C 60 ... 180 s
Average ramp-up rate	$T_{smax}$ to $T_p$	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	$T_L$ $t_L$	183 °C 60 ... 150 s	217 °C 60 ... 150 s
Peak package body temperature *, Classification temperature **	$T_p$ , $T_c$	220 ... 235 °C **	245 ... 260 °C **
Time ( $t_p$ ) ** within 5 °C of the specified classification temperature ( $T_c$ )		20 s ***	30 s ***
Average ramp-down rate	$T_p$ to $T_{smax}$	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min
* = Tolerance for peak profile temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum. ** = For details please refer to JEDEC J-STD-020D. *** = Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.			

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

## Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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## Important notes

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