

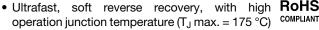
Insulated Ultrafast Rectifier Module, 210 A



PRODUCT SUMMARY					
V_{R}	400 V				
I _{F(AV)} per module at T _C = 133 °C	210 A				
t _{rr}	40 ns				
Туре	Modules - Diode FRED Pt®				
Package	SOT-227				

FEATURES

- Two fully independent diodes
- · Fully insulated package





Low forward voltage drop

- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-UFB210FA40P insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V_R		400	V	
Continuous forward current per diode	I _F ⁽¹⁾	T _C = 90 °C	210	Δ	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	1300	А	
Maximum power dissipation per module	P _D	T _C = 90 °C	531	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

Note

⁽¹⁾ Maximum continuous forward current must be limited to 100 A to do not exceed the maximum temperature of power terminals.

ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	400	-	-	
Forward voltage	V _{FM}	I _F = 100 A	-	1.06	1.24	V
		I _F = 100 A, T _J = 175 °C	-	0.85	0.95	
Reverse leakage current	I _{RM}	$V_R = V_R$ rated	-	1.3	50	μΑ
		T _J = 175 °C, V _R = V _R rated	-	0.36	4	mA
Junction capacitance	C _T	V _R = 400 V	-	100	-	pF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	40	-	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 150 A dI _F /dt = 200 A/μs V _R = 200 V	-	93	-	ns
		T _J = 125 °C		-	172	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	10.5	-	Α
		T _J = 125 °C		-	20.2	-	7
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	490	-	nC
		T _J = 125 °C		-	1740	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	В		-	-	0.32	
Junction to case, both leg conducting	R _{thJC}		-	-	0.16	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque			-	-	1.3	Nm
Case style				SOT	-227	

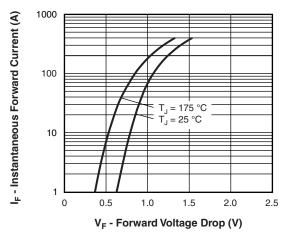


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

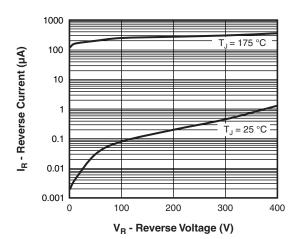


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

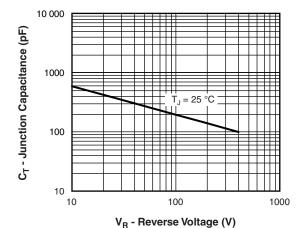


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

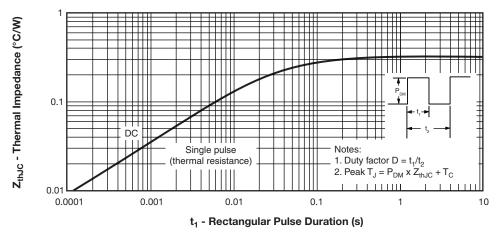
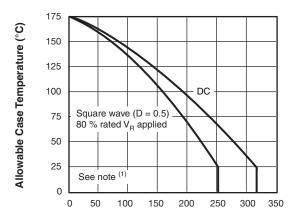


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



I_{F(AV)} - **Average Forward Current (A)**Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current (Per Leg)

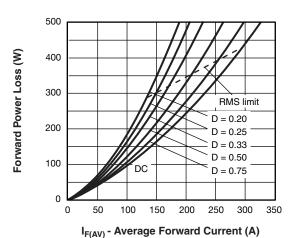


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

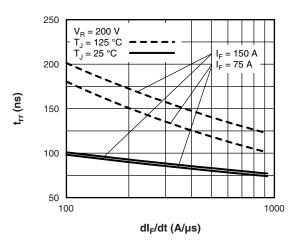


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

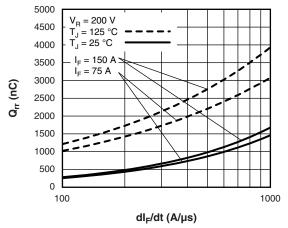


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

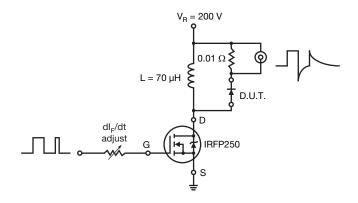
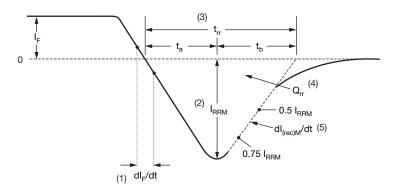


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

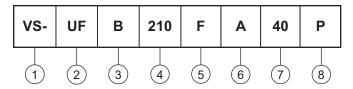
(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Ultrafast rectifier

3 - Ultrafast Pt diffused

- Current rating (210 = 210 A)

5 - Circuit configuration (2 separate diodes, parallel pin-out)

6 - Package indicator (SOT-227 standard insulated base)

7 - Voltage rating (40 = 400 V)

None = Standard production

• P = Lead (Pb)-free

Quantity per tube is 10, M4 screw and washer included

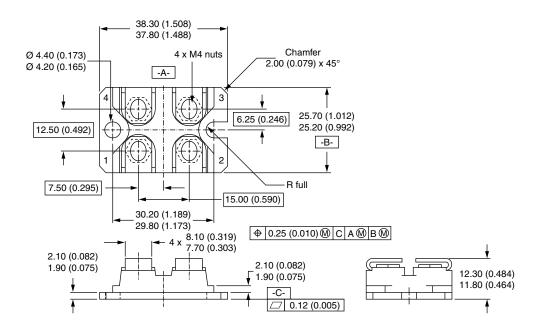
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
2 separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 3 1 1 0 0 2 1			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95036</u>				
Packaging information	www.vishay.com/doc?95425			



SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- Controlling dimension: millimeter

Document Number: 95036 Revision: 28-Aug-07



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