

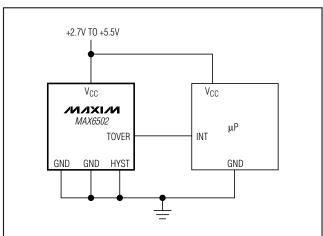
General Description

The MAX6501-MAX6504 low-cost, fully integrated temperature switches assert a logic signal when their die temperature crosses a factory-programmed threshold. Operating from a +2.7V to +5.5V supply, these devices feature two on-chip, temperature-dependent voltage references and a comparator. They are available with factory-trimmed temperature trip thresholds from -45°C to +125°C in 10°C increments, and are accurate to ±0.5°C (typ) or ±6°C (max). These devices require no external components and typically consume 30µA supply current. Hysteresis is pin-selectable at 2°C or 10°C.

The MAX6501/MAX6503 have an active-low, open-drain output intended to interface with a microprocessor (µP) reset input. The MAX6502/MAX6504 have an activehigh, push-pull output intended to directly drive fancontrol logic. The MAX6501/MAX6502 are offered with hot-temperature thresholds (+35°C to +125°C), asserting when the temperature is above the threshold. The MAX6503/MAX6504 are offered with cold-temperature thresholds (-45°C to +15°C), asserting when the temperature is below the threshold.

The MAX6501-MAX6504 are offered in eight standard temperature versions; contact the factory for pricing and availability of nonstandard temperature versions. They are available in a 5-pin SOT23 package.

Typical Operating Circuit



Selector Guide and Pin Configurations appear at end of data sheet.

Features

- ♦ ±0.5°C (typical) Threshold Accuracy Over **Full Temperature Range**
- **♦ No External Components Required**
- **♦ Low Cost**
- ♦ 30µA Supply Current
- **♦** Factory-Programmed Thresholds from -45°C to +125°C in 10°C Increments
- ♦ Open-Drain Output (MAX6501/MAX6503) Push-Pull Output (MAX6502/MAX6504)
- ♦ Pin-Selectable 2°C or 10°C Hysteresis
- ♦ SOT23-5 Package

Ordering Information

| PART | TEMP RANGE | PIN-PACKAGE |
|---------------------|-----------------|-------------|
| MAX6501UK+T | -55°C to +125°C | 5 SOT23 |
| MAX6502 UK+T | -55°C to +125°C | 5 SOT23 |
| MAX6503UK+T | -55°C to +125°C | 5 SOT23 |
| MAX6504UK+T | -55°C to +125°C | 5 SOT23 |

Note: These parts are offered in eight standard temperature versions with a minimum order of 2,500 pieces. To complete the suffix information, add P or N for positive or negative trip temperature, and select an available trip point in degrees centigrade. For example, the MAX6501UKP065+T describes a MAX6501 in a SOT23 package with a +65°C threshold. Contact the factory for pricing and availability of nonstandard temperature versions (minimum order 10,000 pieces). +Denotes a lead(Pb)-free/RoHS-compliant package. T = Tape and reel.

Applications

μP Temperature Monitoring in High-Speed Computers

Temperature Control

Temperature Alarms

Fan Control

ABSOLUTE MAXIMUM RATINGS

| Supply Voltage (V _{CC}) Range0.3V to +7V | Output Current (all pins)20mA |
|----------------------------------------------------|-------------------------------------------|
| TOVER (MAX6501)0.3V to +7V | Continuous Power Dissipation (TA = +70°C) |
| TOVER (MAX6502)0.3V to (V _{CC} + 0.3V) | SOT23 (derate 3.1mW/°C above +70°C)247mW |
| TUNDER (MAX6503)0.3V to +7V | Operating Temperature Range55°C to +135°C |
| TUNDER (MAX6504)0.3V to (VCC + 0.3V) | Storage Temperature Range65°C to +165°C |
| All Other Pins0.3V to (V _{CC} + 0.3V) | Lead Temperature (soldering, 10s)+300°C |
| Input Current (all pins)20mA | Soldering Temperature (reflow)+260°C |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +2.7 \text{V to } +5.5 \text{V}, \text{R}_{PULLUP} = 100 \text{k}\Omega \text{ (MAX6501/MAX6503 only)}, T_{A} = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_{A} = +25 ^{\circ}\text{C.}) \text{ (Note 1)}$

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
|--------------------------------------|-----------------|------------------------------------------------------------------------------------------------------|------------------------|------|-----------------------|---------|
| Supply Voltage Range | Vcc | | 2.7 | | 5.5 | V |
| Supply Current | Icc | | | 30 | 85 | μΑ |
| | | -45°C to -25°C | -6 | ±0.5 | +6 | |
| Temperature Threshold | A.T., . | -15°C to +15°C | -4 | ±0.5 | +4 | · · · · |
| Accuracy (Note 2) | ΔT_{TH} | +35°C to +65°C | -4 | ±0.5 | +4 | °C |
| | | +75°C to +125°C | -6 | ±0.5 | +6 | |
| Temperature Threshold | T | HYST = GND | | 2 | | °C |
| Hysteresis | THYST | HYST = V _{CC} | | 10 | | |
| HYST Input Threshold | VIH | | 0.8 x V _{CC} | | | V |
| (Note 3) | VIL | | | (|).2 x V _{CC} | V |
| Outout Voltage High | \/-·· | ISOURCE = 500μA, V _{CC} > 2.7V (MAX6502/MAX6504 only) | 0.8 x V _{CC} | | | V |
| Output Voltage High | VOH | ISOURCE = 800μA, V _{CC} > 4.5V (MAX6502/MAX6504 only) | V _C C - 1.5 | | | |
| Outrout Valtaga Laur | Va | I _{SINK} = 1.2mA, V _{CC} > 2.7V | | | 0.3 | \/ |
| Output Voltage Low | VoL | ISINK = 3.2mA, VCC > 4.5V | | 0.4 | | V |
| Open-Drain Output Leakage Current | | V _{CC} = 2.7V, V _{TUNDER} = 5.5V (MAX6503), V _{TOVER} = 5.5V (MAX6501) | | 10 | | nA |

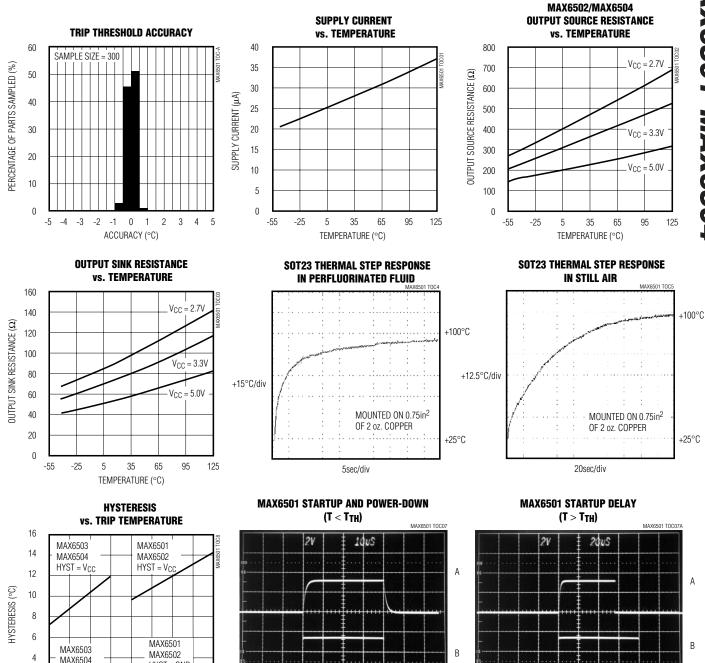
Note 1: 100% production tested at $T_A = +25^{\circ}C$. Specifications over temperature limits are guaranteed by design.

Note 2: The MAX6501–MAX6504 are available with internal, factory-programmed temperature trip thresholds from -45°C to +125°C in +10°C increments (see *Selector Guide*).

Note 3: Guaranteed by design.

Typical Operating Characteristics

 $(V_{CC} = +5V, R_{PULLUP} = 100k\Omega (MAX6501/MAX6503), T_A = +25^{\circ}C, unless otherwise noted.)$



TRACE A: $\overline{\text{TOVER}}$ VOLTAGE, $R_{PULLUP} = 100 \text{k}\Omega$

TRACE B: VCC PULSE DRIVEN FROM 3.3V CMOS LOGIC OUTPUT

2 0 HYST = GND

-5 15 HYST = GND

115

35

TRIP TEMPERATURE (°C)

55 75 95 TRACE A: $\overline{\text{TOVER}}$ VOLTAGE, $R_{PULLUP} = 100 \text{k}\Omega$

TRACE B: VCC PULSE DRIVEN FROM 3.3V CMOS LOGIC OUTPUT

Pin Description

| | PIN | | | NAME | FUNCTION | | |
|---------|---------|---------|---------|--------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|
| MAX6501 | MAX6502 | MAX6503 | MAX6504 | NAME | FUNCTION | | |
| 1, 2 | 1, 2 | 1, 2 | 1, 2 | GND | Ground. Not internally connected. Connect both ground pins together close to the chip. Pin 2 provides the lowest thermal resistance to the die. | | |
| 3 | 3 | 3 | 3 | HYST | Hysteresis Input. Connect HYST to GND for 2°C hysteresis, or connect to V _{CC} for 10°C hysteresis. | | |
| 4 | 4 | 4 | 4 | Vcc | Supply Input (+2.7V to +5.5V) | | |
| 5 | _ | _ | _ | TOVER | Open-Drain, Active-Low Output. $\overline{\text{TOVER}}$ goes low when the die temperature exceeds the factory-programmed temperature threshold. Connect to a 100k Ω pullup resistor. May be pulled up to a voltage higher than VCC. | | |
| _ | 5 | _ | _ | TOVER | Push-Pull Active-High Output. TOVER goes high when the die temperature exceeds the factory-programmed temperature threshold. | | |
| _ | _ | 5 | _ | TUNDER | Open-Drain, Active-Low Output. TUNDER goes low when the die temperature goes below the factory-programmed temperature threshold. Connect to a 100k Ω pullup resistor. May be pulled up to a voltage higher than VCC. | | |
| _ | _ | _ | 5 | TUNDER | Push-Pull Active-High Output. TUNDER goes high when the die temperature falls below the factory-programmed temperature threshold. | | |

General Description

The MAX6501–MAX6504 fully integrated temperature switches incorporate two temperature-dependent references and a comparator. One reference exhibits a positive temperature coefficient and the other a negative temperature coefficient (Figure 1). The temperature at which the two reference voltages are equal determines the temperature trip point. Pin-selectable 2°C or 10°C hysteresis keeps the output from oscillating when the die temperature approaches the threshold temperature. The MAX6501/MAX6503 have an active-low, opendrain output structure that can only sink current. The MAX6502/MAX6504 have an active-high, push-pull output structure that can sink or source current. The internal power-on reset circuit guarantees the output is at $T_{TH} = +25^{\circ}C$ state at startup for 50µs.

The MAX6501–MAX6504 are available with factory-preset temperature thresholds from -45°C to +125°C in 10°C increments. Table 1 lists the available temperature threshold ranges. The MAX6501/MAX6503 outputs are intended to interface with a microprocessor (μ P) reset input (Figure 2). The MAX6502/MAX6504 outputs are intended for applications such as driving a fan control (Figure 3).

Table 1. Factory-Programmed Threshold Range

| PART | THRESHOLD (T _{TH}) RANGE |
|---------|------------------------------------|
| MAX6501 | +35°C < T _{TH} < +125°C |
| MAX6502 | +35°C < T _{TH} < +125°C |
| MAX6503 | -45°C < T _{TH} < +15°C |
| MAX6504 | -45°C < T _{TH} < +15°C |

Hysteresis Input

The HYST pin is a CMOS-compatible input that selects hysteresis at either a high level (10° C for HYST = V_{CC}) or a low level (2° C for HYST = GND). Hysteresis prevents the output from oscillating when the temperature approaches the trip point. The HYST pin should not be left unconnected. Drive HYST close to ground or V_{CC} . Other input voltages cause increased supply current. The actual amount of hysteresis depends on the part's programmed trip threshold (see the *Typical Operating Characteristics*).

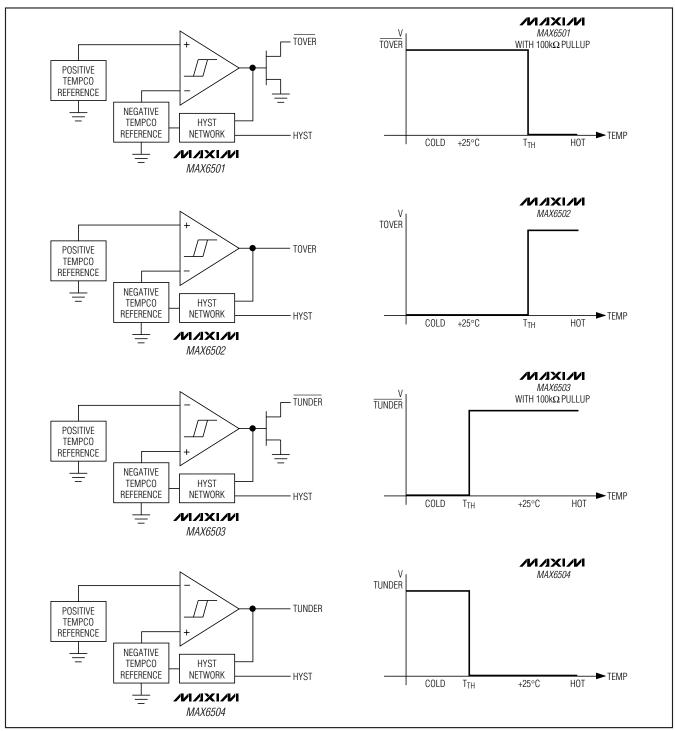


Figure 1. Block and Functional Diagrams

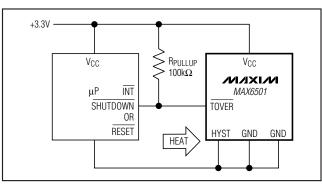


Figure 2. Microprocessor Alarm/Reset

Applications Information

Thermal Considerations

The MAX6501–MAX6504 supply current is typically 30 μ A. When used to drive high-impedance loads, the devices dissipate negligible power. Therefore, the die temperature is essentially the same as the package temperature. The key to accurate temperature monitoring is good thermal contact between the MAX6501–MAX6504 package and the device being monitored. In some applications, the SOT23 package may be small enough to fit underneath a socketed μ P, allowing the device to monitor the μ P's temperature directly. Use the monitor's output to reset the μ P, assert an interrupt, or trigger an external alarm.

Accurate temperature monitoring depends on the thermal resistance between the device being monitored and the MAX6501–MAX6504 die. Heat flows in and out of plastic packages, primarily through the leads. Pin 2 of the SOT23-5 package provides the lowest thermal resistance to the die. Short, wide copper traces leading to the temperature monitor ensure that heat transfers quickly and reliably.

The rise in die temperature due to self-heating is given by the following formula:

$\Delta T_J = PDISSIPATION \times \theta_{JA}$

where PDISSIPATION is the power dissipated by the MAX6501-MAX6504, and θ_{JA} is the package's thermal resistance.

The typical thermal resistance is 140°C/W for the SOT23 package. To limit the effects of self-heating, minimize the output currents. For example, if the MAX6501 or MAX6503 sink 1mA, the output voltage is guaranteed to be less than 0.3V. Therefore, an additional 0.3mW of power is dissipated within the IC. This corresponds to a 0.042°C shift in the die temperature in the SOT23.

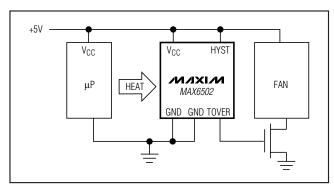


Figure 3. Overtemperature Fan Control

Temperature-Window Alarm

The MAX6501–MAX6504 temperature switch outputs assert when the die temperature is outside the factory-programmed range. Combining the outputs of two devices creates an over/undertemperature alarm. The MAX6501/MAX6503 and the MAX6502/MAX6504 are designed to form two complementary pairs, each containing one cold trip-point output and one hot trip-point output. The assertion of either output alerts the system to an out-of-range temperature. The MAX6502/MAX6504 push/pull output stages can be ORed to produce a thermal out-of-range alarm. More favorably, a MAX6501/MAX6503 can be directly wire-ORed with a single external resistor to accomplish the same task (Figure 4).

The temperature window alarms shown in Figure 4 can be used to accurately determine when a device's temperature falls out of the -5°C to +75°C range. The thermal-overrange signal can be used to assert a thermal shutdown, power-up, recalibration, or other temperature-dependent function.

Low-Cost, Fail-Safe Temperature Monitor

In high-performance/high-reliability applications, multiple temperature monitoring is important. The high-level integration and low cost of the MAX6501–MAX6504 facilitate the use of multiple temperature monitors to increase system reliability. Figure 5's application uses two MAX6502s with different temperature thresholds to ensure that fault conditions that can overheat the monitored device cause no permanent damage. The first temperature monitor activates the fan when the die temperature exceeds +45°C. The second MAX6502 triggers a system shutdown if the die temperature reaches +75°C. The second temperature monitor's output asserts when a wide variety of destructive fault conditions occur, including latchups, short circuits, and cooling-system failures.

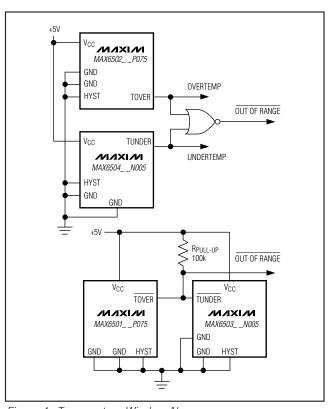


Figure 4. Temperature-Window Alarms

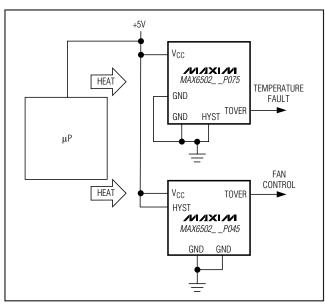


Figure 5. Low-Power, High-Reliability, Fail-Safe Temperature Monitor

Table 2. Device Marking Codes

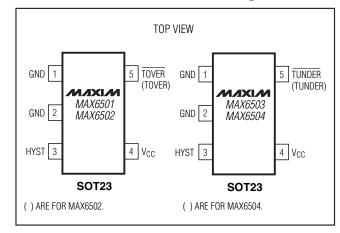
| DEVICE | CODE | MINIMUM ORDER |
|---------------|------|------------------|
| MAX6501UKP035 | ABZF | 10k |
| MAX6501UKP045 | ABZR | 2.5k |
| MAX6501UKP055 | ACFW | 2.5k |
| MAX6501UKP065 | ABZS | 2.5k |
| MAX6501UKP075 | ACFV | 2.5k |
| MAX6501UKP085 | ACDP | 2.5k |
| MAX6501UKP095 | ABZT | 2.5k |
| MAX6501UKP105 | ACFU | 10k |
| MAX6501UKP115 | ACAG | 2.5k |
| MAX6501UKP125 | ADQK | 25k |
| MAX6502UKP035 | ABZG | 10k |
| MAX6502UKP045 | ABZU | 2.5k |
| MAX6502UKP055 | ACGC | 2.5k |
| MAX6502UKP065 | ABZV | 2.5k |
| MAX6502UKP075 | ACGB | 2.5k |
| MAX6502UKP085 | ACGA | 2.5k |
| MAX6502UKP095 | ABZW | 2.5k |

| DEVICE | CODE | MINIMUM ORDER |
|---------------|------|------------------|
| MAX6502UKP105 | ACFZ | 10k |
| MAX6502UKP115 | ACFY | 2.5k |
| MAX6502UKP125 | ADUD | 25k |
| MAX6503UKN045 | ADIZ | 10k |
| MAX6503UKN035 | ADVS | 10k |
| MAX6503UKN025 | ADVR | 10k |
| MAX6503UKN015 | ACFX | 2.5k |
| MAX6503UKN005 | ADNZ | 10k |
| MAX6503UKP005 | ABZX | 2.5k |
| MAX6503UKP015 | ADPN | 10k |
| MAX6504UKN045 | ACAX | 10k |
| MAX6504UKN035 | ADVU | 10k |
| MAX6504UKN025 | ADVT | 10k |
| MAX6504UKN015 | ACGD | 2.5k |
| MAX6504UKN005 | ADVX | 10k |
| MAX6504UKP005 | ABZY | 2.5k |
| MAX6504UKP015 | ADKE | 10k |

Selector Guide

| PART | | MAX6501 | MAX6502 | MAX6503 | MAX6504 |
|--------------------------------------|------|----------------|-------------|----------------|-----------|
| OUTPUT STAGE | | Open- Drain | Push-Pull | Open- Drain | Push-Pull |
| TRIP TEMP THRESHOLD | | Hot | Hot | Cold | Cold |
| | -45 | | | ✓ | ✓ |
| ပ | -35 | | | ✓ | ✓ |
| °) | -25 | | | ✓ | 1 |
| Ë | -15 | | | ✓ | 1 |
| 웃 | -5 | | | ✓ | 1 |
| 3ES | +5 | | | ✓ | ✓ |
| 丰 | +15 | | | ✓ | 1 |
| 뿚 | +35 | ✓ | ✓ | | |
| Τ | +45 | ✓ | > | | |
| ïB∕ | +55 | ✓ | ✓ | | |
| MPE | +65 | ✓ | ✓ | | |
| 臣 | +75 | ✓ | \ | | |
| ВD | +85 | ✓ | \ | | |
| STANDARD TEMPERATURE THRESHOLDS (°C) | +95 | ✓ | √ | | |
| A | +105 | ✓ | √ | | |
| S | +115 | ✓ | √ | | |
| | +125 | ✓ | ✓ | | |

Pin Configurations



Chip Information

SUBSTRATE CONNECTED TO GND

Package Information

For the latest package outline information and land patterns (footprints), go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

| PACKAGE | PACKAGE | OUTLINE | LAND |
|---------|---------|----------------|-------------|
| TYPE | CODE | NO. | PATTERN NO. |
| 5 SOT23 | U5+2 | <u>21-0057</u> | |

Revision History

| REVISION NUMBER | REVISION DATE | DESCRIPTION | PAGES CHANGED |
|--------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------|
| 5 | In Table 2 updated the device marking codes for MAX6503UKN035, 10/06 MAX6503UKN025, MAX6503UKN005, MAX6503UKP015, MAX6504UKN035, MAX6504UKN025, and MAX6504UKN005 | | 7 |
| 6 | 2/11 | Removed the TO-220 package from entire data sheet; changed all leaded parts to lead(Pb)-free parts in the <i>Ordering Information</i> table; in the <i>Absolute Maximum Ratings</i> section changed the continuous power dissipation numbers (7.1mW/°C to 3.1mW/°C and 571mW to 247mW) and added the soldering temperature; added the <i>Package Information</i> table | AII |

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