# MBR2535CTG, MBR2545CTG

# Switch-mode Power Rectifiers

The MBR2535CTG/45CTG series uses the Schottky Barrier principle with a platinum barrier metal. These state-of-the-art devices have the following features:

## Features

- Guardring for Stress Protection
- Low Forward Voltage
- 175°C Operating Junction Temperature
- These are Pb-Free Devices\*

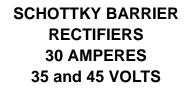
## **Mechanical Characteristics**

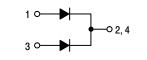
- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds

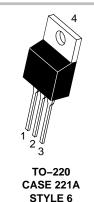


# **ON Semiconductor®**

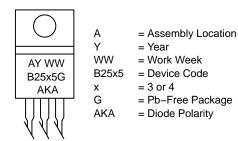
http://onsemi.com







## MARKING DIAGRAM



## ORDERING INFORMATION

Device	Package	Shipping
MBR2535CTG	TO–220 (Pb–Free)	50 Units/Rail
MBR2545CTG	TO–220 (Pb–Free)	50 Units/Rail

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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## MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage MBR2535CTG MBR2545CTG	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	35 45	V	
Average Rectified Forward Current (Rated $V_R$ , $T_C = 160^{\circ}C$ ) Per Device Per Diode	I <sub>F(AV)</sub>	30 15	A	
Peak Repetitive Forward Current per Diode Leg (Rated $V_R$ , Square Wave, 20 kHz, $T_C = 150^{\circ}C$ )	I <sub>FRM</sub>	30	А	
Non-Repetitive Peak Surge Current per Diode Leg (Surge Applied at Rated Load Conditions, Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	150	А	
Peak Repetitive Reverse Surge Current (2.0 µs, 1.0 kHz)	I <sub>RRM</sub>	1.0	А	
Storage Temperature Range	T <sub>stg</sub>	-65 to +175	°C	
Operating Junction Temperature (Note 1)	TJ	-65 to +175	°C	
Voltage Rate of Change (Rated V <sub>R</sub> )	dv/dt	10,000	V/μs	
ESD Ratings: Machine Model = C Human Body Model = 3B	ESD	> 400 > 8000	V	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .

### THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case Junction-to-Ambient (Note 2)	${\sf R}_{ heta {\sf JC}} \ {\sf R}_{ heta {\sf JA}}$	1.5 50	°C/W

2. When mounted using minimum recommended pad size on FR-4 board.

### ELECTRICAL CHARACTERISTICS (Per Diode)

Symbol	Characteristic	Condition	Min	Тур	Мах	Unit
VF	Instantaneous Forward Voltage (Note 3)	$ \begin{array}{l} I_F = 15 \text{ Amp, } T_J = 25^\circ C \\ I_F = 15 \text{ Amp, } T_J = 125^\circ C \\ I_F = 30 \text{ Amp, } T_J = 25^\circ C \\ I_F = 30 \text{ Amp, } T_J = 125^\circ C \end{array} $	- - -	_ 0.50 _ 0.65	0.62 0.57 0.82 0.72	V
I <sub>R</sub>	Instantaneous Reverse Current (Note 3)	Rated dc Voltage, $T_J = 25^{\circ}C$ Rated dc Voltage, $T_J = 125^{\circ}C$		_ 9.0	0.2 25	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Pulse Test: Pulse Width =  $300 \ \mu$ s, Duty Cycle  $\leq 2.0\%$ .

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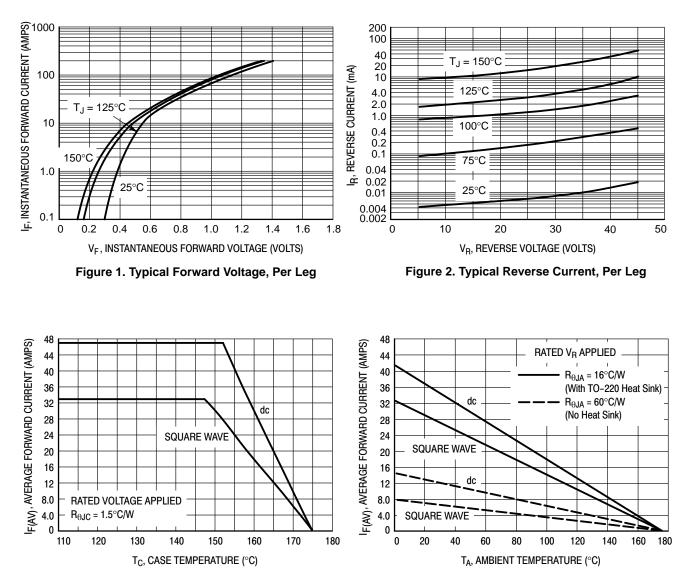


Figure 3. Current Derating, Per Device

Figure 4. Current Derating, Per Device

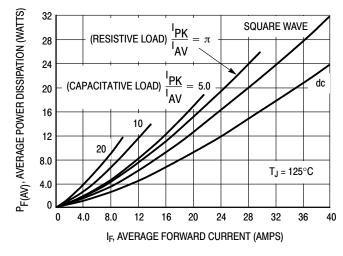


Figure 5. Forward Power Dissipation

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