

#### NOT RECOMMENDED FOR NEW DESIGN NO ALTERNATIVE PART



## DMJ70H601SV3

#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	Ι <sub>D</sub> T <sub>C</sub> = +25°C
700V	0.6Ω @ V <sub>GS</sub> = 10V	8A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

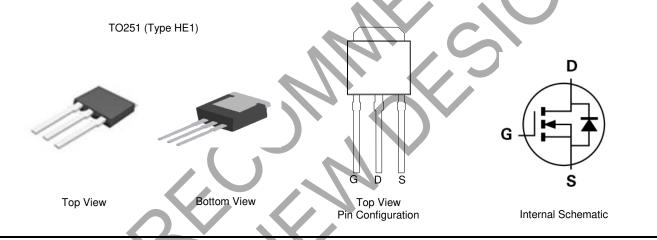
- Adaptor
- LCD & PDP TV
- Lighting

# Features and Benefits Low On-Resistance

- High BV<sub>DSS</sub> Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: TO251 (Type HE1)
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H601SV3	TO251 (Type HE1)	75 Pieces / Tube

EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

<1000ppm antimony compounds. 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**

Notes:



**D**: I = Manufacturer's Marking8N70SV = Product Type Marking CodeYYWW = Date Code MarkingYY or <u>YY</u>= Last Two Digits of Year (ex: 17 = 2017)WW or <u>WW</u> = Week Code (01 to 53)



#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	700	V	
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Continuous Drain Current (Note 5) $V_{GS}$ = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ID	8 6.4	А
Maximum Body Diode Forward Current (Note 6)		IS	4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	15	А
Avalanche Current (Note 7)	L = 60mH	I <sub>AS</sub>	1.7	А
Avalanche Energy (Note 7)	L = 60mH	E <sub>AS</sub>	86	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	7	V/ns

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Unit	Value	Symbol		Characteristic
W	125	PD	$T_{\rm C} = +25^{\circ}{\rm C}$ $T_{\rm C} = +100^{\circ}{\rm C}$	Total Power Dissipation (Note 5)
°C/W	72	R <sub>0JA</sub>		Thermal Resistance, Junction to Ambient (Note 6)
0/00	1.0	Rejc		Thermal Resistance, Junction to Case (Note 5)
°C	-55 to +150	T <sub>J</sub> , T <sub>STG</sub>		Operating and Storage Temperature Range
-	-55 to +150	IJ, ISTG		Operating and Storage Temperature Range

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## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified,)

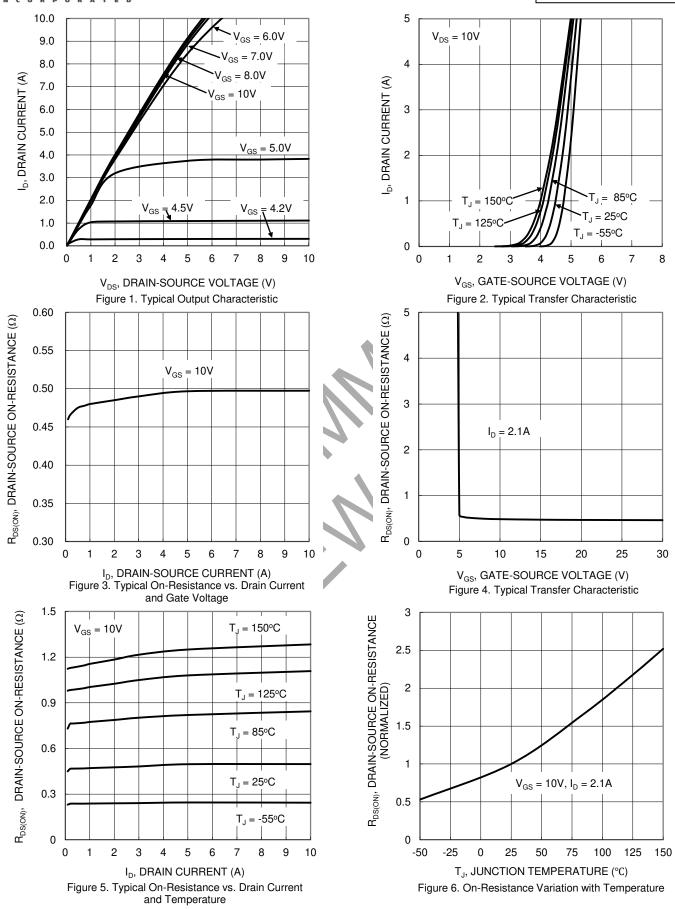
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			170	Mux	onne		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	700	—	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS			1	μA	$V_{DS} = 700V, V_{GS} = 0V$	
Gate-Source Leakage	Igss			100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	3.4	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	RDS(ON)		0.5	0.6	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.1A	
Diode Forward Voltage	V <sub>SD</sub>		0.85	1.3	V	$V_{GS} = 0V, I_S = 2.1A$	
DYNAMIC CHARACTERISTICS (Note 7)				•		·	
Input Capacitance	Ciss	_	686	_		V <sub>DS</sub> = 50V, f = 1MHz,	
Output Capacitance	Coss	_	267	_	pF	$V_{DS} = 50V, T = TWHZ,$ $V_{GS} = 0V$	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	8	_		VGS – UV	
Gate Resistance	R <sub>G</sub>	—	2.6	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	—	20.9	—			
Gate-Source Charge	Qgs	—	3.0	—	nC	$V_{DD} = 560V, I_D = 8A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q <sub>gd</sub>	—	9.4	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	—	10	—			
Turn-On Rise Time	t <sub>R</sub>	—	23	—	ns	$\label{eq:VDD} \begin{split} V_{DD} &= 350 V, \ V_{GS} = 10 V, \\ R_G &= 4.7 \Omega, \ I_D = 8 A \end{split}$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	32	—	115		
Turn-Off Fall Time	t <sub>F</sub>		17	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>		261	_	ns		
Body Diode Reverse Recovery Time $(T_J = +150^{\circ}C)$	t <sub>RR</sub>		337		ns		
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		3.0		μC	- I <sub>S</sub> = 8A, dI/dt = 100A/μs	
Body Diode Reverse Recovery Charge $(T_J = +150^{\circ}C)$	Q <sub>RR</sub>		4.0	_	μC	7	

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
 Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.

Notes:

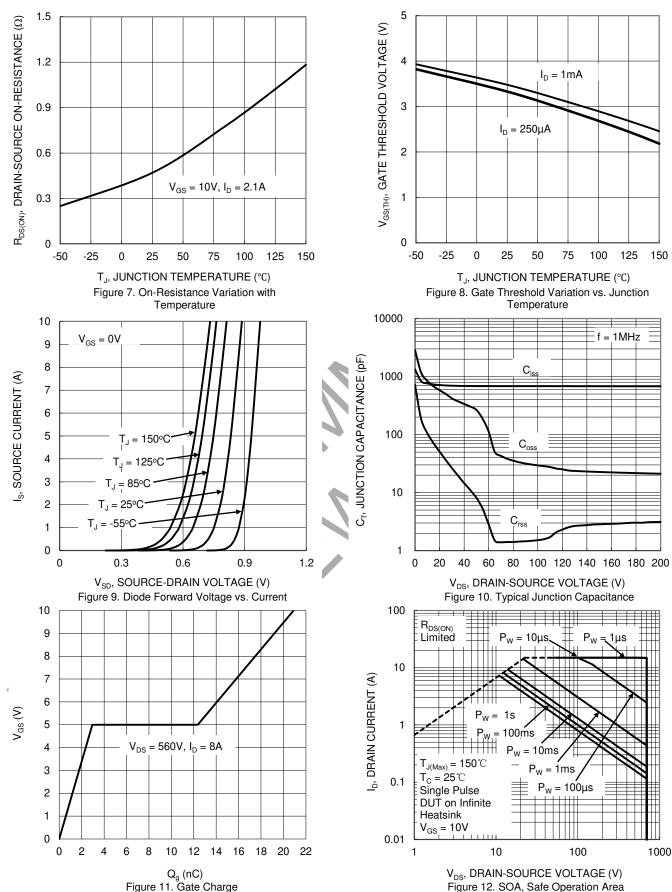


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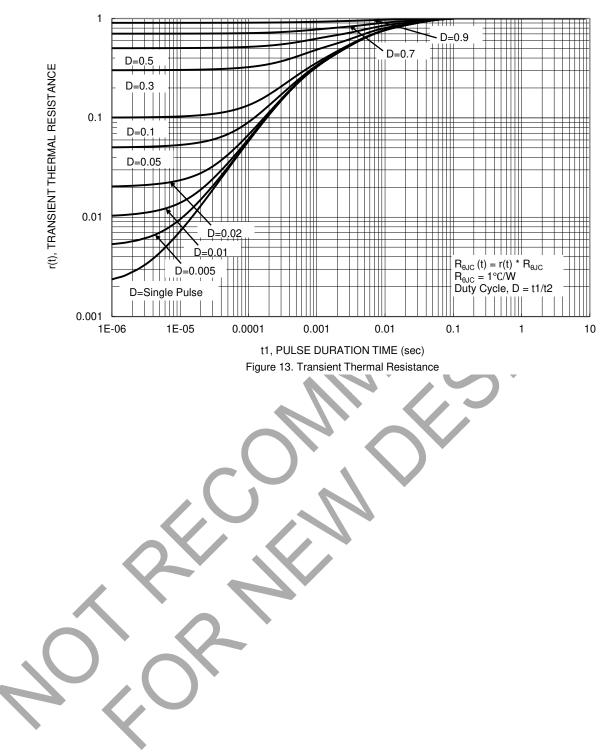




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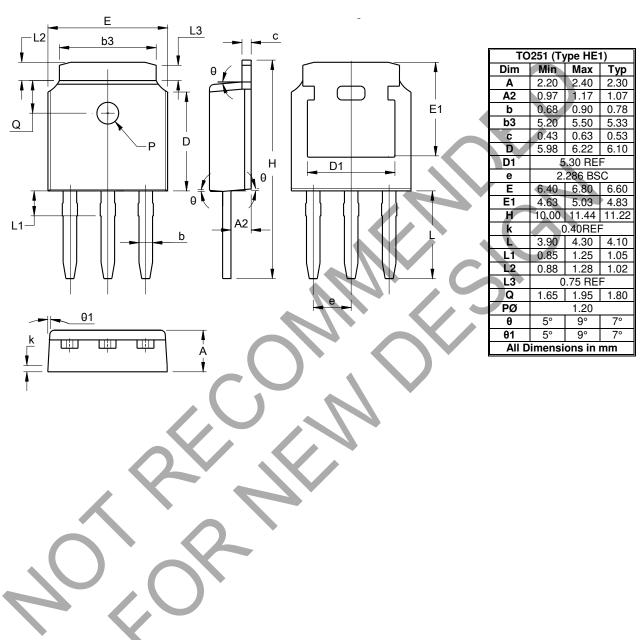






#### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



TO251 (Type HE1)



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