

Surge arrester

3-electrode arrester

Series/Type: T90-A90XSMD Ordering code: B88069X2331T902

Date: 2021-01-22

Version: 09

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3-electrode arrester T90-A90XSMD

Features

- Small size
- Fast response time
- High current rating
- Stable performance over life
- Very low capacitance
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications

- Modem
- Data lines

Electrical specifications

DC spa	rk-over voltage	1) 2) 3)		90	V
	Tolerance			±20	%
	Min.			72	V
	Max.			108	V
Impulse	spark-over vo				
·	at 100 V/μs - for 99% of measured values - typical values of distribution		ed values	< 450	V
			stribution	< 350	V
	at 1 kV/µs	- for 99% of measure		< 600	V
		 typical values of dis 	stribution	< 500	V
Service	life				
	10 operation	S	50 Hz; 1 s ⁴⁾	10	Α
	1 operation		50 Hz; 0.18 s (9 cycl.) 4)	40	Α
	10 Operation	าร	8/20 μs ⁴⁾	10	kA
	1 operation		8/20 μs ^{4) 6)}	20	kA
	1 operation		10/350 μs ⁴⁾	2	kA
	300 operation	s (+/-, alternating pol.)	10/1000 μs ⁴⁾	200	Α
Insulation	on resistance a	> 1	$G\Omega$		
Capacit	ance at 1 MHz	< 1.5	рF		
Transve	erse delay time	< 0.2	μs		
Arc volt	age at 1 A	~ 15	V		
Glow to	arc transition of	< 0.5	Α		
Glow vo	oltage at 0.1 A	~ 70	V		
Weight				~ 0.8	g
Operati	on and storage	−55 +125	°C		
Climatio	category (IEC	55/125/21			
Marking, blue negative				EPCOS 90 YY O 90 - Nominal voltage YY - Year of production O - Non radioactive	

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PPD AB PD / PPD AB PM



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UL 497B (E163070)

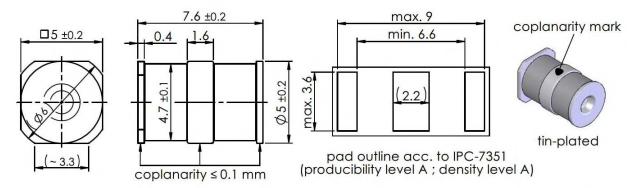
- 1) At delivery AQL 0.65 level II, DIN ISO 2859
- 2) In ionized mode

Certifications

- 3) Tip or ring electrode to center electrode
- 4) Total current through center electrode, half value through tip respectively ring electrode.
- ⁵⁾ Test according to ITU-T Rec. K.12
- 6) DC spark-over voltage may exceed limit of +/-25% but will continue to protect without venting

Terms in accordance with ITU-T Rec. K.12; IEC 61663 and IEC 61643-311.

Dimensional drawing in mm



* for reflow soldering, coplanarity mark upwards

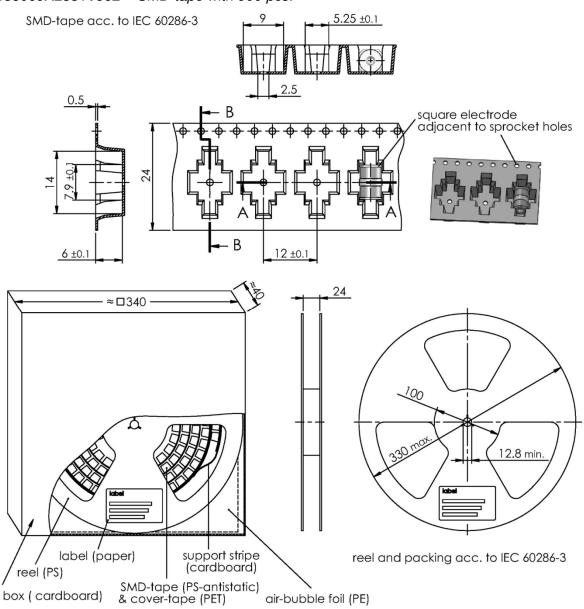


3-electrode arrester

T90-A90XSMD

Ordering code and packing advice

B88069X2331**T902** = SMD-tape with 900 pcs.

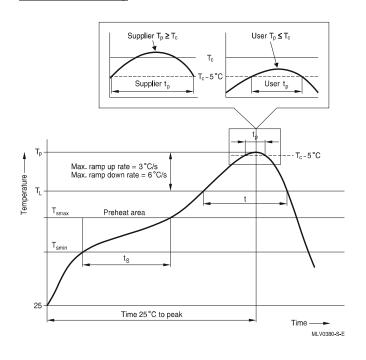




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Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	T _{smin} T _{smax} t _{smin} to t _{smax}	100 °C 150 °C 60 120 s	150 °C 200 °C 60 180 s
Average ramp-up rate	T _{smax} to T _p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T _L	183 °C 60 150 s	217 °C 60 150 s
Peak package body temperature *, Classification temperature **	T _p , T _C	220 235 °C **	245 260 °C **
Time (t _p) ** within 5 °C of the specified classification temperature (T _C)		20 s ***	30 s ***
Average ramp-down rate	T _p to T _{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Surface mounted components (SMD) may exhibit a temporary increase in the DC spark-over voltage after the solder reflow process. The components will recover within 24 hours. There is no quality defect nor change in protection levels during the temporary change in DC spark-over voltage.

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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^{** =} For details please refer to JEDEC J-STD-020D.

^{*** =} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.



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