

PC852X Series PC853X Series

DIP 4pin Darlington Phototransistor Ouput, High Collector-emitter Voltage Photocoupler



■ Description

PC852X Series/PC853X Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0kV.

Collector-emitter voltage is 350V and CTR is MIN. 1 000% at input current of 1mA.

■ Features

- 1. 4pin DIP package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High collector-emitter voltage (V_{CEO}: 350V)
- 4. Durlington phototransistor output (CTR : MIN. 1 000% at $I_F=1mA$, $V_{CE}=2V$)
- Large collector power disspation : PC853X (P_C : 300mW)
- 6. High isolation voltage between input and output (V_{iso(rms)}: 5kV)

■ Agency approvals/Compliance

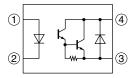
- Recognized by UL1577, file No. E64380 (as model No. PC852/PC853)
- 2. Package resin: UL flammability grade (94V-0)

■ Applications

- 1. Telephone line interface/isolation
- 2. Interface to power supply circuit
- 3. Controller for SSRs, DC motors



■ Internal Connection Diagram

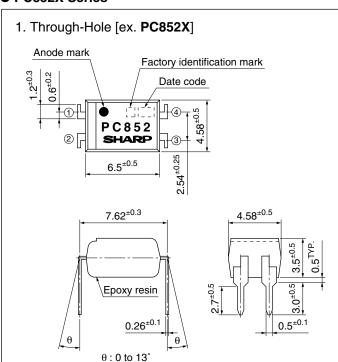


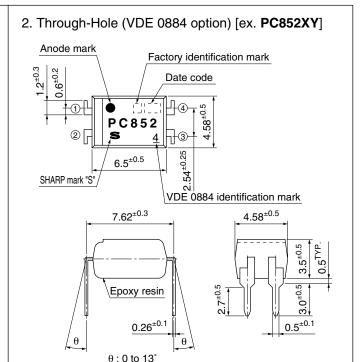
- 1 Anode
- ③ Cathode
- 4 Emitter
- (5) Collector

■ Outline Dimensions

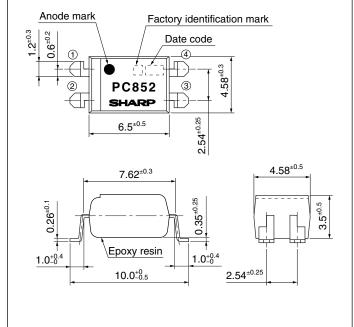
(Unit: mm)

PC852X Series

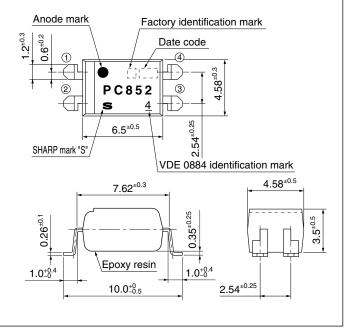




3. SMT Gullwing Lead-Form [ex. PC852XI]

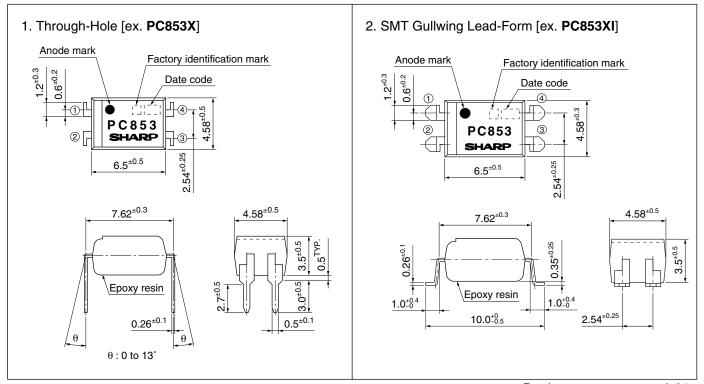


4. SMT Gullwing Lead-Form (VDE 0884 option) [ex. **PC852XPY**]





● PC853X Series (Unit : mm)



Product mass: approx. 0.21g



Date code (2 digit)

	1st o	digit		2nd	digit	
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin
no mark	Ionon
	- Japan
	Indonesia
$\overline{\hspace{1cm}}$	Philippines
_	China

^{*} This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actural status of the production.



■ Absolute Maximum Ratings

$(1_a=25 \text{ C})$

Parameter		Symbol	Rat	ing PC853X	Unit	
	Forward current	I_{F}	50		mA	
m	*1 Peak forward current	I_{FM}	1	1	A	
Input	Reverse voltage	V_R	(5	V	
	Power dissipation	P	7	0	mW	
	Collector-emitter voltage	V_{CEO}	350		V	
but I	Emitter-collector voltage	V _{ECO}	0.1		V	
Output	Collector current	I_{C}	15	50	mA	
	Collector power dissipation	P_{C}	150	300	mW	
Total power dissipation		P _{tot}	200	320	mW	
*2 Isolation voltage		V _{iso (rms)}	5.0		kV	
Operating temperature		Topr	-30 to +100		°C	
Storage temperature		T_{stg}	-55 to +125		°C	
*3 5	Soldering temperature	T_{sol}	260		°C	

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
	Forward volta	orward voltage V _F		$I_F=10mA$	ı	1.2	1.4	V
Input	Reverse volta	ge	I_R	$V_R=4V$	ı	Ī	10	μΑ
	Terminal capa	acitance	C_{t}	V=0, $f=1kHz$	ı	30	250	pF
Output	Collector dark	k current	I_{CEO}	$V_{CE}=200V, I_{F}=0$	-	-	200	nA
Output	Collector-emitter brea	akdown voltage	$\mathrm{BV}_{\mathrm{CEO}}$	$I_{C}=0.1 \text{mA}, I_{F}=0$ 350		ı	-	V
Collector current		rent	I_{C}	$I_F=1mA$, $V_{CE}=2V$	10	40	150	mA
Co	Collector-emitter saturation voltage		$V_{\text{CE (sat)}}$	$I_F = 20 \text{mA}, I_C = 100 \text{mA}$	-	ı	1.2	V
Transfer Isolation resistance charac- Floating capacitance		stance	$R_{\rm ISO}$	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	_	Ω
		C_{f}	V=0, $f=1MHz$	ı	0.6	1.0	pF	
teristics	Cut-off frequency		f_C	V_{CE} =2V, I_{C} =20mA, R_{L} =100 Ω , -3dB	1	7	-	kHz
	Paspansa tima	Rise time	t_r	V 2V I 20 A D 1000	_	100	300	μs
	Kesponse unie	Response time Fall time		$V_{CE}=2V$, $I_{C}=20$ mA, $R_{L}=100\Omega$	_	20	100	μs

^{*1} Pulse width≤100μs, Duty ratio : 0.001 *2 40 to 60%RH, AC for 1 minute, f=60Hz *3 For 10s



■ Model Line-up

● PC852X Series

Lead Form	Throug	h-Hole	SMT Gullwing			
Package	Sleeve			Taping		
1 ackage	100pcs/sleeve			2 000pcs/reel		
VDE0884	— Approved				Approved	
Model No.	PC852X	PC852XY	PC852XI	PC852XP	PC852XPY	

● PC853X Series

Lead Form	Through-Hole	SMT Gullwing		
Package	Sle	leeve Taping		
1 ackage	100pcs	s/sleeve 2 000pcs/s		
VDE0884				
Model No.	PC853X	PC853XI	PC853XP	

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Forward Current vs. Ambient Temperature

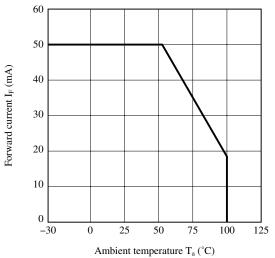


Fig.3-a Collector Power Dissipation vs.

Ambient Temperature (PCS52X)

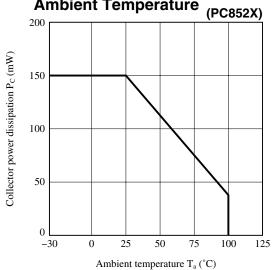


Fig.4 Total Power Dissipation vs. Ambient Temperature

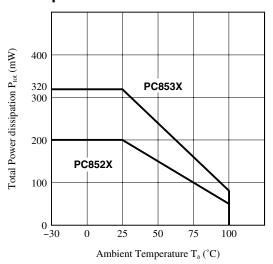


Fig.2 Diode Power Dissipation vs.
Ambient Temperature

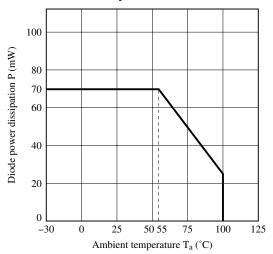


Fig.3-b Collector Power Dissipation vs.

Ambient Temperature

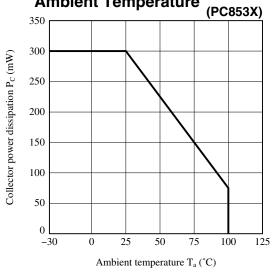


Fig.5 Peak Forward Current vs. Duty Ratio

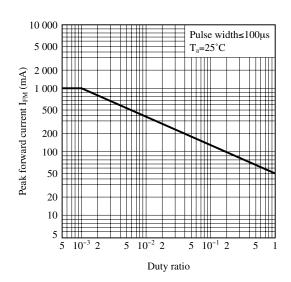




Fig.6 Forward Current vs. Forward Voltage

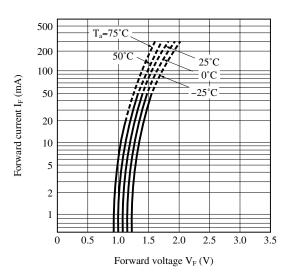


Fig.7-b Current Transfer Ratio vs. Forward Current

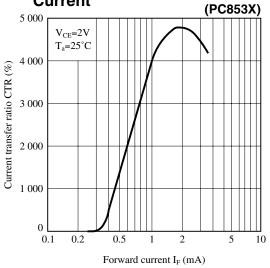


Fig.8-b Collector Current vs. Collectoremitter Voltage

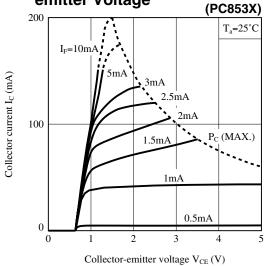


Fig.7-a Current Transfer Ratio vs. Forward
Current

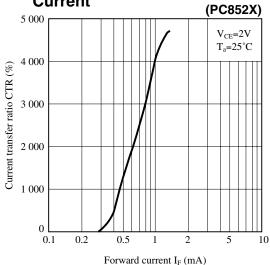


Fig.8-a Collector Current vs. Collectoremitter Voltage

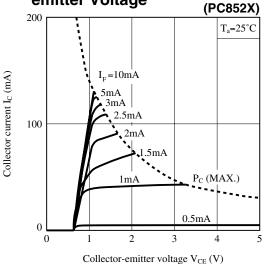


Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

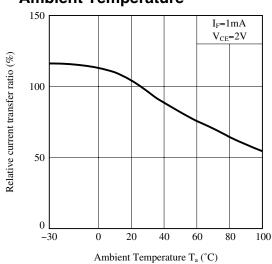




Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

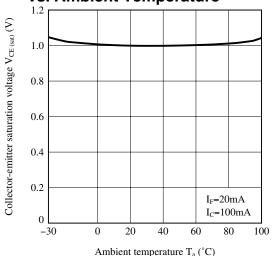


Fig.12 Response Time vs. Load Resistance

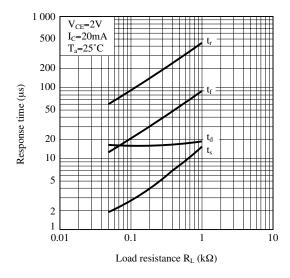


Fig.14 Frequency Response

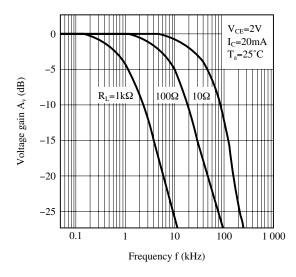


Fig.11 Collector Dark Current vs. Ambient Temperature

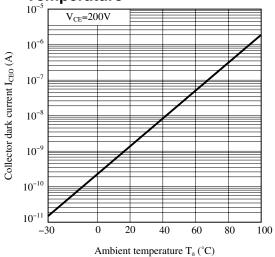
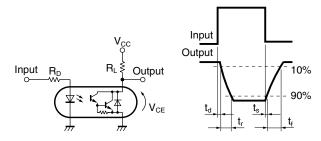
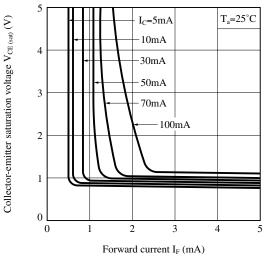


Fig.13 Test Circuit for Response Time



Please refer to the conditions in Fig.12.

Fig.15 Collector-emitter Saturation Voltage vs. Forward Current



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<1.0mA, CTR variation may increase.

Please make design considering this fact.

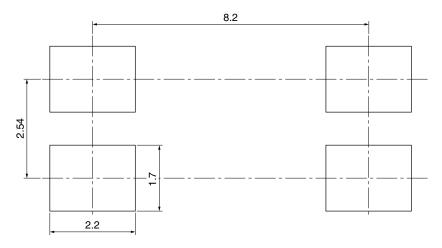
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

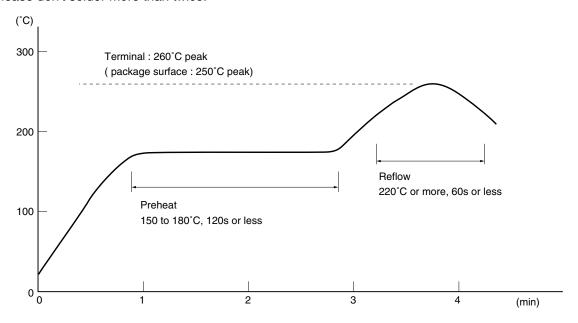
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

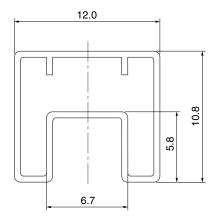
Package method

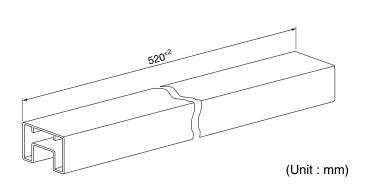
 $MAX.\ 100pcs\ of\ products\ shall\ be\ packaged\ in\ a\ sleeve.\ Both\ ends\ shall\ be\ closed\ by\ tabbed\ and\ tabless\ stoppers.$

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

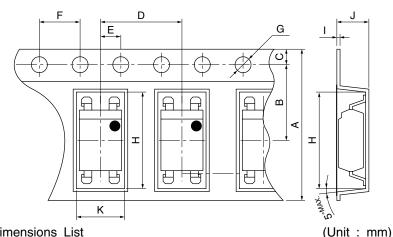
Package materials

Carrier tape: PS

Cover tape: PET (three layer system)

Reel: PS

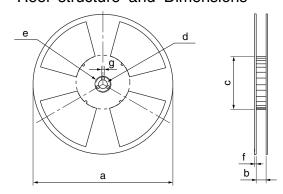
Carrier tape structure and Dimensions



I limonoiono I	101
Dimensions L	151

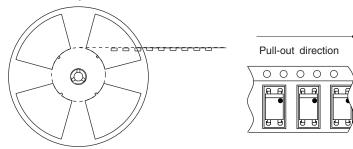
Difficition	IO LIGI				٠, ٠	1111 . 1111111
A	В	C	D	Е	F	G
16.0±0.3	7.5 ^{±0.1}	1.75 ^{±0.1}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 ^{+0.1}
Н	I	J	K			
10.4 ^{±0.1}	0.4 ^{±0.05}	4.2 ^{±0.1}	5.1 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(U	nit: mm)
a	b	с	d
330	17.5 ^{±1.5}	100±1.0	13 ^{±0.5}
e	f	g	
23±1.0	2.0 ^{±0.5}	2.0 ^{±0.5}	

Direction of product insertion



[Packing: 2 000pcs/reel]



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- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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