

CRYSTAL OSCILLATOR (SPXO)

OUTPUT: LV-PECL, LVDS





Product Number

SG2016EHN: X1G006141xxxx15 SG2016VHN: X1G006121xxxx15 SG2520EHN: X1G005921xxxx15

SG2520VHN: X1G005941xxxx15

SG2016EHN/VHN SG2520EHN/VHN

: 25 MHz to 500 MHz •Frequency range

 Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.

: $\pm 20 \times 10^{-6}$ •Frequency tolerance

Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C Output enable (OE) or Standby (ST) Function

: 50 fs Max. (391 MHz < fo \leq 500 MHz, $V_{CC} = 2.5 \text{ V}$, 3.3 V) Phase iitter



SG2016EHN SG2016VHN $(2.0 \times 1.6 \times 0.63 \text{ mm})$



SG2520EHN SG2520VHN $(2.5 \times 2.0 \times 0.74 \text{ mm})$

Specifications (characteristics)

| | | Specifications | | Conditions / Remarks | | |
|--------------------------|------------------|--|--|--|---|--|
| Item | Symbol | LV-PECL LVDS | | | | |
| Rom | Cymbol | SG2016EHN / SG2520EHN | SG2016VHN / SG2520VHN | | Conditions / Remarks | |
| Output frequency range | fo | | 25 MHz to 500 MHz | | Please contact us for available frequencies. | |
| Supply voltage | Vcc | C: 3.3 V ± 5 % D: 2.5 V ± 5 % E: 1.8 V ± 5 % | | E: 1.8 V ± 5 % | | |
| Storage temperature | T_stg | | -55 °C to +125 °C | | | |
| Operating temperature | T_use | G: | -40 °C to +85 °C, H: -40 °C to | +105 °C | | |
| Frequency tolerance | f_tol | C: ±20 × 10 ⁻⁶ Max. | | Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C) | | |
| | | 60 mA Max. – | | OE or $\overline{ST} = V_{CC}$, L_ECL = \$ | 50 Ω | |
| Current consumption | Icc | - | 25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max. | 25 mA / – / 25 mA Max. | 25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 392 MHz 392 MHz ≤ fo ≤ 500 MHz | OE or $\overline{ST} = V_{CC}$, Output option: A / B / C |
| Disable current | I dis | 35 mA Max. | 20 mA M | av | OE = GND | |
| | | 00 m/ max. | 30 µA Max. | ux. | ST = GND, T use Max. = - | -85 °C |
| Stand-by current | I_std | | 60 µA Max. | | ST = GND. T use Max. = - | |
| Symmetry | SYM | 45 % to 55 % | | At output crossing point | | |
| * * | V _{OH} | V 11 V Min | | Output option: A, DC characteristic | | |
| Output voltage (LV-PECL) | Vol | | | | | |
| | Vsw | 0.8 V to 2.0 V | 500 mV to 900 mV | 500 mV to 900 mV | Output option: A | |
| Differential swing | | - | 800 mV to 1 600 mV | _ | Output option: B | |
| | | - | 600 mV to 1 200 mV | 600 mV to 1 200 mV | Output option: C | |
| | V _{OD} | | 250 mV to 450 mV | 250 mV to 450 mV | Output option: A | Differential output voltage, |
| | | _ | 400 mV to 800 mV | - | Output option: B | V _{OD1} , V _{OD2} |
| Output voltage (LVDS) | | | 300 mV to 600 mV | | Output option: C | 1 051, 1 052 |
| a apar remage (= r = c) | dV _{OD} | - | 50 mV M | | $dV_{OD} = V_{OD1} - V_{OD2} $ | |
| | Vos | | 1.15 V to 1.35 V | 0.65 V to 0.85 V | Offset voltage, V _{OS1} , V _{OS2} | |
| | dV _{os} | 50 Ω | – 50 mV Max. | | dV _{OS} = V _{OS1} - V _{OS2} Terminated to V _{CC} - 2.0 V | |
| Output load condition | L LVDS | - 30 12 | - 100 Ω | | Connected between OUT and OUT | |
| | V _{IH} | _ | | | | and Ooi |
| Input voltage | VIL | 30 % V _{CC} Max. | | OE or ST terminal | | |
| | | | | | LV-PECL: 20 % - 80 % (V | OH - VOI) |
| Rise/Fall times | tr/tf | 0.35 ns Max. | | | ferential output peak to peak | |
| Start-up time | t_str | 10 ms Max. | | t = 0 at 90 % V _{CC} | | |
| Phase jitter | | 250 fs Max. | 250 fs Max. | 400 fs Max. | 25 MHz ≤ fo < 100 MHz | Offset frequency |
| | | 90 fs Max. | 100 fs Max. | 130 fs Max. | 100 MHz ≤ fo ≤ 156 MHz | fo < 50 MHz: |
| | tpJ | 70 fs Max. | 60 fs Max. | 70 fs Max. | 156 MHz < fo ≤ 212 MHz | 12 kHz to 5 MHz |
| | | 60 fs Max. | 50 fs Max. | 60 fs Max. | 212 MHz < fo ≤ 391 MHz | fo ≥ 50 MHz: |
| | | 50 fs Max. | 55 15 IVIAX. 50 15 IVIAX. | | 391 MHz < fo ≤ 500 MHz | 12 kHz to 20 MHz |

Product Name

SG2016 EHN 156.250000MHz C C H P Z A

(Standard form)

456789

①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance ©Operating temperature ⑦Function ®Output disable type (Z: High impedance) ⑨Output option

| | 0 - 1 3 | | | |
|---|---------------|--|----|----|
| 4 S | upply voltage | | ⑤F | re |
| С | 3.3 V Typ. | | С | - |
| D | 2.5 V Typ. | | | |
| Ě | 1.8 V Typ. | | | |
| * "E" is only for SG2016VHN and SG2520VHN | | | | |

| ⑤Freq. tolerance | | | |
|------------------|------------------------|--|--|
| C | ±20 × 10 ⁻⁶ | | |
| | | | |

| _ | | , , | |
|----|--------------|-------|---|
| @C | perating tem | p. | |
| G | -40 °C to +8 | 85 °C | Ī |
| Н | -40 °C to +1 | 05 °C | |

Output is high impedance

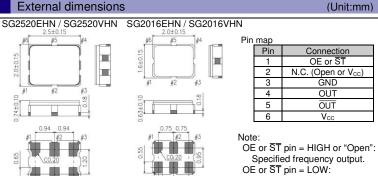
| 7Function | | |
|-----------|----|--|
| Р | OE | |
| S | S₹ | |
| | | |

| (9)C | 9Output option | | | |
|------|-----------------------|------------------------------------|--|--|
| | SG2016EHN / SG2520EHN | SG2016VHN / SG2520VHN | | |
| Α | Default | V _{OD} = 250 mV to 450 mV | | |
| B* | _ | V _{OD} = 400 mV to 800 mV | | |
| С | _ | V _{OD} = 300 mV to 600 mV | | |

(Unit:mm)

*Not available for V_{CC} = 1.8 V Typ

External dimensions



Footprint (Recommended)

SG2520EHN SG2016EHN SG2520VHN SG2016VHN A B 0.88 0.85 0.76 0.574 C 1.38 1.15 D 1.99 1.564

In order to achieve optimum jitter performance, it is recommended that $0.1~\mu F$ and $10~\mu F$ bypass capacitors should be connected between V_{CC} and GND and placed as close to the Vcc pin as possible.

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All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

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(Contains Pb in sealing glass, high melting temperature type solder or other.)







▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc.).

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