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Vishay Siliconix

Automotive N-Channel 60 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.042			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.063			
I _D (A)	7			
Configuration	Single			

Marking Code: 8Nxxx

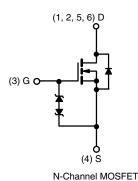
FEATURES

- TrenchFET® power MOSFET
- Typical ESD protection 800 V HBM
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



HALOGEN

FREE



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3426AEEV (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V_{DS}	60	V			
Gate-Source Voltage	V_{GS}	± 20	V			
Continuous Drain Current	T _C = 25 °C		7			
Continuous Drain Current	T _C = 125 °C	I _D	4			
Continuous Source Current (Diode Conduction	on)	I _S	6	Α		
Pulsed Drain Current ^a	I _{DM}	29				
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	10			
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	5	mJ		
Maximum Power Dissipation ^a	T _C = 25 °C	D-	5	W		
Maximum Fower Dissipation "	T _C = 125 °C	P_{D}	1.6	vV		
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to +175	°C			

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient F	PCB Mount b	R_{thJA}	110	°C/W
Junction-to-Foot (Drain)		R _{thJF}	30	G/VV

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static				L				
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu\text{A}$		60	-	-	.,	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μA	1.5	2	2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	-	± 500	nA	
Gate-Source Leakage		V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 1	mA	
		V _{GS} = 0 V	V _{DS} = 60 V	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥5 V	10	-	-	Α	
		V _{GS} = 10 V	I _D = 5 A	-	0.032	0.042	Ω	
Dunin Course On State Presistance 3	R _{DS(on)}	V _{GS} = 10 V	I _D = 5 A, T _J = 125 °C	-	0.056	-		
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 5 A, T _J = 175 °C	-	0.071	-		
		V _{GS} = 4.5 V	I _D = 4 A	-	0.035	0.063		
Forward Transconductance a	9 _{fs}	V _{DS} = 15 V, I _D = 4 A		-	21	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}			-	800	1100		
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$	-	75	100	pF	
Reverse Transfer Capacitance	C _{rss}			-	35	55		
Total Gate Charge ^c	Qg			-	11.5	14		
Gate-Source Charge c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 30 \text{ V}, I_{D} = 6 \text{ A}$	-	1.9	-	nC	
Gate-Drain Charge ^c	Q _{gd}		1		3.5	-		
Gate Resistance	R_g	f = 1 MHz		1.9	3.8	5.7	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	7	10		
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V}, R_L = 7.5 \Omega$ $I_D \cong 4 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		-	10	14	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	20	25		
Fall Time ^c	t _f			-	4	6		
Source-Drain Diode Ratings and Chara	icteristics ^b							
Pulsed Current ^a	I _{SM}				-	29	Α	
Forward Voltage	V _{SD}	I _F = 1.6 A, V _{GS} = 0		_	0.75	1.2	V	

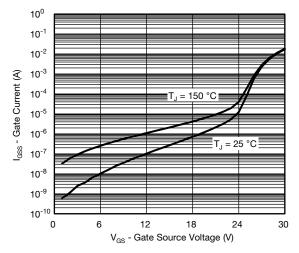
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

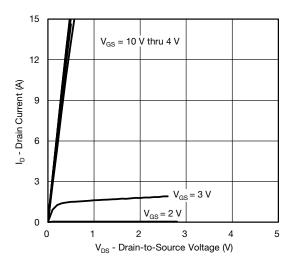
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



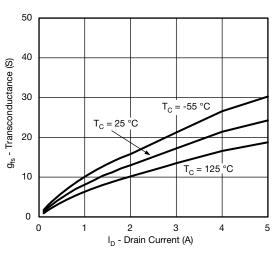
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



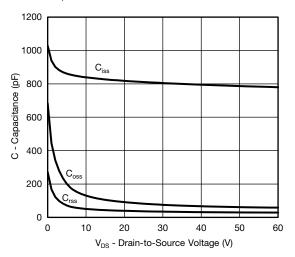
Gate Current vs. Gate-Source Voltage



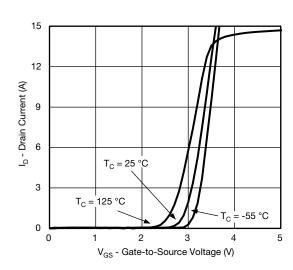
Output Characteristics



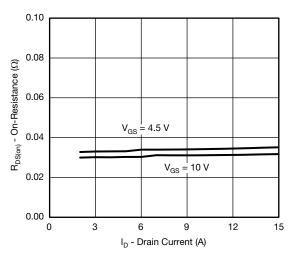
Transconductance



Capacitance



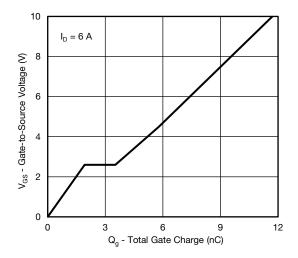
Transfer Characteristics



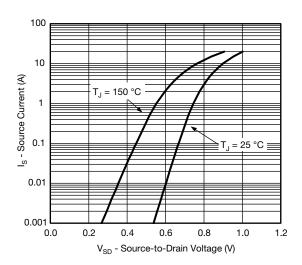
On-Resistance vs. Drain Current



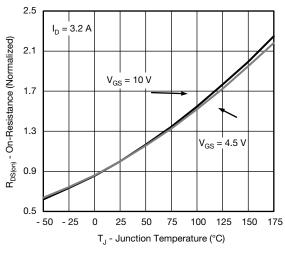
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



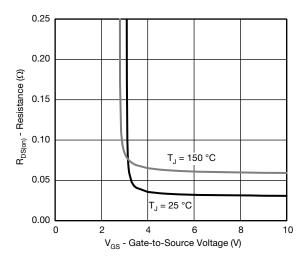
Gate Charge



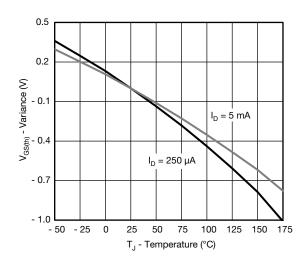
Source-Drain Diode Forward Voltage



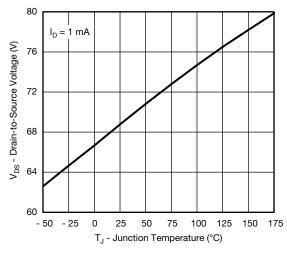
On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-Source Voltage



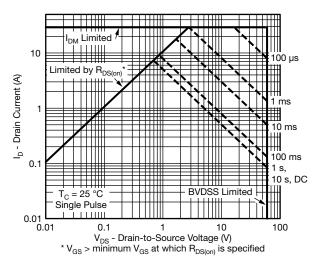
Threshold Voltage



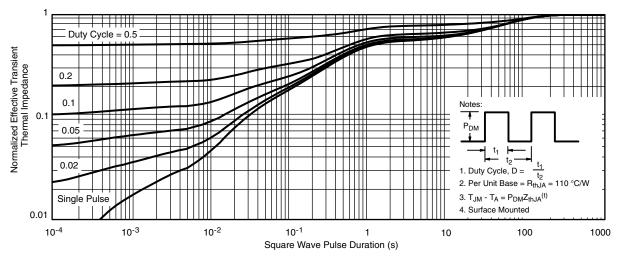
Drain-Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



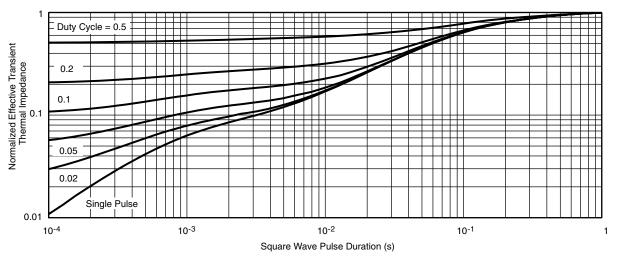
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

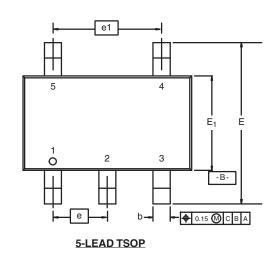
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62982.

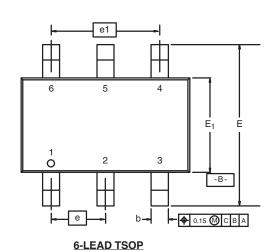


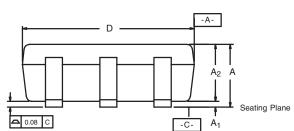


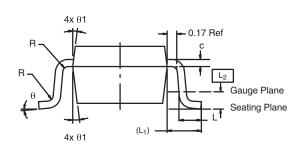
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C









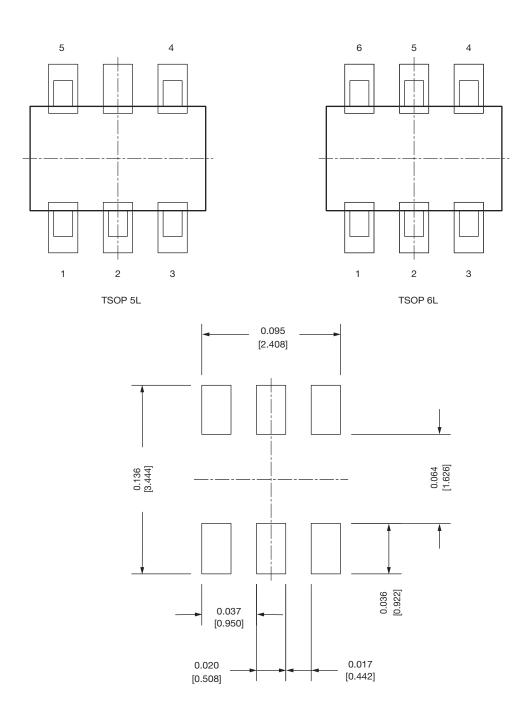
	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref 0.024 Ref					
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200

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Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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