

Vishay Siliconix

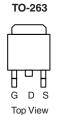
N-Channel 100-V (D-S) 175 °C MOSFET

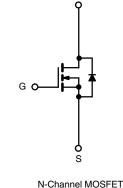
PRODUCT SUMMARY				
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)		
100	0.030 at V _{GS} = 10 V	40		
	0.034 at V _{GS} = 6 V	37.5		

FEATURES

- TrenchFET[®] Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package







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Ordering Information: SUM40N10-30 SUM40N10-30-E3 (Lead (Pb)-free)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	- v	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current ($T_1 = 175 ^{\circ}$ C)	T _C = 25 °C	I _D	40		
Continuous Drain Current $(1) = 175$ C)	T _C = 125 °C	D'D	23	^	
Pulsed Drain Current		I _{DM}	75	- A	
Avalanche Current		I _{AR}	35		
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	61	mJ	
	T _C = 25 °C	Р	107 ^b	w	
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	3.75	vv	
Operating Junction and Storage Temperature Ra	nge	T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	(PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)		R _{thJC}	1.4	0/10	

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{SS} = 0 V, I_D = 250 μ A	100			4 V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
		V_{DS} = 80 V, V_{GS} = 0 V, T_{J} = 125 °C			50		
		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250	1	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	75			А	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.024	0.030	Ω	
		$V_{GS} = 6 V, I_{D} = 10 A$		0.026	0.034		
		V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C			0.054		
		V_{GS} = 10 V, I_{D} = 15 A, T_{J} = 175 °C			0.067		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S	
Dynamic ^b				ļ	ļļ		
Input Capacitance	C _{iss}			2400		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		270			
Reverse Transfer Capacitance	C _{rss}			90			
Total Gate Charge ^c	Qq			35	60	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		11			
Gate-Drain Charge ^c	Q _{gd}			9			
Gate Resistance	R _G			1.7		Ω	
Turn-On Delay Time ^c	t _{d(on)}			11	20	- ns	
Bise Time ^c	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{\text{L}} = 1.25 \Omega$		12	20		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 40 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		30	45		
Fall Time ^c	t _f			12	20		
Source-Drain Diode Ratings and Cha	aracteristics T	°c = 25 °C ^b					
Continuous Current	I _S				40		
Pulsed Current	I _{SM}				75	A	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			60	100	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/μs		5	8	A	
Reverse Recovery Charge	Q _{rr}	, 2 P		0.15	0.4	μC	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

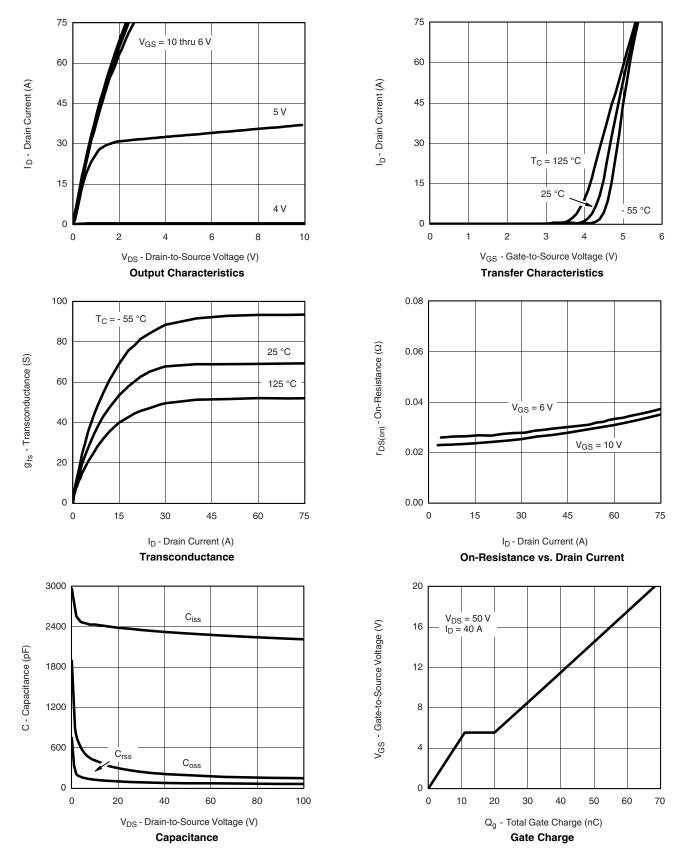
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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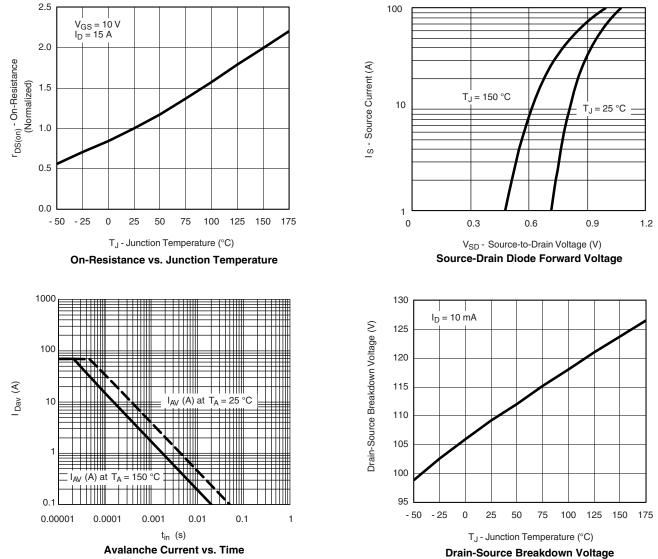
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



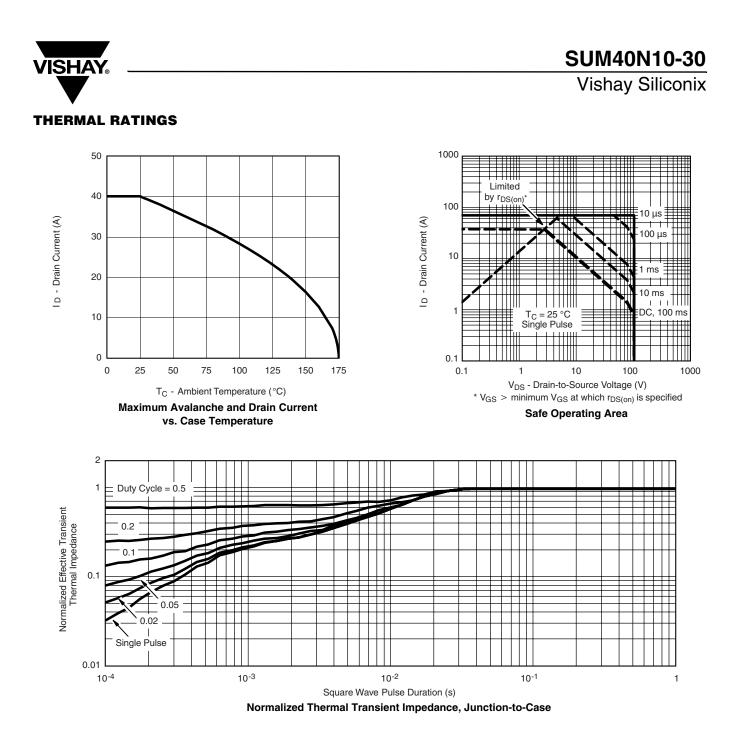
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



vs. Junction Temperature



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