DIFFERENTIAL OUTPUT VIDEO DRIVER WITH SHORT to BATTERY PROTECTION

■FEATURES

- •Operating Voltage 2.65 to 3.45V
- •Operating Temperature -40 to +125°C
- •Short to Battery Protection Circuit of up to 18V
- •Output Capacitor is unnecessary
- •Differential Output, 6dB Amp. , 75Ω Driver
- •LPF Characteristics 0dB at 6.75MHz
 - -40dB at 27MHz
- •CMOS Technology
 •Package Outline
 DFN8-W2
 (ESON8-W2 3mm*3mm)

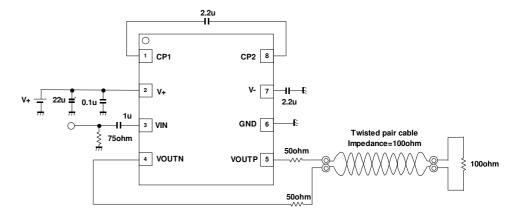
■GENERAL DESCRIPTION

NJU71094 is differential output video driver that built in short to battery protection circuit of up to 18V. Output capacitor is unnecessary because it built in charge-pump circuit. Therefore, NJU71094 can protect from more than voltage of IC's operating voltage, and is suitable to CAR CAMERA, CAR AV system and so on.

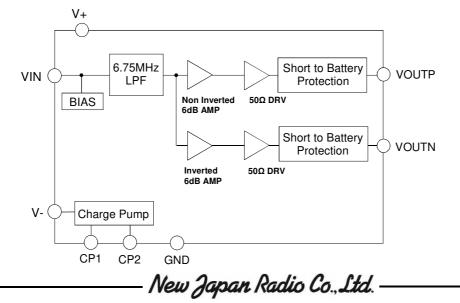
■APPLICATION

■APPLICATION CIRCUIT

- •Car Camera
- Car Navigation



■EQUIVALENT CIRCUIT BLOCK DIAGRAM



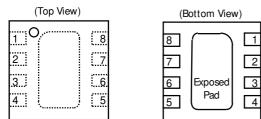


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Built in short to battery protection circuit video driver

Single-end NJU71091-T1	Output type	Part No.
	Single-end	NJU71091-T1

PIN CONFIGURATION

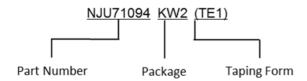


PIN NO.	SYMBOL	DESCRIPTION
1	CP1	Flying Capacitor Terminal
2	V+	Power Supply Terminal
3	VIN	Video Signal Input Terminal
4	VOUTN	Negative Video Signal Output Terminal
5	VOUTP	Positive Video Signal Output Terminal
6	GND	GND Terminal
7	V-	Flying Capacitor Terminal
8	CP2	Flying Capacitor Terminal

Exposed Pad:

Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.

MARK INFORMATION



■ORDERING INFORMATION

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ(pcs)
NJU71094KW2-T1	ESON8-W2	Yes	Yes	Sn-2Bi	71094T	18.0	3,000

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■ABSOLUTE MAXIMUM RATINGS

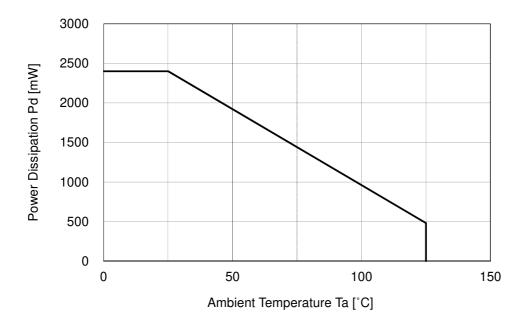
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	3.5	V
Power Dissipation (Ta=25°C)	PD	2400 (1)	mW
Operating Temperature Range	T _{opr}	-40 to 125	°C
Storage Temperature Range	T _{stg}	-55 to 150	°C

(1) Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 4Layers FR-4, with Exposed Pad) (For 4Layers: Applying 99.5×99.5mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

■RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	2.65 to 3.45	V

■POWER DISSIPATION vs. AMBIENT TEMPERATURE



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PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
DC Characteristics						
		No input signal	-	27.0	42.0	
Supply Current	lcc	No input signal,			40.0	mA
		Ta=-40°C to 125°C	-	-	42.0	
Video Amplifier Characteri	stics					
Maximum Outraut		Vin=100kHz, THD=1%,	3.6	-	-	
Maximum Output	Vom	Vin=100kHz, THD=1%,	0.0			Vp-p
Voltage Swing		Ta=-40°C to 125°C	3.6	-	-	
		Vin=100kHz, 1.0Vp-p	- 0			
		Sine wave	5.6	6.0	6.4	
Voltage Gain	Gv	Vin=100kHz, 1.0Vp-p	- 0			dB
		Sine wave, Ta=-40°C to 125°C	5.6	-	6.4	
		Vin=6.75MHz/1MHz, 1.0Vp-p	-1.0	0	1.0	
L DE Characteristics	Gf6.75M	Vin=6.75MHz/1MHz, 1.0Vp-p	-1.0		1.0	
		Ta=-40°C to 125°C		-		
LPF Characteristics		Vin=27MHz/100kHz, 1.0Vp-p	-	-40.0	-24.0	dB
	Gf27M	Vin=27MHz/100kHz, 1.0Vp-p				
		Ta=-40°C to 125°C	-	-	-24.0	
	50	Vin=1.0Vp-p,		0.5		<u> </u>
Differential Gain	DG	10step Video signal	-	0.5	-	%
D'fferentiel Die een	55	Vin=1.0Vp-p,		0.5		
Differential Phase	DP	10step Video signal	-	0.5	-	deg
		RL=75Ω, 1.0Vp-p,				
S/N Ratio	SN	100% White video signal input,	-	70	-	dB
Quuitabing Naisa Laval	Neurol	BW=100kHz to 6MHz	-	1.0	7.0	
Switching Noise Level	Nswpl	10% White video signal input	-	4.0	7.0	mVp-p
Shot to Battery Protection	Connected ZEO to Mart(sinE)			10		
Protected Maximum	Vstbm	Connected 75Ω to Vout(pin5) Connected 75Ω to Vout(pin5)	-	-	18	v
Input Voltage (2)	VSIOIT	Ta=-40°C to 125°C	-	-	18	v
. .		Connected 75Ω to Vout(pin5)	-	6.0	9.0	
Detect Protected	Vth	Connected 75Ω to Vout(pin5)				V
Input Voltage (3)		Ta=-40°C to 125°C	-	-	9.0	
Input Current	lstb	Vout=18V,	-	5.4	-	mA
	.515	Connected 75 Ω to Vout(pin5)		0.1		

■ELECTRICAL CHARACTERISTICS (Ta=25°C, V⁺=3.0V, RL=150Ω, unless otherwise specified)

(2) Maximum input voltage of destination of output resistance 75Ω

(3) It is become protect mode at more than this voltage against input voltage of destination of output resistance 750hm.

(4) If supply voltage is OFF, you must not input the short to battery voltage. IC is broken in the worst case.

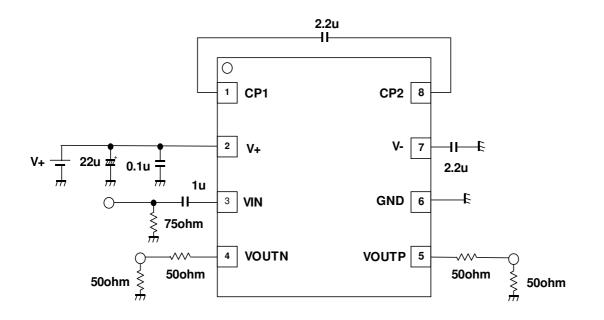
(5) Guaranteed range of Short to Battery voltage is 9V to 18V.

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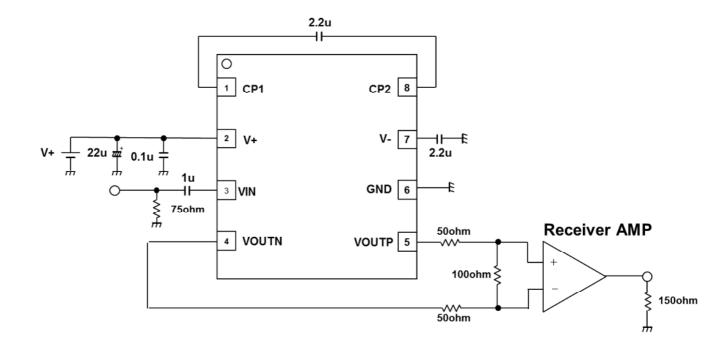
∎TEST CIRCUIT 1

(Supply current, Maximum output level)



∎TEST CIRCUIT 2

(Voltage gain, LPF characteristics, Switching Noise Level)

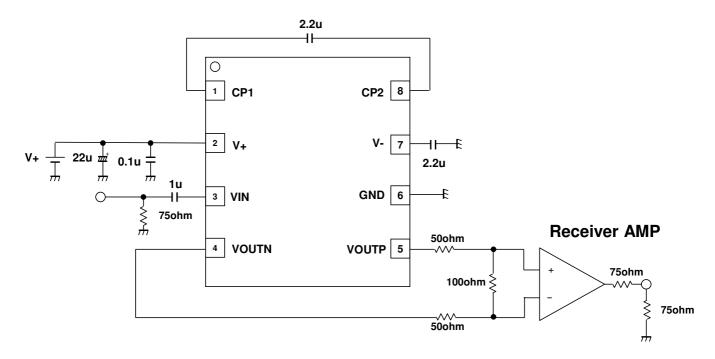


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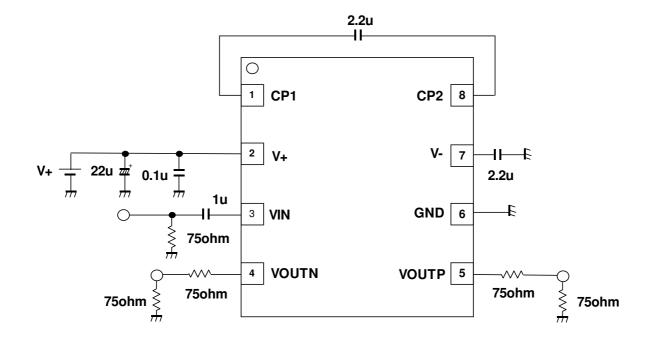
∎TEST CIRCUIT 3

(Differential gain, Differential phase, S/N Ratio)



∎TEST CIRCUIT 4

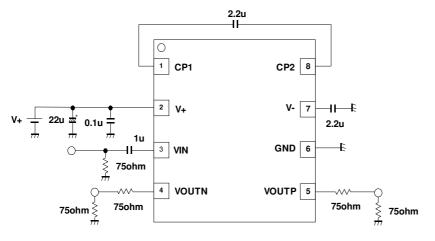
(Detect protected Input voltage)

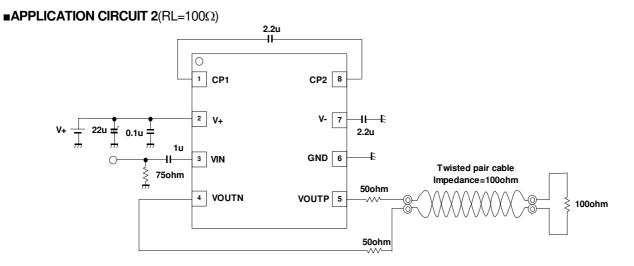


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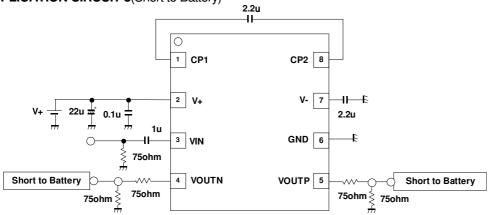


■APPLICATION CIRCUIT 1(RL=150Ω)





■APPLICATION CIRCUIT 3(Short to Battery)



■Note

Exposed Pad: Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.

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TERMINAL FUNCTION

PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
1	CP1	Flying Capacitor Terminal	>+	-
2	V+	V+ Power Supply	-	-
3	VIN	Video Signal Input Terminal	V+ 200 V= 200 V= 150k GND V-	ov
4	VOUTN	Negative Video Signal Output Terminal	10k VOUTN	0V

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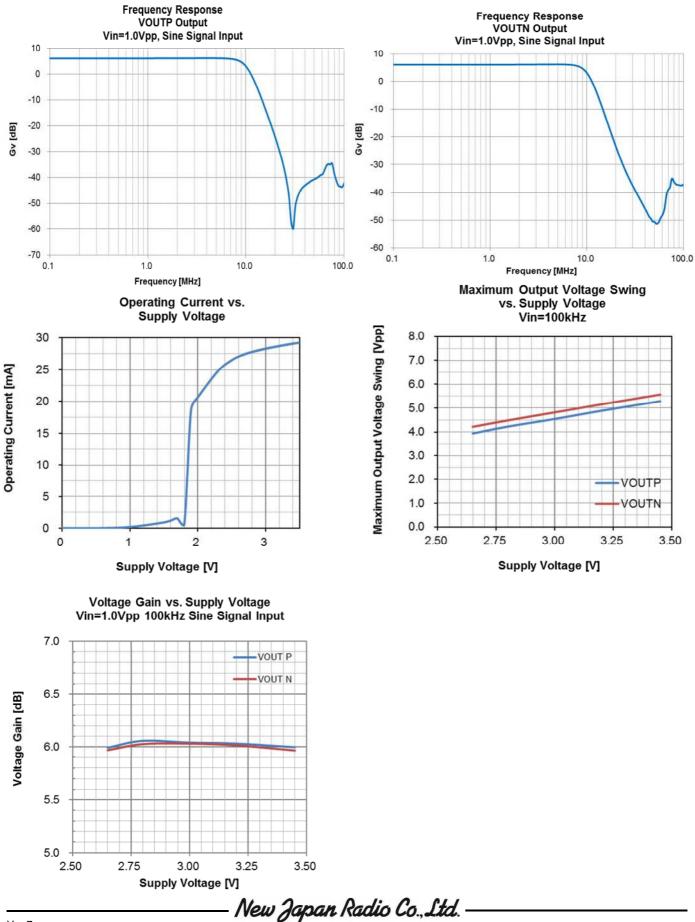


TERMINAL FUNCTION

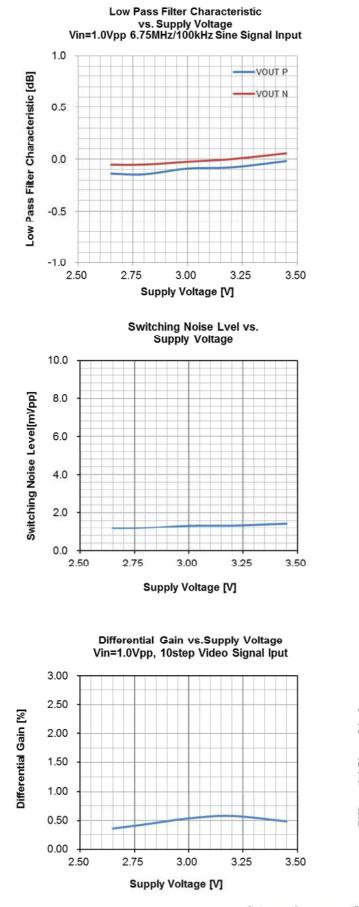
PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
5	VOUTP	Positive Video Signal Output Terminal	5k 5k V-	0V
6	GND	GND	-	-
7	V-	Flying Capacitor Terminal	-	-
8	CP2	Flying Capacitor Terminal	GND GND C C V-	-

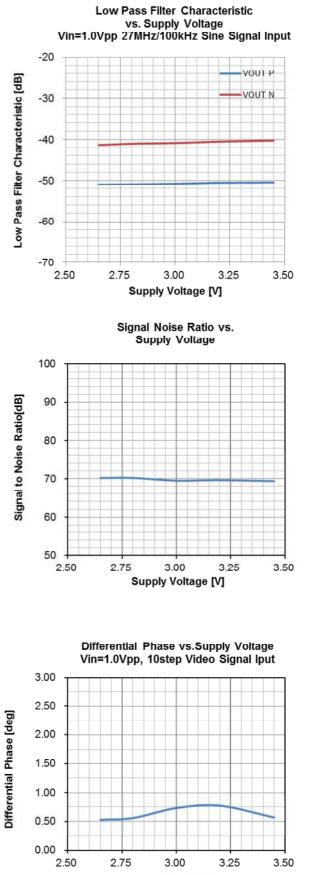
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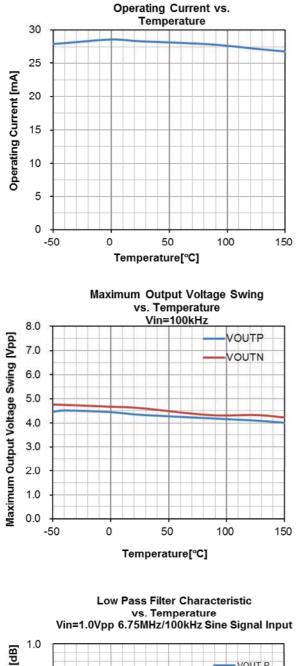


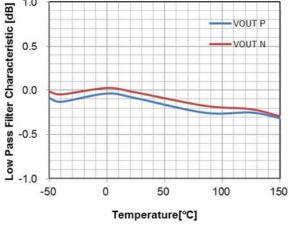


Supply Voltage [V]

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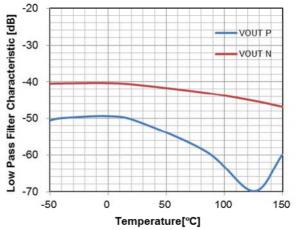






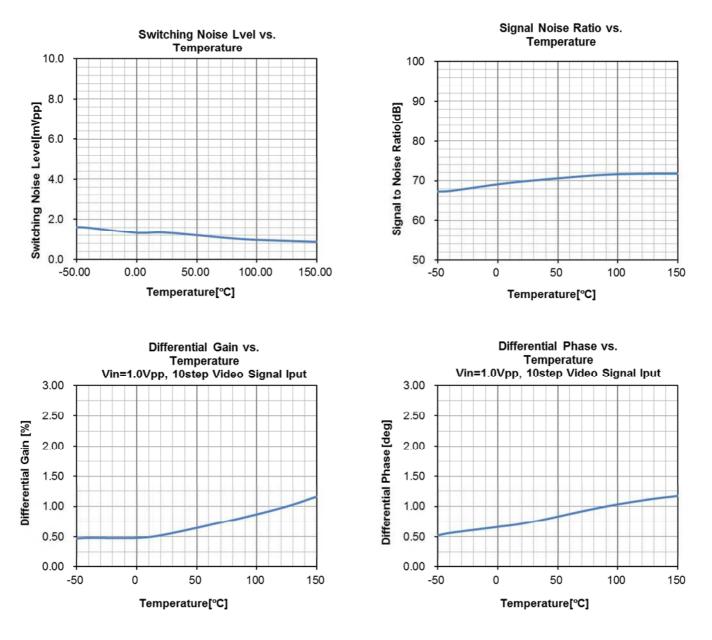
Voltage Gain vs. Temperature Vin=1.0Vpp 100kHz Sine Signal Input 7.0 VOUT P VOUT N 6.5 Voltage Gain [dB] 6.0 5.5 5.0 -50 0 50 100 150 Temperature[°C]

Low Pass Filter Characteristic vs. Temperature Vin=1.0Vpp 27MHz/100kHz Sine Signal Input



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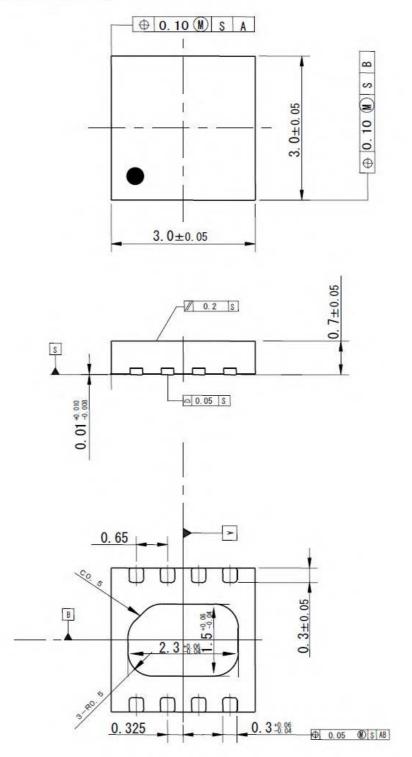


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■PACKAGE OUTLINE

DFN8-W2 (ESON8-W2)

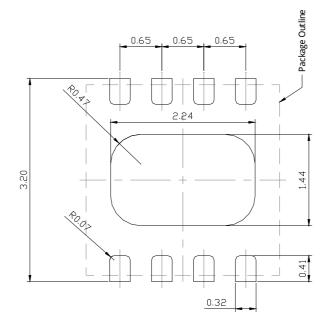


UNIT : mm

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SOLDER FOOT PRINT



Unit : mm

Note : These solder foot print dimensions are just examples. When designing PCB, please estimate the pattern carefully.

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Pull out direction

φDo

■PACKING SPECIFICATION

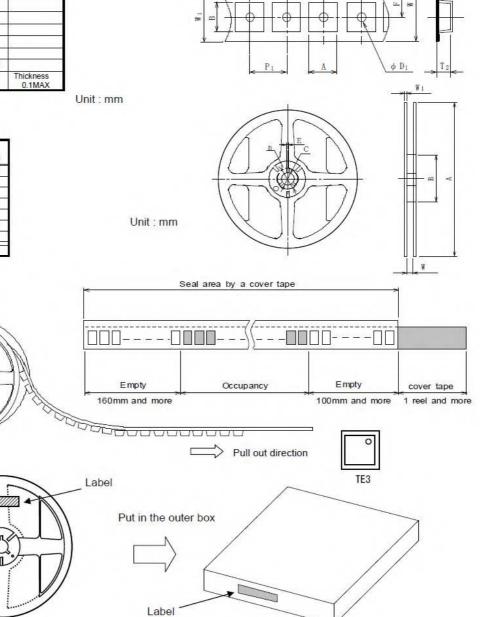
General Description

NJRC delivers ICs in 4 methods, plastic tube container, two kinds of Taping, tray and vinyl bag packing. Except adhesive tape treated anti electrostatic and contain carbon are using as the ESD (Electrostatic Discharge Damage) protection.

ESON Emboss Taping (TE3)

Symbol	DFN8-W2 (ESON8-W2)	Remark
A	3.30±0.1	Bottom size
В	3.30±0.1	Bottom size
Do	1.5+0.1/-0	
D1	1.5+0.1/-0	
E	1.75±0.1	
F	5.5 ±0.05	
Po	4.0 ±0.1	Ç
P ₁	8.0 ±0.1	
P ₂	2.0 ±0.05	
Т	0.30±0.05	
T ₂	1.0	
W	12.0 +0.3/-0.1	
W ₁	9.5	Thickness 0.1MAX

Symbol	DFN8-W2 (ESON8-W2)
Α	φ180 +0/-1.5
В	φ 60 +1/-0
С	φ13.0±0.2
D	φ21.0±0.8
E	2.0±0.5
W	13.0 +0.3/-0
W ₁	1.2
Contents	1,500pcs

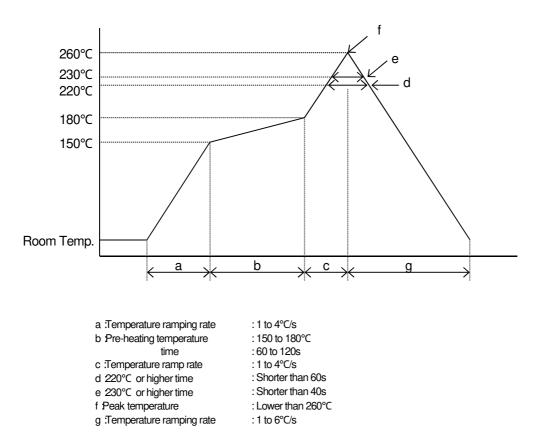


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■RECOMMENDED MOUNTING METHOD

* Recommended reflow soldering procedure



The temperature indicates at the surface of mold package.

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[CAUTION]

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