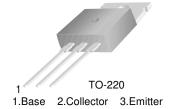


FJP9100

$\begin{array}{l} \textbf{High Voltage Power Darlington Transistor} \\ \bullet \ \ \, \text{Built-in Resistor at Base-Emitter} : R_1(\text{Typ.}) = 2000\Omega \\ \bullet \ \ \, \text{Built-in Resistor at Base} : R_B(\text{Typ.}) = 700 \pm 100\Omega \end{array}$



NPN Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	600	V	
V _{CEO}	Collector-Emitter Voltage	275	V	
V _{EBO}	Emitter-Base Voltage	10	V	
I _C	Collector Current (DC)	4	Α	
I _{CP}	*Collector Current (Pulse)	6	Α	
I _B	Base Current (DC)	0.5	Α	
P _C	Collector Dissipation (T _C =25°C)	40	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 55 ~ 150	°C	

Equivalent Circuit $R_1 \cong 2000 \Omega$ $R_B \cong 700 \,\Omega$

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 500\mu A, I_E = 0$	600			V
BV _{CER}	Collector-Emitter Breakdown Voltage	$I_C = 1 \text{mA}, R_{BE} = 330 \Omega$	600			V
BV _{CEO} (sus)	Collector-Emitter Sustaining Voltage	$I_C = 1.5A$, $I_B = 50mA$, L=25mH	275			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	10			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 600 \text{ V}, I_{E} = 0$			0.1	mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 10V, I_{C} = 0$			0.1	mA
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_{C} = 0.5A$	1000		5000	
		$V_{CE} = 5V, I_{C} = 3A$	1000			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 2A, I_B = 5mA$			1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 2A, I_B = 5mA$			6.0	V
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_{E} = 0, f=1MHz$		110		pF

^{*} Pulse Test: PW=300 μ s, duty Cycle=2% Pulsed

Typical Characteristics

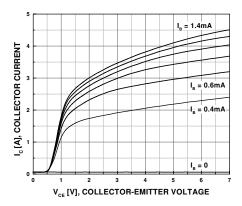


Figure 1. Static Characterstic

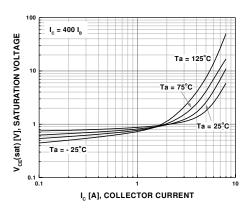


Figure 3. Collector-Emitter Saturation Voltage

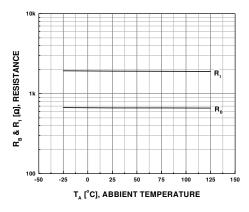


Figure 5. R_B & R₁ vs. Ambient Temperature

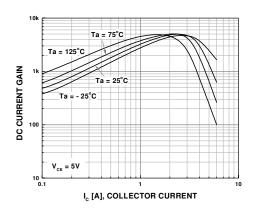


Figure 2. DC current Gain

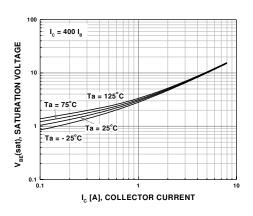


Figure 4. Base-Emitter Saturation Voltage

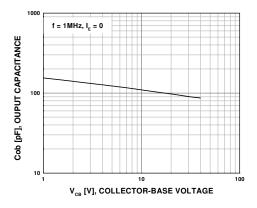


Figure 6. Output Capacitance

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Typical Characteristics (Continued)

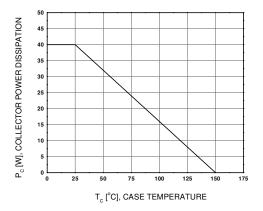
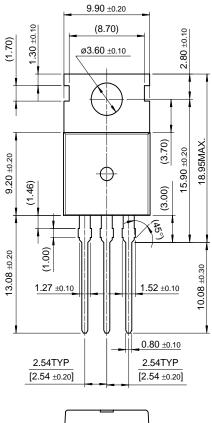


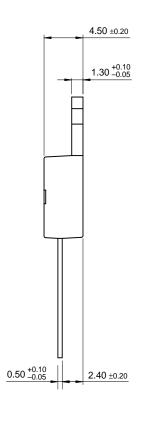
Figure 7. Power Derating

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Package Dimensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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Rev. I2

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