



N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected up to 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

Mechanical Data

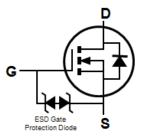
- Package: SOT523
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin Annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

SOT523

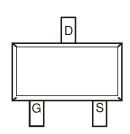








Equivalent Circuit (Note 5)



Top View

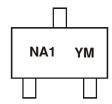
Ordering Information (Note 4)

Part Number	Qualification	Package	Pac	king
Part Number	Quantication	Package	Qty.	Carrier
DMG1012T-7	Commercial	SOT523	3000	Tape & Reel
DMG1012T-13	Commercial	SOT523	10000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/
- 5. The ESD gate protection diode is only designed to protect against ESD events. No gate-source voltage greater than the maximum V_{GSS} rating (given on page 2) can be applied.

Marking Information



NA1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022)M = Month (ex: 1 = January)

Month	lon	Eab	Mor	Anr	Mov	lun	list	Aug	Con	Oot	Nov	Doo
Code	W		J	K	L	М	N	0	Ρ	R	S	Т
Year	2009		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Date Code Key												



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteris	tic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage			V _{GSS}	±6	V
Continuous Drain Current (Note 6) Steady $T_A = +25^{\circ}C$ State $T_A = +85^{\circ}C$			lo	0.63 0.45	А
Pulsed Drain Current			IDM	3	Α

Thermal Characteristics (@ TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	PD	0.28	W	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ heta JA}$	452	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

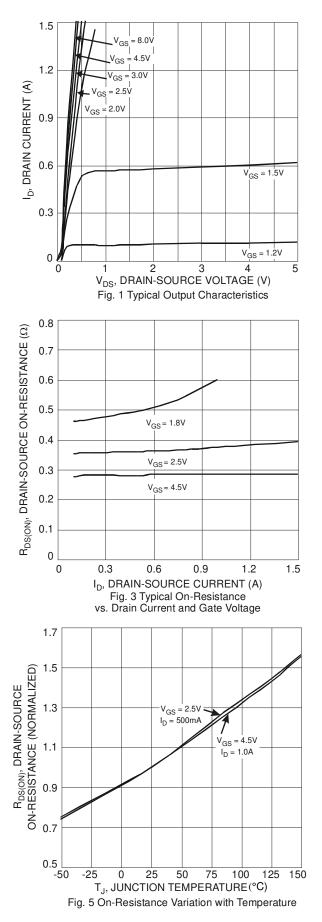
Electrical Characteristics (@ TA = +25°C, unless otherwise specified.)

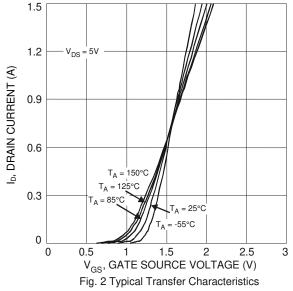
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage		20	_		V	$V_{GS} = 0V$, $I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	1	_	100	nA	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage		1	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	0.3	0.4	Ω	$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}		0.4	0.5		$V_{GS}=2.5V,\ I_D=500mA$	
			0.5	0.7		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y _{fs}	1	1.4		S	V _{DS} = 10V, I _D = 400mA	
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	Vgs = 0V, Is = 150mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	1	60.67	1	pF		
Output Capacitance	Coss	1	9.68	1	pF	V _{DS} = 16V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	l	5.37	1	pF	1 = 1.0101112	
Total Gate Charge	Q_g	1	736.6	1	рC	V 45V V 40V	
Gate-Source Charge	Qgs	1	93.6	1	рC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250 \text{mA}$	
Gate-Drain Charge	Q_{gd}	1	116.6	1	рC	ID = 230IIIA	
Turn-On Delay Time	t _{D(ON)}	-	5.1	-	ns		
Turn-On Rise Time	t _R	-	7.4	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	tD(OFF)		26.7		ns	$R_L = 47\Omega$, $R_G = 10\Omega$, $I_D = 200 \text{mA}$	
Turn-Off Fall Time	tr	_	12.3	_	ns	10 - 200m/t	

Notes:

- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







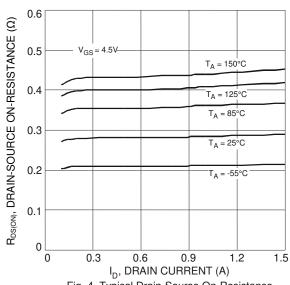


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

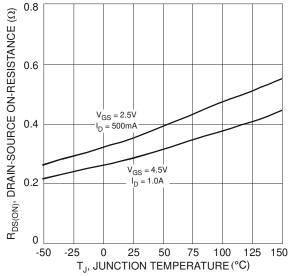


Fig. 6 On-Resistance Variation with Temperature



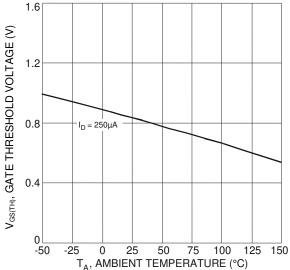
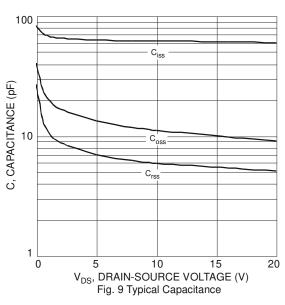


Fig. 7 Gate Threshold Variation vs. Ambient Temperature



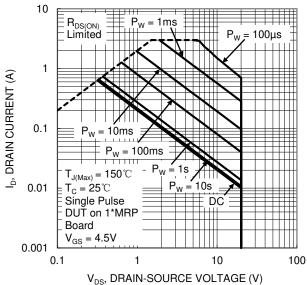
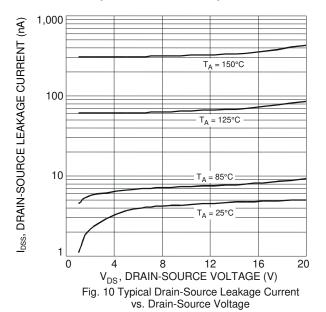


Fig. 11 SOA, Safe Operation Area

10 8 VENDOS 2 0 0.2 0.4 0.6 0.8 1.0 1.2 VSD, SOURCE-DRAIN VOLTAGE (V) Fig. 8 Diode Forward Voltage vs. Current





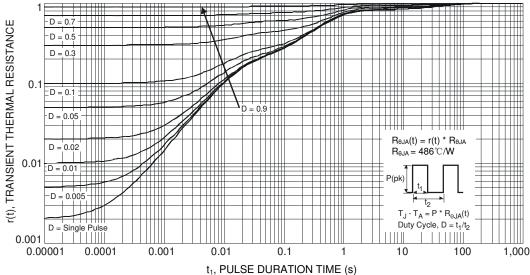


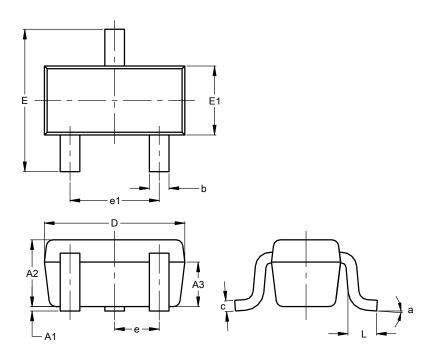
Fig. 12 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

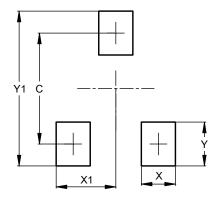


SOT523							
Dim	Min Max Typ						
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10 0.20 0.12						
D	1.50	50 1.70 1.6					
Е	1.45	1.60					
E1	0.75	0.85	0.80				
е	e 0.50 BSC						
e1	0.90 1.10 1.00						
L	0.20	0.40	0.33				
а	0°		8°				
All Dimensions in mm							

Suggested Pad Layout

 $Please\ see\ http://www.diodes.com/package-outlines.html\ for\ the\ latest\ version.$

SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Υ	0.51
Y1	1.80



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