

Features

- Advanced Trench Cell Design
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

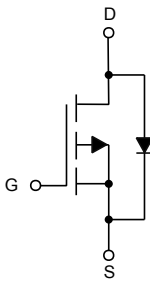
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 2.5°C/W Junction to Case⁽¹⁾

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	±25	V
Continuous Drain Current	I_D	-12	A
Pulsed Drain Current ⁽²⁾	I_{DM}	-48	A
Total Power Dissipation	P_D	50	W
Single Pulsed Avalanche Energy ⁽³⁾	E_{AS}	58	mJ

Note:

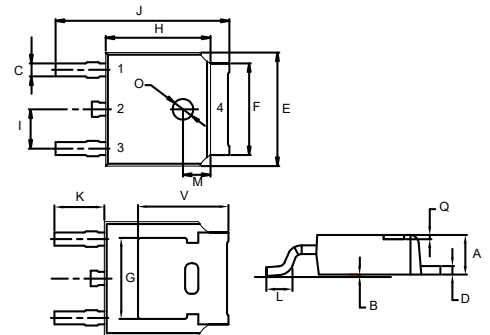
1. Surface Mounted on 1 in² pad area, t≤10 sec.
2. Pulse Test: Pulse Width≤300μs, Duty Cycle ≤2%.
3. T_J=25°C, L=1.0mH, V_{DD}=-50V.

Internal Structure:



P-CHANNEL MOSFET

DPAK(TO-252)



1. Gate
- 2,4. Drain
3. Source

DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.000	0.005	0.00	0.13	
C	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
H	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
O	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-100			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 25V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-80V, V_{GS}=0V$			-1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1		-2	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-5A$		154	185	m Ω
		$V_{GS}=-4.5V, I_D=-3A$		165	215	
Diode Characteristics						
Continuous Body Diode Current	I_S				-12	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=-5A$			-1.3	V
Reverse Recovery Time	t_{rr}	$I_S=-5A, di_f/dt=100A/\mu s$		25		ns
Reverse Recovery Charge	Q_{rr}			32		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=-50V, V_{GS}=0V, f=1MHz$		1460		pF
Output Capacitance	C_{oss}			37		
Reverse Transfer Capacitance	C_{rss}			34		
Total Gate Charge	Q_g	$V_{DS}=-50V, V_{GS}=-10V, I_D=-5A$		26		nC
Gate-Source Charge	Q_{gs}			5.3		
Gate-Drain Charge	Q_{gd}			3.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-50V, V_{GEN}=-10V,$ $R_G=3.9\Omega, R_L=10\Omega,$ $I_{DS}=-5A$		5.9		ns
Turn-On Rise Time	t_r			9.6		
Turn-Off Delay Time	$t_{d(off)}$			104		
Turn-Off Fall Time	t_f			41		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

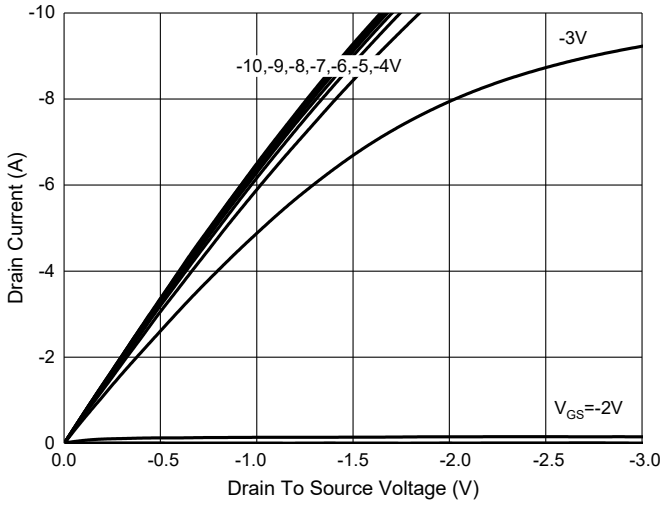


Fig. 2 - $I_S - V_{SD}$

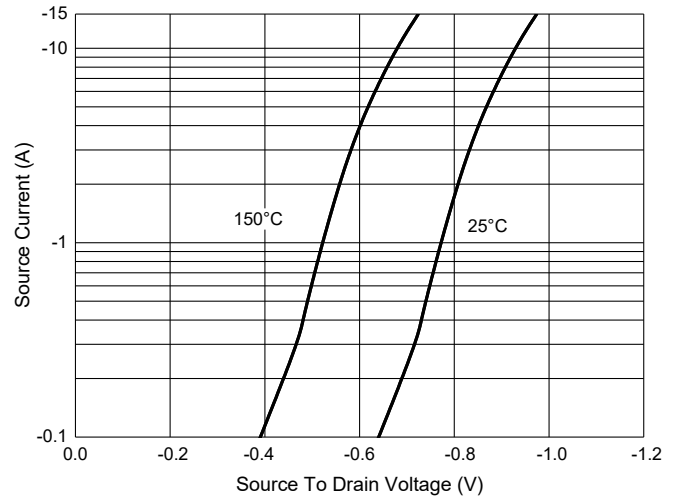


Fig. 3 - $R_{DS(ON)} - I_D$

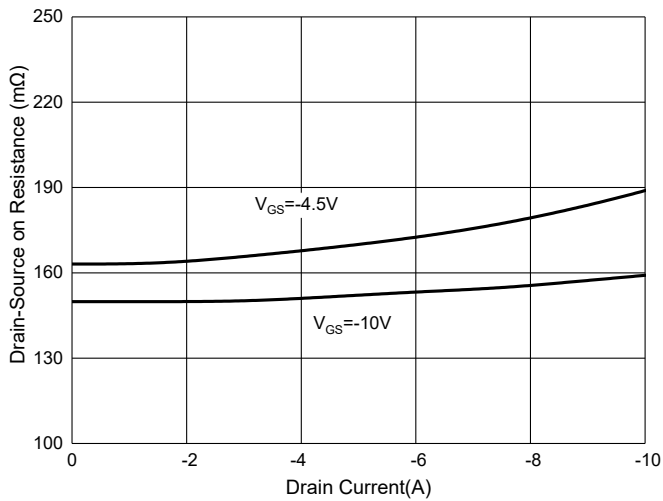


Fig. 4 - Normalized On Resistance Characteristics

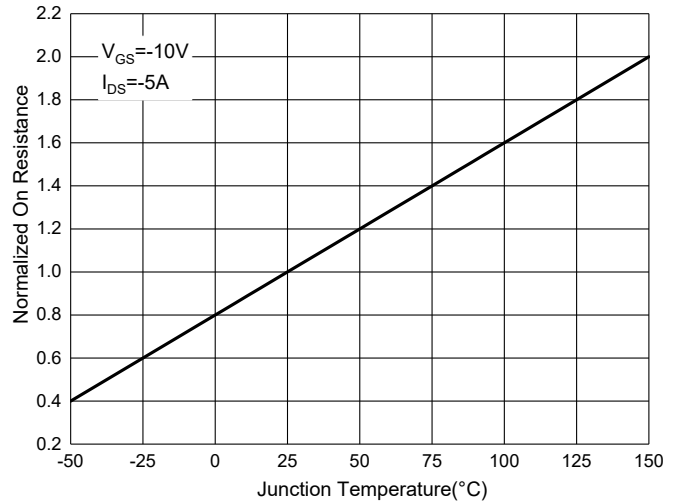


Fig. 5 - Capacitance Characteristics

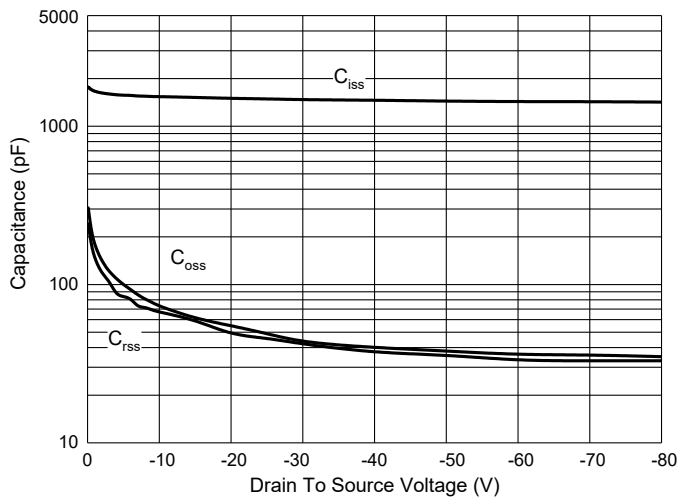
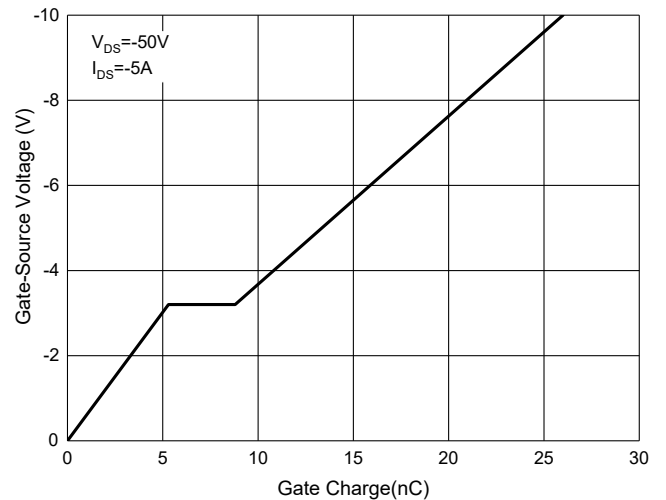


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area

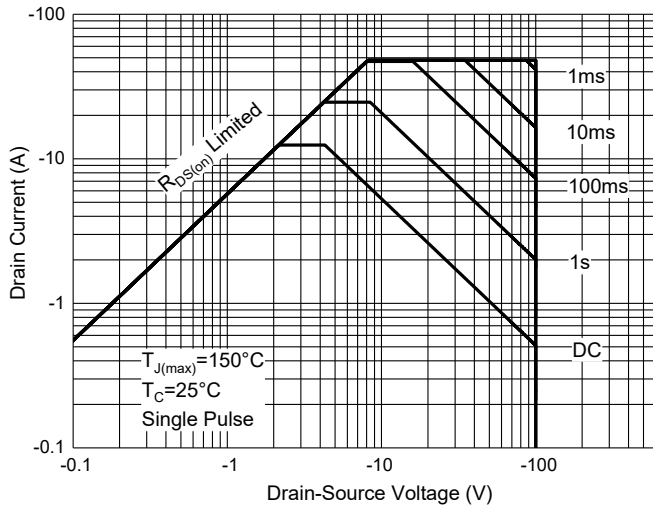
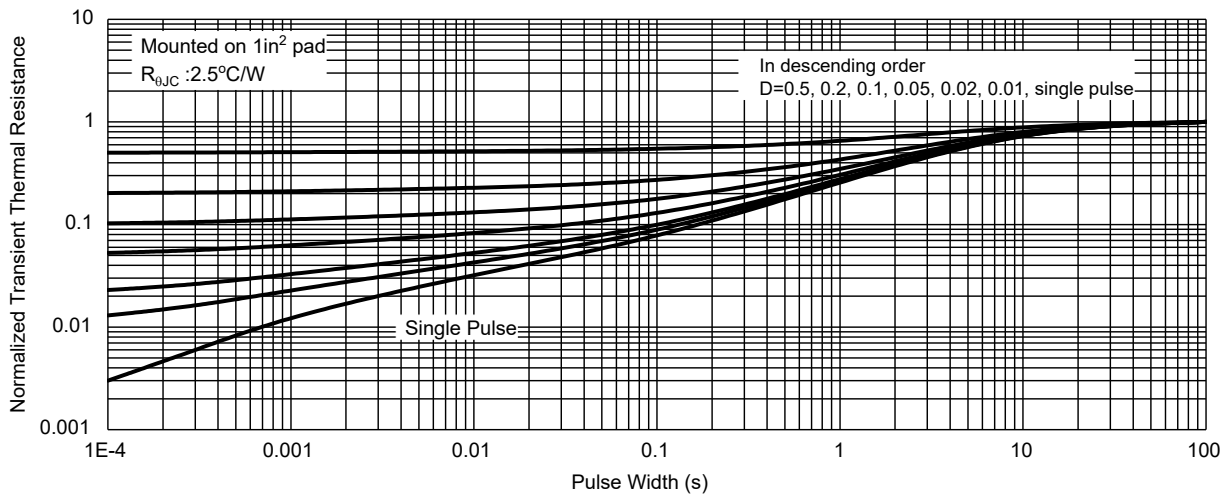


Fig. 8 - Normalized Maximum Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note : Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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