

**Vishay Siliconix** 

# N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)			
30	0.077 at V <sub>GS</sub> = 4.5 V	3.4			
	0.120 at V <sub>GS</sub> = 2.5 V	2.5			

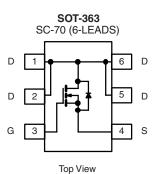
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET: 2.5 V Rated
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

• Load Switch for Portable Applications



Marking Code

Ordering Information: Si1402DH-T1-E3 (Lead (Pb)-free) Si1402DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	<sub>A</sub> = 25 °C, unle	ss otherwise n	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 12		
Continuous Drain Current (T = $150 ^{\circ}\text{C})^{a}$	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	3.4	2.7	А
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		2.7	2.2	
Pulsed Drain Current		I <sub>DM</sub>	8		A
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	1.2	0.8	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.45	0.95	W
	T <sub>A</sub> = 70 °C		0.94	0.6	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \le 5 s$	- R <sub>thJA</sub> R <sub>thJF</sub>	65	85	
Maximum Junction-to-Amblent	Steady State		87	130	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		40	50	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	tions Min.		Max.	Unit	
Static			•	•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ 0.6			1.6	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	= 0 V		1		
		$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C	5 °C			μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 5$ V, $V_{GS}$ = 4.5 V	4			А	
Drain-Source On-State Resistance <sup>a</sup>	P	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.0 \text{ A}$	$V_{GS} = 4.5 \text{ V}, I_D = 3.0 \text{ A}$ 0.064 $V_{GS} = 2.5 \text{ V}, I_D = 2.0 \text{ A}$ 0.095		0.077	Ω	
	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 2.0 \text{ A}$			0.120		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 3.0 \text{ A}$		10		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.05 A, V <sub>GS</sub> = 0 V		0.80	1.1	V	
Dynamic <sup>b</sup>	<u>.</u>						
Total Gate Charge	Qg			3	4.5	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_D$ = 2.0 A		0.6			
Gate-Drain Charge	Q <sub>gd</sub>			1.0			
Gate Resistance	R <sub>g</sub>	f = 1.0 MHz		2.4		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			5	8	-	
Rise Time	t <sub>r</sub>	$\label{eq:V_DD} \begin{array}{l} V_{DD} = 15 \; V, \; R_L = 15 \; \Omega \\ I_D \cong 1 \; A, \; V_GEN = 10 \; V, \; R_g = 6 \; \Omega \end{array}$		12	23		
Turn-Off Delay Time	t <sub>d(off)</sub>			13	23	ns	
Fall Time	t <sub>f</sub>			7	12		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>			15	25		
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 1.05 A, dl/dt = 100 A/μs		7.5	12	nC	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

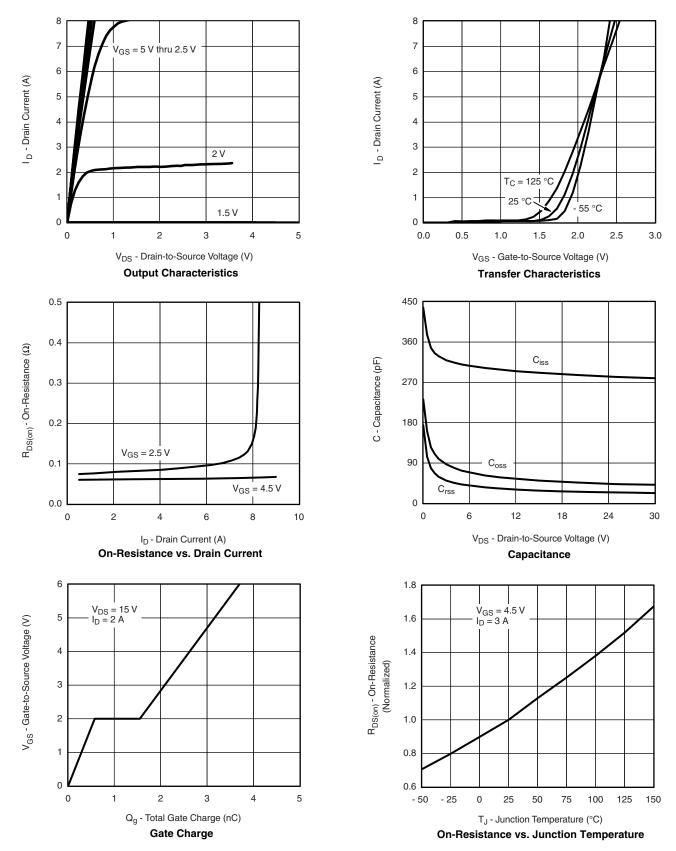
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



### Si1402DH Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



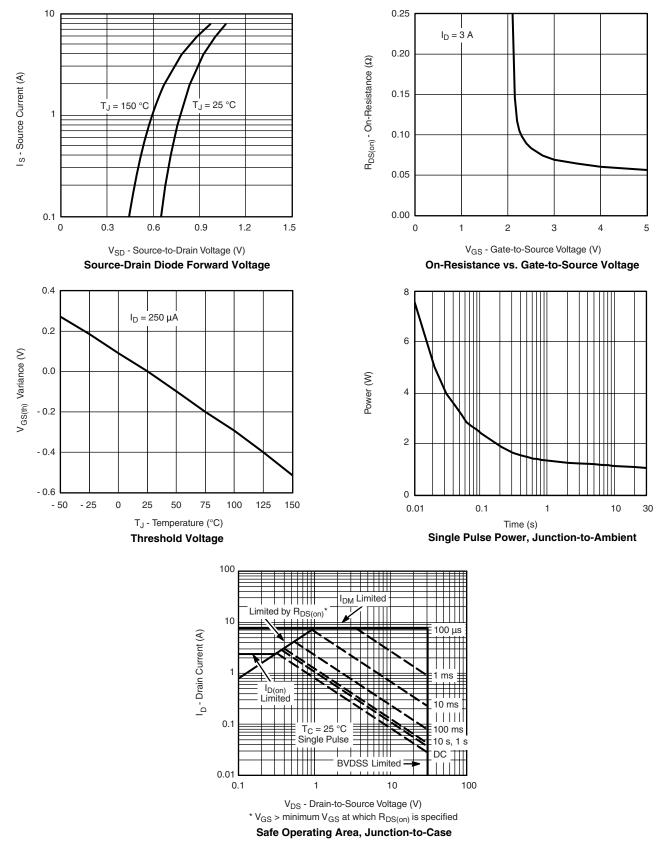
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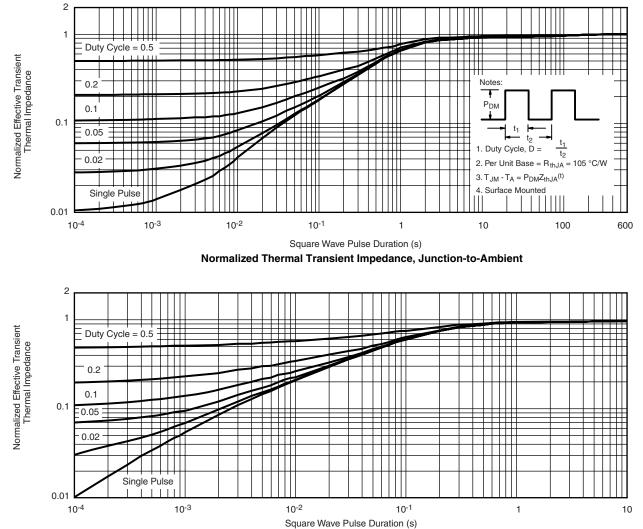




# Si1402DH

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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?73328">www.vishay.com/ppg?73328</a>.



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