





# IF140, IF140A N-Channel JFET

### Features

- InterFET <u>N0014L Geometry</u>
- Low gate leakage: 100fA typical @10V
- Low Ciss: 3pF typical
- Typical noise: 3.0 nV/VHz
- Typical gain: 3.8mS
- Typical cutoff voltage: -3.0V
- Typical loss: 10mA
- Typical BVGss: -35V
- High radiation tolerance
- RoHS, REACH, CMR compliant
- Custom test and binning options available
- SMT, TH, and bare die package options
- Edge case SPICE modeling: InterFET SPICE

### **Industry Standard Crosses**

- IF142, BF512, SST4416, SST4416A, MMBF4416
- MMBF4416A, MMBF5485, SSTJ211

#### **InterFET Similar Parts**

- IFBF512, 2N4416, J304, 2N4303, 2N5485, J211
- SMP4416, SMPJ304, SMP4303, SMP5485, SMPJ211

### **InterFET Dual Parts**

• IFN5911, IFN5912

### **Applications**

- General: Amplifiers; Switches; Voltage regulators; Oscillators; Signal mixers; Noise generators
- Military/Aero: Radar; Communications; Satellites; Missiles guidance; Hydrophone preamplifiers
- Medical: Medical imaging systems; Medical monitors and recorders; Ultrasound equipment
- Audio: Tone control circuits; Headphone amplifiers; Audio filters; Electret Microphone

### Description

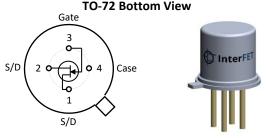
The -20V InterFET IF140 and IF140A are the lowest leakage mid-gain JFET products on the market. Targeted for low noise higher frequencies designs these JFETs are ideal for medical and military applications. Gate leakages are typically 100fA at room temperatures.

Part Number Description Case Pac					
Part Number	Description	Case	Packaging		
IF140T72; IF140AT72	Through-Hole	TO-72	Bulk		
IF140ST3; IF140AST3	Surface Mount	SOT23	Bulk		
	7" Tape and Reel: Max 3,000 Pieces		Minimum 1,000 Pieces		
IF140ST3TR; IF140AST3TR	13" Tape and Reel: Max 9,000 Pieces	SOT23	Tape and Reel		
IF140COT; IF140ACOT	Chip Orientated Tray (COT Waffle Pack)	СОТ	400/Waffle Pack		
IF140CFT; IF140ACFT	Chip Face-up Tray (CFT Waffle Pack)	CFT	400/Waffle Pack		

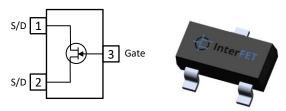
#### Ordering Information Custom Part and Binning Options Available



NOTICE: Please refer to the end of this document for information on product materials, compliance, safety, and legal statements.







NOTE: S/D pins are interchangeable Source Drain connections







Support

# **Electrical Characteristics**

#### Maximum Ratings (@ T<sub>A</sub> = 25°C, Unless otherwise specified)

	Parameters	TO-72	SOT-23	Unit
VRGS	Reverse Gate Source and Gate Drain Voltage	-20	-20	V
IFG	Continuous Forward Gate Current	50	50	mA
PD	Continuous Device Power Dissipation <sup>1</sup>	500	350	mW
Р	Power Derating <sup>1</sup>	3.3	2.8	mW/°C
Τı	Operating Junction Temperature	-65 to 175	-55 to 150	°C
Tstg	Storage Temperature	-65 to 175	-55 to 150	°C

<sup>1</sup> Thermal power dissipation and derating values obtained with gate pin (substrate) thermally connected to pad and/or internal layer.

### Static Characteristics (@ TA = 25°C, Unless otherwise specified)

			IF140		IF140A		
	Parameters	Conditions	Min	Max	Min	Max	Unit
V <sub>(BR)GSS</sub>	Gate to Source Breakdown Voltage	$V_{DS} = 0V$ , $I_G = -1\mu A$	-20		-20		v
IGSS	Gate to Source Reverse Current	V <sub>GS</sub> = -15V, V <sub>DS</sub> = 0V, T <sub>A</sub> = 25°C V <sub>GS</sub> = -15V, V <sub>DS</sub> = 0V, T <sub>A</sub> = 150°C		-0.1 -0.2		-0.1 -0.2	nA nA
V <sub>GS(F)</sub>	Gate to Source Forward Voltage	V <sub>DS</sub> = 0V, I <sub>G</sub> = 1mA		1		1	v
V <sub>GS(OFF)</sub>	Gate to Source Cutoff Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 5nA		-6		-6	V
V <sub>GS</sub>	Gate to Source Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 50μA		-5	-2.5	-6	V
I <sub>DSS</sub>	Drain to Source Saturation Current	$V_{GS} = 0V, V_{DS} = 15V$ (Pulsed)	5	15	5	15	mA

### Dynamic Characteristics (@ TA = 25°C, Unless otherwise specified)

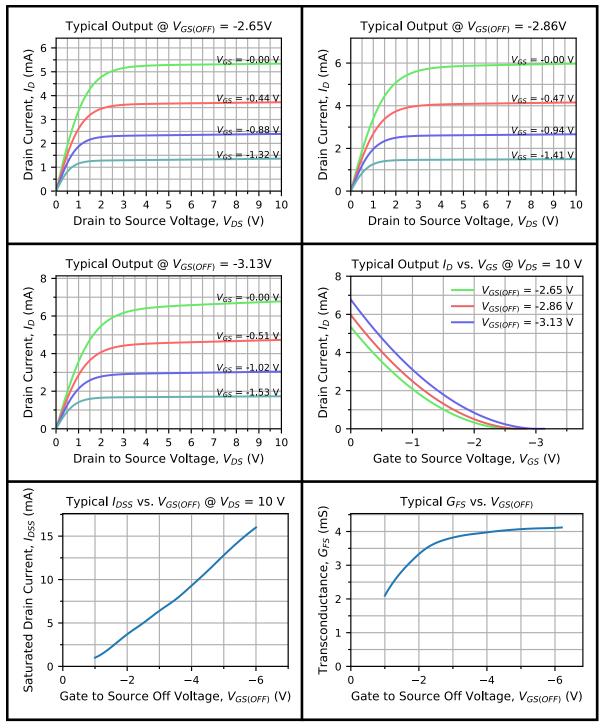
			IF140		IF140A		
	Parameters	Conditions	Min	Max	Min	Max	Unit
GFS	Forward Transconductance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1kHz	3.5		3.5		mS
Gos	Output Conductance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1kHz		0.05		0.05	mS
Ciss	Input Capacitance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz		3		3	pF
Crss	Reverse Transfer Capacitance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz		1.2		1.2	pF
en	Equivalent Circuit Input Noise Voltage	$V_{DS} = 12V, V_{GS} = 0V, f = 10Hz$	3 (t	typ)	3 (1	:yp)	nV/√Hz





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# **Typical IF140 Characteristics**







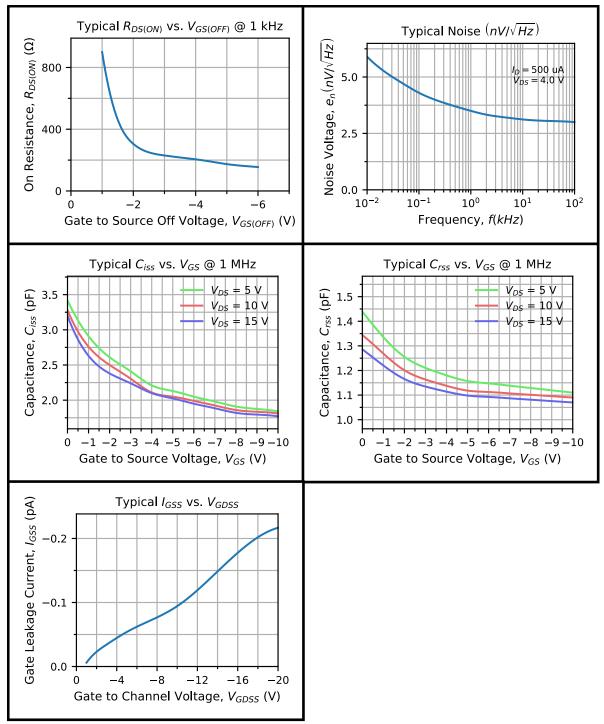
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Technical

Support

# IF140-A

## Typical IF140 Characteristics (Continued)





Technical

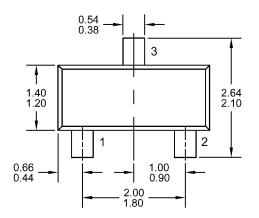
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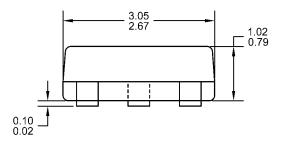
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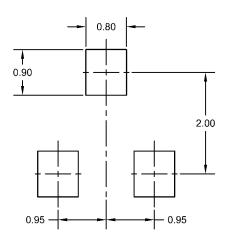
# SOT23 (TO-236AB) Mechanical and Layout Data

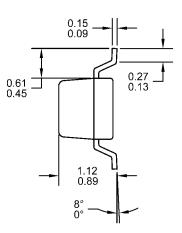
### **Package Outline Data**





### Suggested Pad Layout





- 1. All linear dimensions are in millimeters.
- 2. Package weight approximately 0.12 grams
- 3. Molded plastic case UL 94V-0 rated
- For Tape and Reel specifications refer to InterFET CTC-021 Tape and Reel Specification, Document number: IF39002
- 5. Bulk product is shipped in standard ESD shipping material
- 6. Refer to JEDEC standards for additional information.

- 1. All linear dimensions are in millimeters.
- 2. The suggested land pattern dimensions have been provided for reference only. A more robust pattern may be desired for wave soldering.



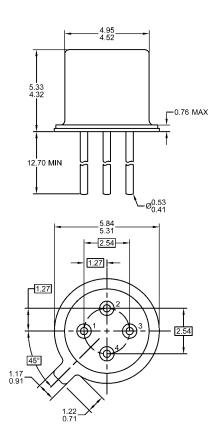


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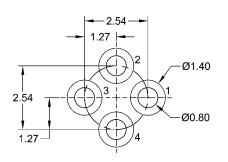
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# **TO-72 Mechanical and Layout Data**

### **Package Outline Data**



### Suggested Through-Hole Layout



- 1. All linear dimensions are in millimeters.
- 2. Four leaded device. Not all leads are shown in drawing views.
- 3. Package weight approximately 0.31 grams
- 4. Bulk product is shipped in standard ESD shipping material
- 5. Refer to JEDEC standards for additional information.

- 1. All linear dimensions are in millimeters.
- 2. The suggested land pattern dimensions have been provided as a straight lead reference only. A more robust pattern may be desired for wave soldering and/or bent lead configurations.







# Compliance and Legal

#### Environment

InterFET parts follow the latest RoHS Compliance, REACH Compliance, Proposition 65 Statement, TSCA Statement, and Chemical Disposal and Waste Mitigation requirement and guidelines. For more on InterFET's Environmental Commitment please visit www.InterFET.com/environmental/.

Support

#### Package materials

Parameters	SOT23	SOIC8	TO-92	Metal Case
Alloy	CDA194	C194 1/2H	C194 1/2H	Kovar
Cu	Balance	97% min	97% min	
Fe	2.1 - 2.6%	2.1 – 2.6%	2.1 - 2.6%	53%
Zn	0.05 - 0.2%	0.05 – 0.2%	0.05 - 0.15%	
Р	0.015 - 0.15%	0.015 - 0.15%	0.015 - 0.15%	
Pb	0.03% max	0.03% max	0.03% max	
Ni				29%
Со				17%
Mn				0.3%
Si				0.2%
С				<0.01%
Au				Plating

#### Package tests

Parameters	SOT23	SOIC8	TO-92	Metal Case
MSL	Level 1	Level 1	N/A	N/A
ESD	Class M4 Machine Model Class 3A HBM			

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