

# FPGA-Modul with Xilinx Artix-7 100T (Variante 72C), 2 x 50 Pin, 3,3V supply

Order number: TE0725LP-01-72C-1



## Product information "FPGA-Modul with Xilinx Artix-7 100T (Variante 72C), 2 x 50 Pin, 3,3V supply"

This module is the replacement of the TE0725LP-01-100-2C. All changes caused by the new revision are included in the Product Change Notification (PCN).

The Trenz Electronic TE0725LP-01-72C-1 is a low cost small-sized FPGA module integrating a Xilinx Artix-7 (15T-100T) and 64 MByte Flash memory for configuration and operation. The 2 x 50 pin headers with a 2.54 mm standard pitch are perfect for breadboard or low cost dual layer PCB.

Trenz HyperBus enabled reference designs are typically bundled with a FREE evaluation edition of the commercially proven, low-cost, low-circuit area, high performance, HyperBus Memory Controller (HBMC) IP supplied by Synaptic Laboratories Ltd.

Synaptic Labs HBMC IP is commercially proven in both Intel and Xilinx projects, and was selected by Intel. This FREE HBMC IP evaluation license never expires, and no customer registration or NIC ID is required. You can check for and obtain the latest version of the FREE evaluation HBMC IP from S/Labs website for Xilinx and Intel. Please send your HBMC IP support questions to info@synaptic-labs.com.

All parts are at least commercial temperature range of 0°C to +70°C. The module operating temperature range depends on customer design and cooling solution. Please contact us for options.

### **Key Features**

- Xilinx Artix-7 XC7A100T-2CSG324C
- Commercial temperature grade (industrial on request)
- 64 MByte Flash memory
- 2 x 50 pin headers with 2.54 mm pitch, ideal for breadboard use
- 3,3 V single supply with on board voltage regulators
- 95 I/O's (42 + 42 + 3 + 8)
- 25 MHz system clock (100 MHz can be customized on request)
- I2C EEPROM
- 7.3 x 3.5 cm form factor
- JTAG/UART connector
- One user LED
- optional HyperRAM (8 bis 32 MByte) or HyperFlash
  HyperRAM from Cypress cypress.com/products/hyperram-memory
  HyperFlash from Cypress cypress.com/products/hyperflash-nor-flash-memory

Other assembly options for cost or performance optimization plus high volume prices available on request.

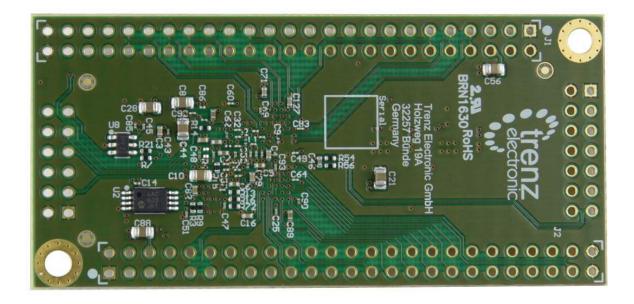
#### Recommended Software

#### Vivado HL WebPACK Edition (free version)

The Vivado Design Suite HL WebPACK Edition is the FREE version of the design suite. Vivado HL WebPACK delivers instant access to some basic Vivado features and functionality at no cost.

Overview of all editions of Vivado Design Suite





https://shop.trenz-electronic.de/en/TE0725LP-01-72C-1-FPGA-Modul-with-Xilinx-Artix-7-100T-Variante-72C-2-x-50-Pin-3-3V-supply?path=Trenz\_Electronic/Modules\_and\_Module\_Carriers/3.5x7.3/TE0725LP/REV01/Documents/6-12-19