

EN

C1+ 2

V+13

 $C1-\Pi 4$

C2+ 15

C2-[6

V-**[**7

DOUT2 8

DB, DW, OR PW PACKAGE

(TOP VIEW)

FEATURES

- RS-232 Bus-Pin ESD Protection Exceeds ±15 kV Using Human-Body Model (HBM)
- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates With 3-V to 5.5-V V_{CC} Supply
- Operates up to 250 kbit/s
- Two Drivers and Two Receivers
- Low Standby Current . . . 1 µA Typical
- External Capacitors . . . $4 \times 0.1 \ \mu F$
- Accepts 5-V Logic Input With 3.3-V Supply
- Alternative High-Speed Pin-Compatible Device (1 Mbit/s)
 - TRSF3223

APPLICATIONS

- Battery-Powered Systems
- PDAs
- Notebooks
- Laptops
- Palmtop PCs
- Hand-Held Equipment

DESCRIPTION/ORDERING INFORMATION

The TRS3223 consists of two line drivers, two line receivers, and a dual charge-pump circuit with \pm 15-kV ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between an asynchronous communication controller and the serial-port connector. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. The device operates at data signaling rates up to 250 kbit/s and a maximum of 30-V/µs driver output slew rate.

Flexible control options for power management are available when the serial port is inactive. The auto-powerdown feature functions when FORCEON is low and FORCEOFF is high. During this mode of operation, if the device does not sense a valid RS-232 signal, the driver outputs are disabled. If FORCEOFF is set low and \overline{EN} is high, both drivers and receivers are shut off, and the supply current is reduced to 1 μ A. Disconnecting the serial port or turning off the peripheral drivers causes auto-powerdown to occur. Auto-powerdown can be disabled when FORCEON and FORCEOFF are high. With auto-powerdown enabled, the device is activated automatically when a valid signal is applied to any receiver input. The INVALID output is used to notify the user if an RS-232 signal is present at any receiver input. INVALID is high (valid data) if any receiver input voltage is greater than 2.7 V or less than -2.7 V, or has been between -0.3 V and 0.3 V for less than 30 μ s. INVALID is low (invalid data) if the receiver input voltage is between -0.3 V and 0.3 V for more than 30 μ s. Refer to Figure 4 for receiver input levels.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

RIN2[9 ROUT2[10	12] DIN2 11] INVALID	

20 FORCEOFF

19 V_{CC}

18 GND

16 **RIN1**

13 DIN1

17 DOUT1

15 ROUT1

14 FORCEON



DESCRIPTION/ORDERING INFORMATION (CONTINUED)

T _A	PAC	(AGE ⁽¹⁾⁽²⁾	ORDERABLE PART NUMBER	TOP-SIDE MARKING				
		Tube of 25	TRS3223CDW	TDC2000C				
	SOIC – DW	Reel of 2000	TRS3223CDWR	- TRS3223C				
−0°C to 70°C	SSOP – DB	Tube of 70	TRS3223CDB	Deade				
	2206 – DR	Reel of 2000	TRS3223CDBR	- RS23C				
		Tube of 70	TRS3223CPW	RS23C				
	TSSOP – PW	Reel of 2000	TRS3223CPWR	- K523C				
		Tube of 25	TRS3223IDW	TDC20001				
	SOIC – DW	SSOP – DB	TRS3223IDWR	- TRS3223I				
4000 to 0500		Tube of 70	TRS3223IDB	DC001				
–40°C to 85°C	SSOP – DB	Reel of 2000	TRS3223IDBR	- RS23I				
		Tube of 70	TRS3223IPW	DC001				
	TSSOP – PW	Reel of 2000	TRS3223IPWR	- RS23I				

ORDERING INFORMATION

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available atwww.ti.com/sc/package.

(2) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

FUNCTION TABLES

Each Driver⁽¹⁾

	I	NPUTS		OUTPUT	
DIN	FORCEON	FORCEOFF	VALID RIN RS-232 LEVEL	DOUT	DRIVER STATUS
Х	Х	L	Х	Z	Powered off
L	Н	Н	Х	Н	Normal operation with
н	Н	Н	Х	L	auto-powerdown disabled
L	L	Н	Yes	Н	Normal operation with
н	L	Н	Yes	L	auto-powerdown enabled
L	L	Н	No	Z	Powered off by
Н	L	Н	No	Z	auto-powerdown feature

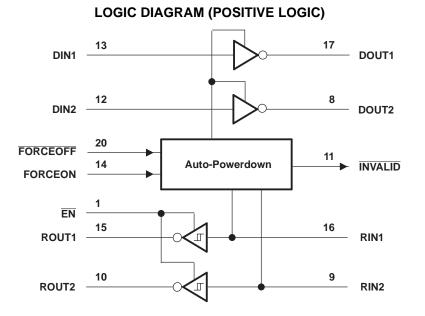
(1) H = high level, L = low level, X = irrelevant, Z = high impedance

Each Receiver⁽¹⁾

	INPUTS		OUTPUT
RIN	EN	VALID RIN RS-232 LEVEL	OUTPUT ROUT
L	L	Х	Н
н	L	х	L
х	Н	х	Z
Open	L	No	Н

 H = high level, L = low level, X = irrelevant, Z = high impedance (off), Open = input disconnected or connected driver off

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Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V _{CC}	Supply voltage range ⁽²⁾		-0.3	6	V
V+	Positive output supply voltage range	Positive output supply voltage range ⁽²⁾		7	V
V–	Negative output supply voltage rang	e ⁽²⁾	0.3	-7	V
V+ - V-	Supply voltage difference ⁽²⁾			13	V
<i>\</i> /		Driver, FORCEOFF, FORCEON, EN	-0.3	6	V
/I Input voltage range	Input voltage range	Receiver	-25	25	
M		Driver	-13.2	13.2	V
Vo	Output voltage range	Receiver, INVALID	-0.3	V _{CC} + 0.3	v
		DB package		70	
θ_{JA}	Package thermal impedance ⁽³⁾⁽⁴⁾	DW package		58	°C/W
		PW package		83	
TJ	Operating virtual junction temperatur	e		150	°C
T _{stg}	Storage temperature range		-65	150	°C

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to network GND.

(3) Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.

(4) The package thermal impedance is calculated in accordance with JESD 51-7.

TRS3223 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER WITH ±15-kV ESD PROTECTION

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Recommended Operating Conditions⁽¹⁾

See Figure 6

				MIN	NOM	MAX	UNIT
	Supply voltage	$V_{CC} = 3.3 V$		3	3.3	3.6	V
	Supply voltage	ge $V_{CC} = 3.3 \text{ V}$ V ontrol high-level input voltage DIN, EN, PWRDOWN, FORCEON V ontrol low-level input voltage DIN, EN, PWRDOWN, FORCEON V ontrol input voltage DIN, EN, PWRDOWN, FORCEON V out voltage DIN, EN, PWRDOWN, FORCEON V out voltage DIN, EN, PWRDOWN, FORCEON V out voltage DIN, EN, PWRDOWN, FORCEON V	4.5	5	5.5	v	
VIH	Driver and control high lovel input veltage	DIN, EN, PWRDOWN,	$V_{CC} = 3.3 V$	2			V
VН	Driver and control high-level input voltage	FORCEON V _C	$V_{CC} = 5 V$	2.4			v
V_{IL}	Driver and control low-level input voltage	DIN, EN, PWRDOWN, FOR	RCEON			0.8	V
V	Driver and control input voltage	DIN, EN, PWRDOWN, FOR	RCEON	0		5.5	V
VI	Receiver input voltage			-25		25	V
т	Operating free air temperature	TRS223C		0		70	°C
T _A	Operating nee-an temperature	TRS223I		-40		85	C

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V \pm 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V \pm 0.5 V.

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

	PARA	METER	TEST	CONDITIONS	MIN TYP ⁽²⁾	MAX	UNIT
I _I	Input leakage current	EN, FORCEOFF, FORCEON			±0.01	±1	μA
		No load, FORCEOFF and FORCEON at V_{CC}	0.3	1	mA		
Icc	Supply current	Powered off $V_{co} = 3.3 V \text{ or } 5 V$	No load, FORCEOFF at GND	1	10		
		Auto-powerdown enabled	T _A = 25°C	No load, FORCEOFF at V _{CC} , FORCEON at GND, All RIN are open or grounded	1	10	μA

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

DRIVER SECTION

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

	PARAMETER	TE	ST CONDITIONS	S	MIN	TYP ⁽²⁾	MAX	UNIT
V _{OH}	High-level output voltage	DOUT at $R_L = 3 k\Omega$ to GN	D,		5	5.4		V
V _{OL}	Low-level output voltage	DOUT at $R_L = 3 k\Omega$ to GN	OUT at $R_L = 3 \text{ k}\Omega$ to GND,			-5.4		V
I _{IH}	High-level input current	$V_I = V_{CC}$				±0.01	±1	μA
I_{IL}	Low-level input current	V _I at GND				±0.01	±1	μA
	Short-circuit output	V _{CC} = 3.6 V,	$V_{O} = 0 V$			±35	±60	mA
IOS	current ⁽³⁾	V _{CC} = 5.5 V,	$V_{O} = 0 V$			±35	±60	ША
r _o	Output resistance	V_{CC} , V+, and V– = 0 V,	$V_0 = \pm 2 V$		300	10 M		Ω
	Output lookage ourrent	FORCEOFF = GND	$V_{O} = \pm 12 V$,	V_{CC} = 3 V to 3.6 V			±25	
loff	Output leakage current	FORGEOFF = GND	$V_O = \pm 10 V$,	V_{CC} = 4.5 V to 5.5 V			±25	μA

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C. (3) Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

Switching Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

	PARAMETER	TEST CONDITIONS		MIN	TYP ⁽²⁾	MAX	UNIT
	Maximum data rate	C _L = 1000 pF, One DOUT switching,	$R_L = 3 k\Omega$, See Figure 1	250			kbit/s
t _{sk(p)}	Pulse skew ⁽³⁾	$C_L = 150 \text{ pF to } 2500 \text{ pF},$ See Figure 2	$R_L = 3 \ k\Omega \text{ to } 7 \ k\Omega,$		100		ns
SD(tr)	Slew rate, transition region	V _{CC} = 3.3 V	C _L = 150 pF to 1000 pF	6		30	\//uo
SR(tr)	(see Figure 1)	$R_L = 3 k\Omega$ to 7 k Ω	C _L = 150 pF to 2500 pF	4		30	V/µs

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V.

(2) All typical values are at $V_{CC} = 3.3$ V or $V_{CC} = 5$ V, and $T_A = 25^{\circ}$ C. (3) Pulse skew is defined as $|t_{PLH} - t_{PHL}|$ of each channel of the same device.

TRS3223 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER WITH ±15-kV ESD PROTECTION

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RECEIVER SECTION

Electrical Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

	PARAMETER	TEST CONDITIONS	MIN	TYP ⁽²⁾	MAX	UNIT
V _{OH}	High-level output voltage	$I_{OH} = -1 \text{ mA}$	V _{CC} – 0.6	V _{CC} – 0.1		V
V _{OL}	Low-level output voltage	I _{OH} = 1.6 mA			0.4	V
V	Positive-going input threshold voltage	$V_{CC} = 3.3 V$		1.6	2.4	V
V _{IT+}	roslive-going input theshold voltage	$V_{CC} = 5 V$		1.9	2.4	v
V	Negative going input threshold voltage	$V_{CC} = 3.3 V$	0.6	1.1		V
V _{IT}	Negative-going input threshold voltage	$V_{CC} = 5 V$	0.8	1.4		v
V _{hys}	Input hysteresis (V _{IT+} – V _{IT-})			0.5		V
I _{off}	Output leakage current	$\overline{EN} = V_{CC}$		±0.05	±10	μA
r _l	Input resistance	$V_1 = \pm 3 V$ to $\pm 25 V$	3	5	7	kΩ

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

Switching Characteristics⁽¹⁾

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST C	ONDITIONS	MIN TYP ⁽²⁾	МАХ	UNIT
t _{PLH}	Propagation delay time, low- to high-level output	$C_{L} = 150 \text{ pF},$	See Figure 3	150		ns
t _{PHL}	Propagation delay time, high- to low-level output	C _L = 150 pF,	See Figure 3	150		ns
t _{en}	Output enable time	$C_L = 150 \text{ pF},$ See Figure 4	$R_L = 3 k\Omega$,	200		ns
t _{dis}	Output disable time	$C_L = 150 \text{ pF},$ See Figure 4	$R_L = 3 k\Omega$,	200		ns
t _{sk(p)}	Pulse skew ⁽³⁾	See Figure 3		50		ns

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 3.3 V ± 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C. (3) Pulse skew is defined as $|t_{PLH} - t_{PHL}|$ of each channel of the same device.

AUTO-POWERDOWN SECTION

Electrical Characteristics

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

	PARAMETER	TEST CONDITIONS		MIN	MAX	UNIT
V _{T+(valid)}	Receiver input threshold for INVALID high-level output voltage	FORCEON = GND,	$\overline{FORCEOFF} = V_{CC}$		2.7	V
V _{T-(valid)}	Receiver input threshold for INVALID high-level output voltage	FORCEON = GND,	$\overline{FORCEOFF} = V_{CC}$	-2.7		V
V _{T(invalid)}	Receiver input threshold for INVALID low-level output voltage	FORCEON = GND,	$\overline{FORCEOFF} = V_{CC}$	-0.3	0.3	V
V _{OH}	INVALID high-level output voltage	$I_{OH} = -1 \text{ mA},$ FORCEOFF = V _{CC}	FORCEON = GND,	V _{CC} – 0.6		V
V _{OL}	INVALID low-level output voltage	$I_{OH} = 1.6 \text{ mA},$ FORCEOFF = V _{CC}	FORCEON = GND,		0.4	V

Switching Characteristics

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

	PARAMETER	TYP ⁽¹⁾	UNIT
t _{valid}	Propagation delay time, low- to high-level output	1	μs
t _{invalid}	Propagation delay time, high- to low-level output	30	μs
t _{en}	Supply enable time	100	μs

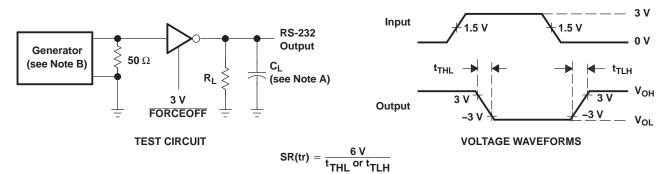
(1) All typical values are at V_{CC} = 3.3 V or V_{CC} = 5 V, and T_A = 25°C.

TRS3223 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER WITH $\pm 15\text{-kV}$ ESD PROTECTION



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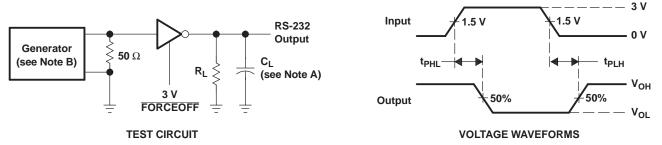
PARAMETER MEASUREMENT INFORMATION



A. C_L includes probe and jig capacitance.

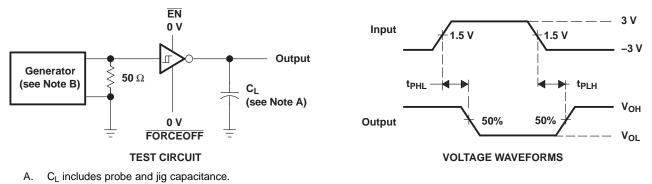
B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z_0 = 50 Ω , 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 1. Driver Slew Rate



- A. C_L includes probe and jig capacitance.
- B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z_0 = 50 Ω , 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.



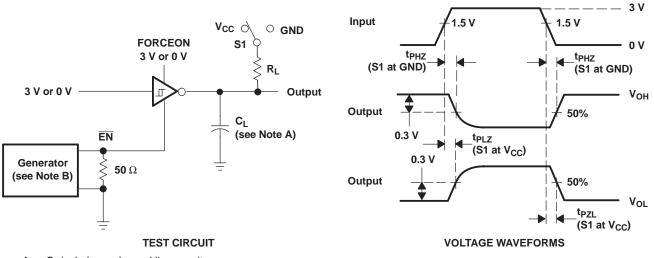


B. The pulse generator has the following characteristics: $Z_0 = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns. $t_f \le 10$ ns.

Figure 3. Receiver Propagation Delay Times

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PARAMETER MEASUREMENT INFORMATION (continued)



A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: Z_O = 50 Ω , 50% duty cycle, $t_r \le 10$ ns. $t_f \le 10$ ns.

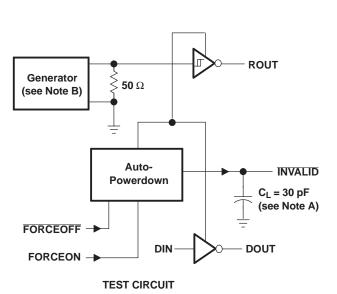
Figure 4. Receiver Enable and Disable Times

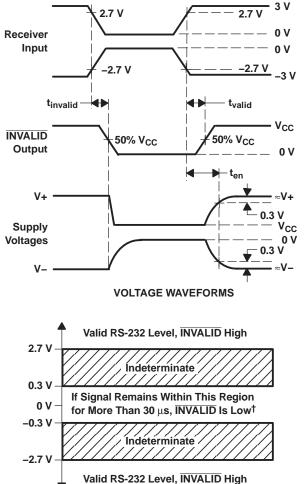
TRS3223 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER WITH $\pm 15\text{-kV}$ ESD PROTECTION

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PARAMETER MEASUREMENT INFORMATION (continued)





[†] Auto-powerdown disables drivers and reduces supply current to 1 μA.

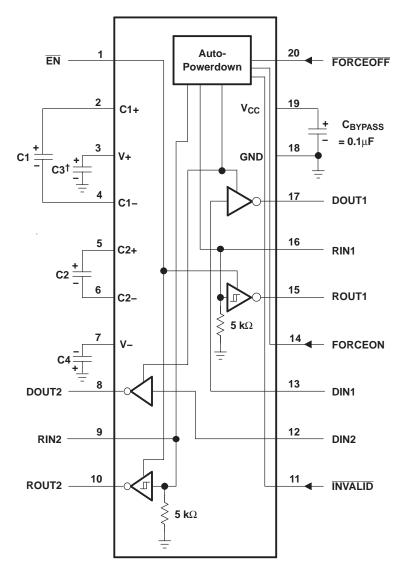
- A. C_L includes probe and jig capacitance.
- B. The pulse generator has the following characteristics: PRR = 5 kbit/s, Z_0 = 50 Ω , 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 5. INVALID Propagation Delay Times and Supply Enabling Time

TRS3223 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER WITH ±15-kV ESD PROTECTION

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APPLICATION INFORMATION



[†] C3 can be connected to V_{CC} or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

V _{CC}	C1	C2, C3, C4	
$\begin{array}{c} \textbf{3.3 V} \pm \textbf{0.3 V} \\ \textbf{5 V} \pm \textbf{0.5 V} \\ \textbf{3 V to 5.5 V} \end{array}$	0.1 μF 0.047 μF 0.1 μF	0.1 μF 0.33 μF 0.47 μF	

V_{CC} vs CAPACITOR VALUES

Figure 6. Typica	l Operating	Circuit and	Capacitor V	alues
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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TRS3223CDBR	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Contact TI Distributor or Sales Office
TRS3223CDBRG4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Contact TI Distributor or Sales Office

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF TRS3223 :

Automotive: TRS3223-Q1

PACKAGE OPTION ADDENDUM



www.ti.com

28-Aug-2010

• Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

PACKAGE MATERIALS INFORMATION

P1

(mm)

12.0

K0 (mm)

2.5

w

(mm)

16.0

Pin1

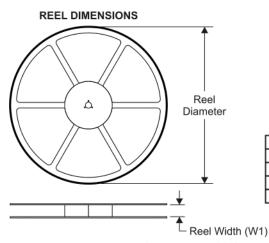
Quadrant

Q1

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Texas Instruments

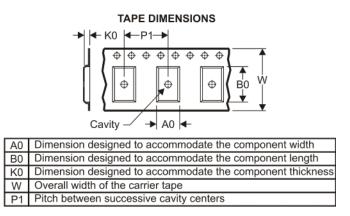
TAPE AND REEL INFORMATION



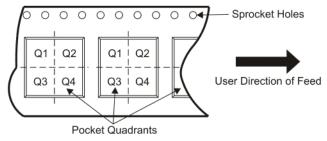
TRS3223CDBR

SSOP

DB



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



330.0

16.4

8.2

7.5

All dimensions are nominal								
Device	•	Package Drawing		Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	

20

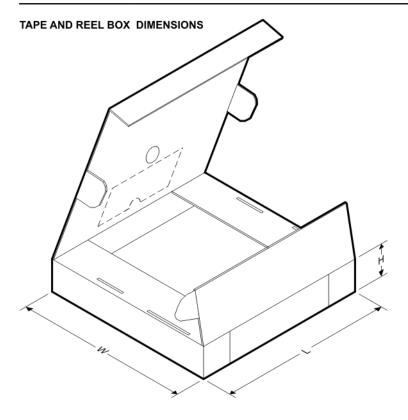
2000

TEXAS INSTRUMENTS

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PACKAGE MATERIALS INFORMATION

23-Jul-2010



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TRS3223CDBR	SSOP	DB	20	2000	346.0	346.0	33.0

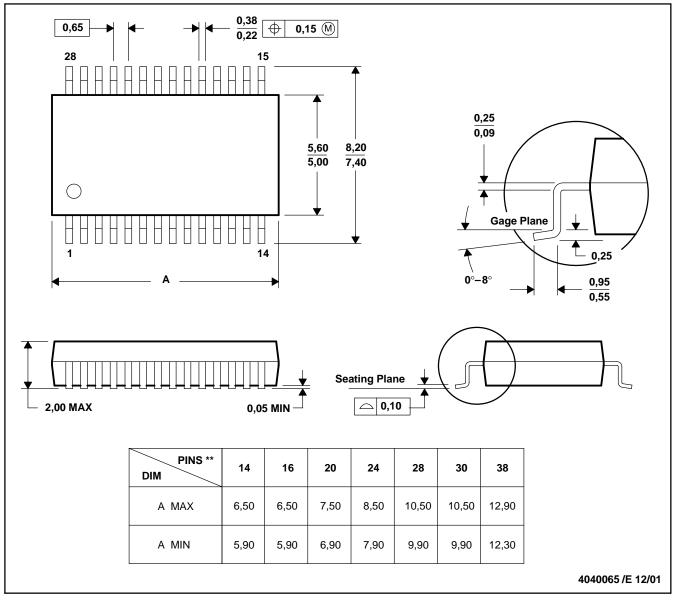
MECHANICAL DATA

MSSO002E - JANUARY 1995 - REVISED DECEMBER 2001

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- D. Falls within JEDEC MO-150



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