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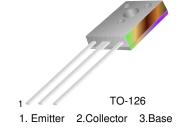


July 2011

BD433/435/437 NPN Epitaxial Silicon Transistor

Features

- · Medium Power Linear and Switching Applications
- · Complement to BD434, BD436 and BD438 respectively



Ordering Information

Part Number	Marking	Package	Packing Method	Remarks
BD433S	BD433	TO-126	BULK	
BD435S	BD435	TO-126	BULK	
BD435STU	BD435	TO-126	RAIL	
BD437S	BD437	TO-126	BULK	

^{*} The suffix "S" of FSID denotes TO126 package.

Absolute Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage			
	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{CES}	Collector-Emitter Voltage			
	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{CEO}	Collector-Emitter Voltage			
323	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current (DC)	4	Α	
I _{CP}	*Collector Current (Pulse)	7	А	
I _B	Base Current	1	Α	
P _C	Collector Dissipation (T _C = 25°C)	36	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 to 150	°C	

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage : BD433 : BD435 : BD437	I _C = 100mA, I _B = 0	22 32 45			V V V
I _{CBO}	Collector Cut-off Current : BD433 : BD435 : BD437	$V_{CB} = 22V, I_{E} = 0$ $V_{CB} = 32V, I_{E} = 0$ $V_{CB} = 45V, I_{E} = 0$			100 100 100	μ Α μ Α μ Α
I _{CEO}	Collector Cut-off Current : BD433 : BD435 : BD437	$V_{CE} = 22V, V_{BE} = 0$ $V_{CE} = 32V, V_{BE} = 0$ $V_{CE} = 45V, V_{BE} = 0$			100 100 100	μ Α μ Α μ Α
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1	mA
h _{FE}	* DC Current Gain : BD433/435 : BD437 : ALL DEVICE : BD433/435 : BD437	$V_{CE} = 5V, I_{C} = 10mA$ $V_{CE} = 1V, I_{C} = 500mA$ $V_{CE} = 1V, I_{C} = 2A$	40 30 85 50 40	130 130 140		
V _{CE} (sat)	* Collector-Emitter Saturation Voltage : BD433 : BD435 : BD437	$I_C = 2A$, $I_B = 0.2A$		0.2 0.2 0.2	0.5 0.5 0.6	V V
V _{BE} (on)	* Base-Emitter ON Voltage : BD433 : BD435 : BD437	V _{CE} = 1V, I _C = 2A			1.1 1.1 1.2	V V V
f _T	Current Gain Bandwidth Product	$V_{CE} = 1V, I_{C} = 250mA$	3			MHz

^{*} Pulse Test: PW≤300μs, duty Cycle≤1.5% Pulsed

Typical Performance Characteristics

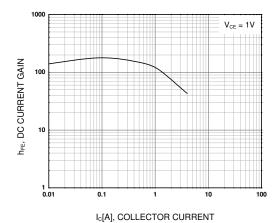


Figure 1. DC current Gain

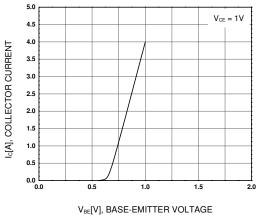


Figure 3. Base-Emitter On Voltage

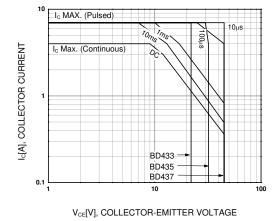


Figure 5. Safe Operating Area

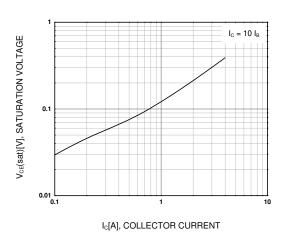


Figure 2. Collector-Emitter Saturation Voltage

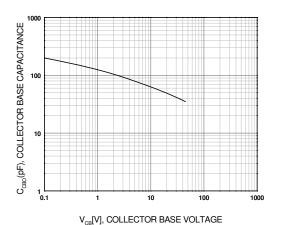


Figure 4. Collector-Base Capacitance

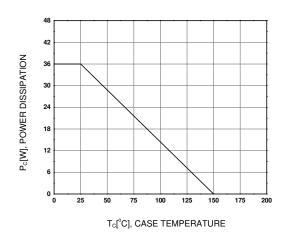
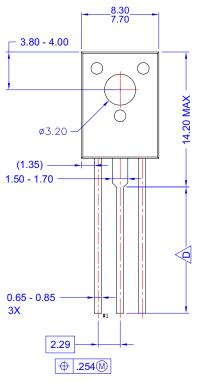
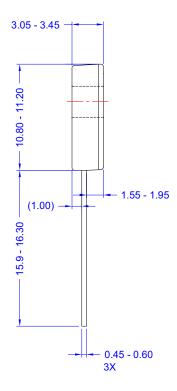


Figure 6. Power Derating

Physical Dimension

TO-126







PRODUCTION CODE	TERMINAL LENGTH "D"
TSSTU	3.45-4.05
TSTU	2.36-2.96
NONE (STD LENGTH)	12.76-13.36

NOTES:

- A) THIS PACKAGE DOES NOT COMPLY TO ANY CURENT PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH,
- AND TIE BAR EXTRUSIONS.
- FOR TERMINAL LENGTH SEE TABLE
- E) DRAWING FILE NAME AND REVISION: MKT-TO126AArev1

Dimensions in Millimeters





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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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Rev. I54

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