

# EMIPAK 1B PressFit Power Module 1200 V AC Line Input Rectification, Flexible Configuration, 20 A

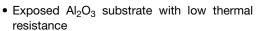


EMIPAK 1B (package example)

PRIMARY CHARACTERISTICS			
D1 - D12			
V <sub>RRM</sub>	1200 V		
V <sub>FM</sub> typical at 20 A	1.29 V		
I <sub>O</sub> at T <sub>SINK</sub> = 97 °C	20 A		
Package	EMIPAK 1B		
Circuit configuration	6 x independent diodes legs fo AC line input rectification		
Type	Modules - diode, high voltage		

#### **FEATURES**

• MOAT standard recovery diode





- Very low forward voltage drop
- Low internal inductances
- · Qualified using AQG324 guideline as reference
- PressFit pins locking technology PATENT(S): <a href="www.vishav.com/patents">www.vishav.com/patents</a>
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The EMIPAK 1B package is easy to use thanks to the PressFit pins. The exposed substrate provides improved thermal performance.

The optimized layout also helps to minimize stray parameters, allowing for better EMI performance.

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Operating junction temperature	TJ		150	°C	
Storage temperature range	T <sub>Stg</sub>		-40 to +150	C	
RMS isolation voltage	V <sub>ISOL</sub>	$T_J = 25$ °C, all terminals shorted, $f = 50$ Hz, $t = 1$ s	3500	V	
D1 - D12					
Maximum DC output current		T <sub>SINK</sub> = 25 °C	33	^	
	I <sub>F(AV)</sub>	T <sub>SINK</sub> = 80 °C	23	Α	
Power dissipation	P <sub>D</sub>	T <sub>SINK</sub> = 25 °C	87	W	
		T <sub>SINK</sub> = 80 °C	49		
Maximum peak one cycle forward non-repetitive	I <sub>FSM</sub>	10 ms sine or 6 ms rectangular pulse, T <sub>J</sub> = 150 °C, no voltage reapplied	230	Α	
surge current		8.3 ms sine, T <sub>J</sub> = 150 °C, no voltage reapplied	241	Α	
	I <sup>2</sup> t	No voltage reapplied, t = 10 ms	264	42-	
Maximum I <sup>2</sup> t capability for fusing		No voltage reapplied, t = 8.3 ms	241	A <sup>2</sup> s	
Maximum I <sup>2</sup> √t capability for fusing	I <sup>2</sup> √t	t = 0.1 ms to 10 ms, no voltage reapplied	2647	A²√s	
Repetitive peak reverse voltage	$V_{RRM}$		1200	V	
Low level value of threshold voltage	V <sub>F(TO)1</sub>	$(16.7 \% \text{ x } I_{F(AV)} < I < \text{x } I_{F(AV)}), T_J = T_J \text{ maximum}$	0.92	V	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > x   I_{F(AV)}), T_J = T_J maximum$	1.10	V	
Low level value of forward slope resistance	r <sub>f1</sub>	$(16.7 \% \text{ x } I_{F(AV)} < I < \text{x } I_{F(AV)}), T_J = T_J \text{ maximum}$	51.3	m.C	
High level value of forward slope resistance	r <sub>f2</sub>	$r_{f2}$ (I > x $I_{F(AV)}$ ), $T_J = T_J$ maximum		mΩ	

PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.



<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
D1 - D12						
Forward voltage drop V <sub>FM</sub>	V	I <sub>F</sub> = 20 A	-	1.29	1.90	V
	I <sub>F</sub> = 20 A, T <sub>J</sub> = 150 °C	-	1.26	-	V	
Breakdown voltage	$V_{BR}$	I <sub>R</sub> = 500 μA	1200	-	-	V
Reverse leakage current I <sub>RM</sub>	,	V <sub>R</sub> = 1200 V	-	1.0	100	
	V <sub>R</sub> = 1200 V, T <sub>J</sub> = 150 °C	-	900	-	μA	

INTERNAL NTC - THERMISTOR SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUE		UNITS	
Resistance	R <sub>25</sub>	T <sub>C</sub> = 25 °C	5000	0	
Resistance	R <sub>100</sub>	T <sub>C</sub> = 100 °C	493 ± 5 %	Ω	
B-value	B <sub>25/50</sub>	$R_2 = R_{25} \text{ exp. } [B_{25/50}(1/T2 - 1/(298.15K))]$	3375 ± 5 %	K	
Maximum operating temperature			220	°C	
Dissipation constant			2	mW/°C	
Thermal time constant			8	S	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS
D1 - D12 - thermal resistance junction to sink (per diode) (1)	R <sub>thJS</sub>	-	1.19	-	°C/W
Case to sink thermal resistance (per module) (1)		-	0.1	-	C/VV
Mounting torque (M4)		2		3	Nm
Weight		-	28	-	g

#### Note

 $<sup>^{(1)}</sup>$  Mounting surface flat, smooth, and greased,  $\lambda_{grease}$  = 0.67 W/mK

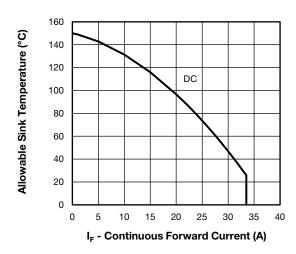


Fig. 1 - Allowable Sink Temperature vs. Continuous Forward Current (Forward Current vs. Sink Temperature)

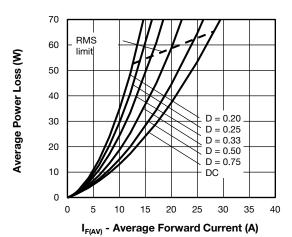


Fig. 2 - Average Power Loss vs Average Forward Current (Forward Power Loss Characteristics)

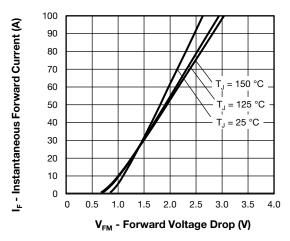


Fig. 3 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

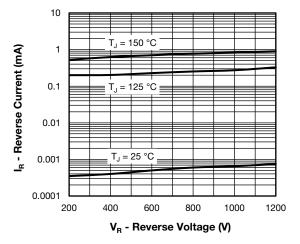


Fig. 4 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

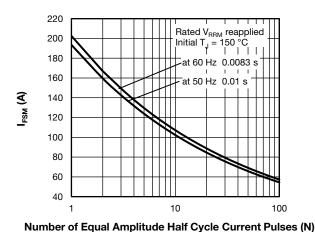
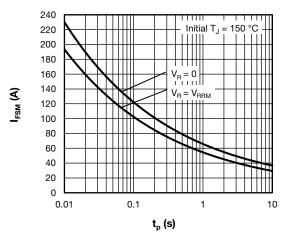


Fig. 5 - I<sub>FSM</sub> vs. N (Non-Repetitive Peak Forward Surge Current vs. Number Pulses)



 $\label{eq:Fig. 6-loss} \mbox{Fig. 6-l}_{FSM} \mbox{ vs. } t_p \\ \mbox{(Non-Repetitive Peak Forward Surge Current vs. Pulse Duration)}$ 

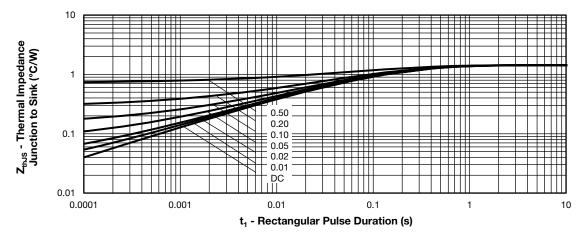
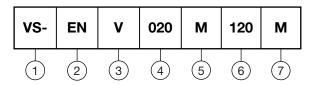


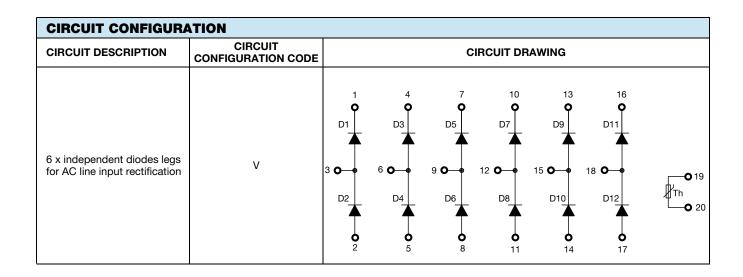
Fig. 7 -  $Z_{thJS}$  Thermal Impedance Junction to Sink vs. t1 Rectangular Pulse Duration (Maximum Thermal Impedance  $Z_{thJS}$  Characteristics Per Diode)

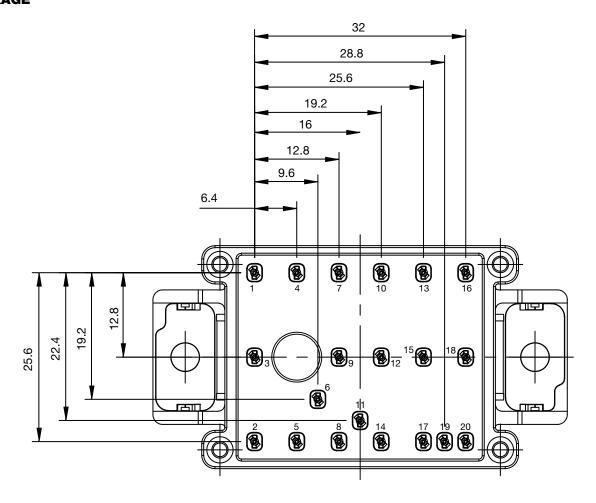
#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- Package indicator (EN = EMIPAK 1B)
- Gircuit configuration (V = 6 x independent diodes legs for AC line input rectification)
- 4 Current rating (020 = 20 A)
- 5 Switch die technology (M = MOAT standard recovery diode)
- 6 Voltage rating (120 = 1200 V)
- 7 Diode technology (M = MOAT standard recovery diode)



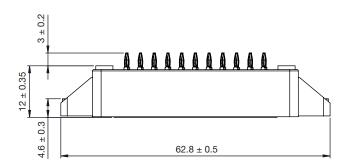


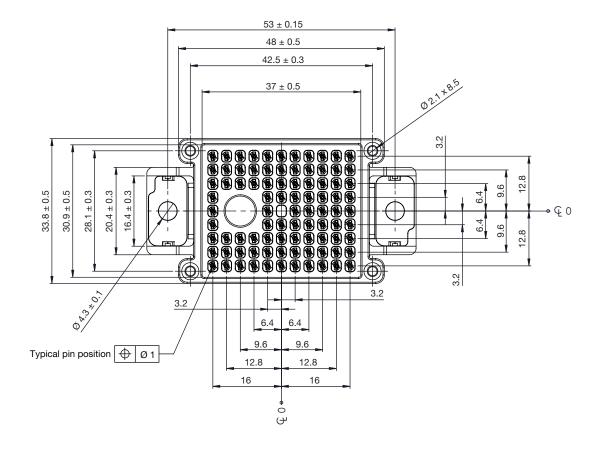
LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95558</u>			
Application Note	www.vishay.com/doc?95580		



## **EMIPAK-1B PressFit**

#### **DIMENSIONS** in millimeters







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Vishay

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