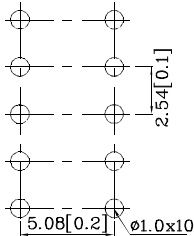


**Features**

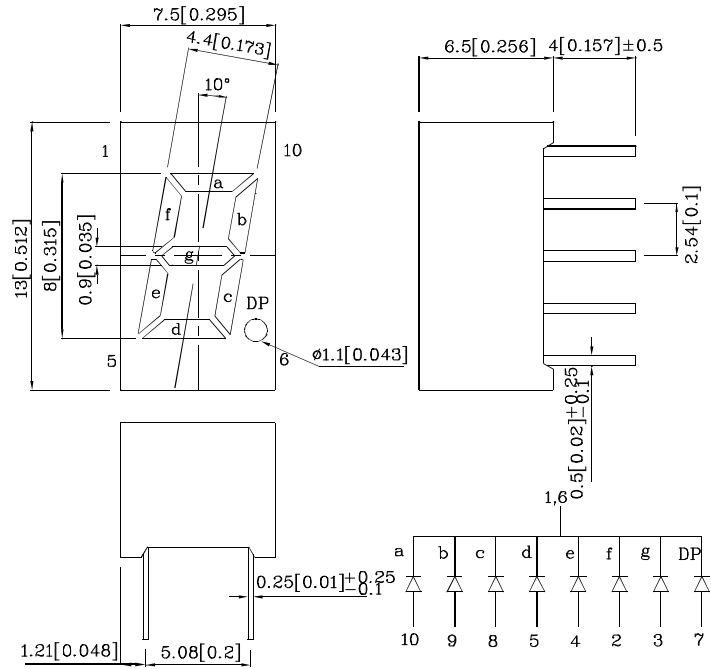
- Low power consumption
- Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color contrast
- RoHS Compliant



Recommended PCB Layout



**Package Schematics**



**Notes:**

1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01") unless otherwise noted.
2. Specifications are subject to change without notice.

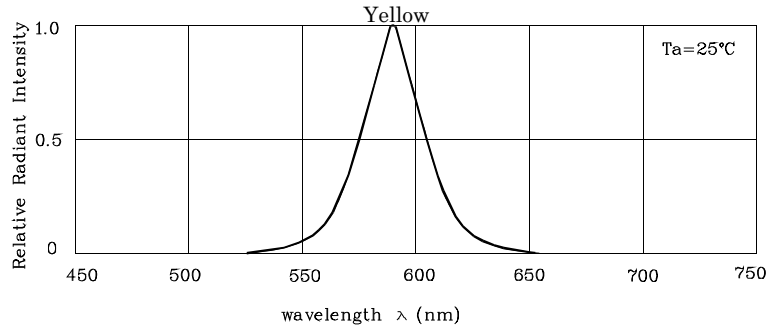
Absolute Maximum Ratings (T <sub>A</sub> =25°C)		Yellow (GaAsP/GaP)	Unit
Reverse Voltage	V <sub>R</sub>	5	V
Forward Current	I <sub>F</sub>	30	mA
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	i <sub>FS</sub>	140	mA
Power Dissipation	P <sub>D</sub>	75	mW
Operating Temperature	T <sub>A</sub>	-40 ~ +85	°C
Storage Temperature	T <sub>stg</sub>	-40 ~ +85	
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds		

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Operating Characteristics (T <sub>A</sub> =25°C)		Yellow (GaAsP/GaP)	Unit
Forward Voltage (Typ.) (I <sub>F</sub> =10mA)	V <sub>F</sub>	1.95	V
Forward Voltage (Max.) (I <sub>F</sub> =10mA)	V <sub>F</sub>	2.5	V
Reverse Current (Max.) (V <sub>R</sub> =5V)	I <sub>R</sub>	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I <sub>F</sub> =10mA)	λ <sub>P</sub>	590*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I <sub>F</sub> =10mA)	λ <sub>D</sub>	588*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I <sub>F</sub> =10mA)	Δλ	35	nm
Capacitance (Typ.) (V <sub>F</sub> =0V, f=1MHz)	C	20	pF

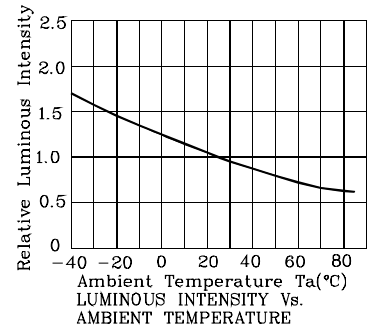
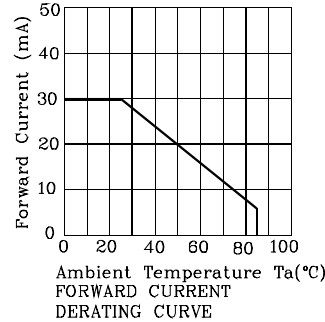
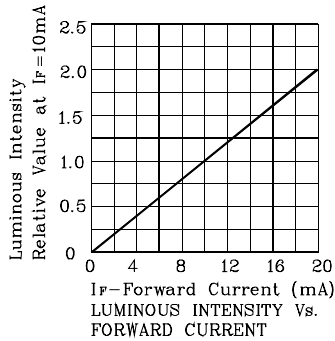
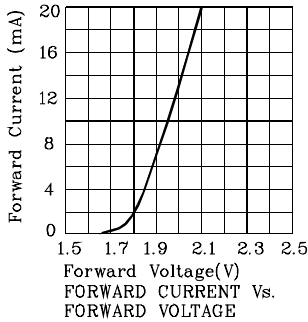
Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* (I <sub>F</sub> =10mA) ucd		Wavelength CIE127-2007* nm λ <sub>P</sub>	Description
			min.	typ.		
XDUY06C	Yellow	GaAsP/GaP	2200 900*	4990 1690*	590*	Common Cathode, Rt.Hand Decimal.

\*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

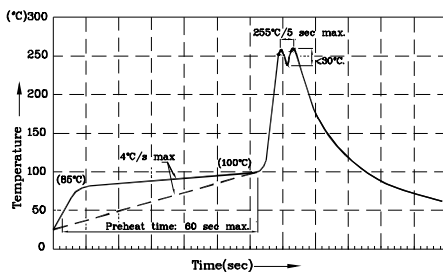


RELATIVE INTENSITY Vs. CIE WAVELENGTH

❖ Yellow



Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



Notes:

- 1.Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2.Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
- 3.Do not apply stress to the epoxy resin while the temperature is above 85°C.
- 4.Fixtures should not incur stress on the component when mounting and during soldering process.
- 5.SAC 305 solder alloy is recommended.
- 6.No more than one wave soldering pass.
- 7.During wave soldering, the PCB top-surface temperature should be kept below 105°C.

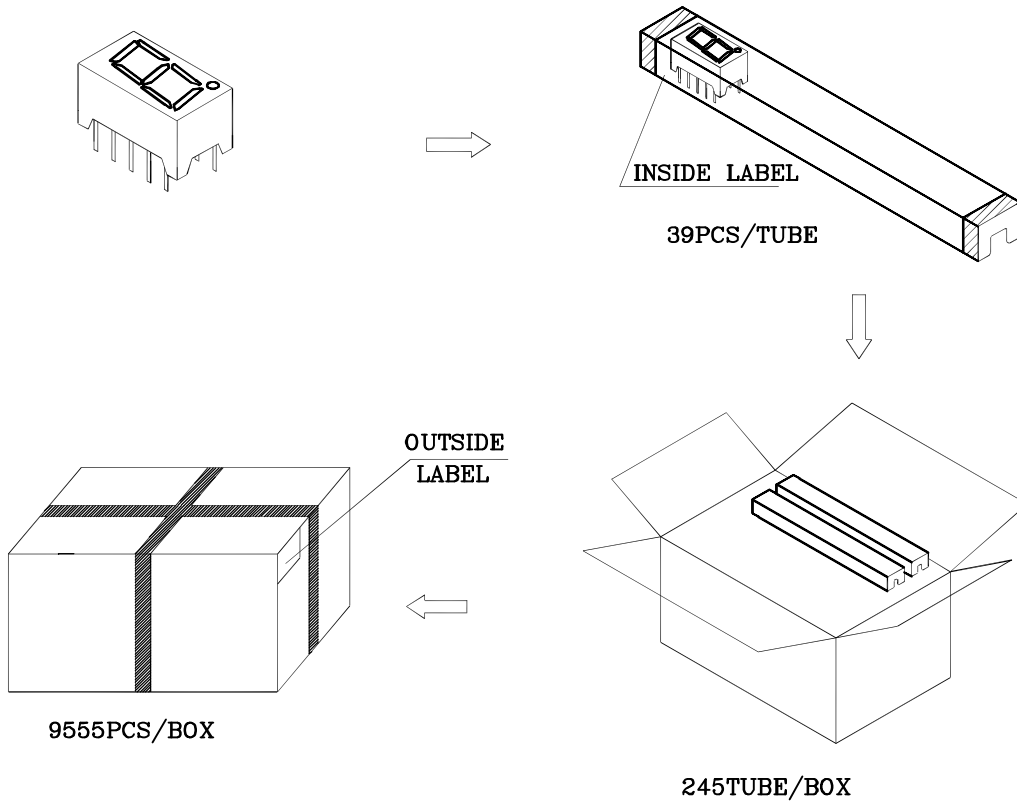
Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength), the typical accuracy of the sorting process is as follows:

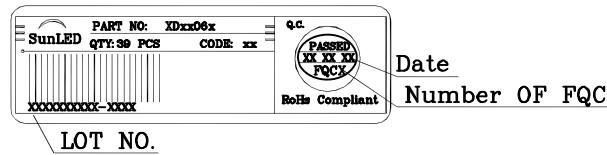
1. Wavelength: +/-1nm
2. Luminous Intensity / Luminous Flux: +/-15%
3. Forward Voltage: +/-0.1V

Note: Accuracy may depend on the sorting parameters.

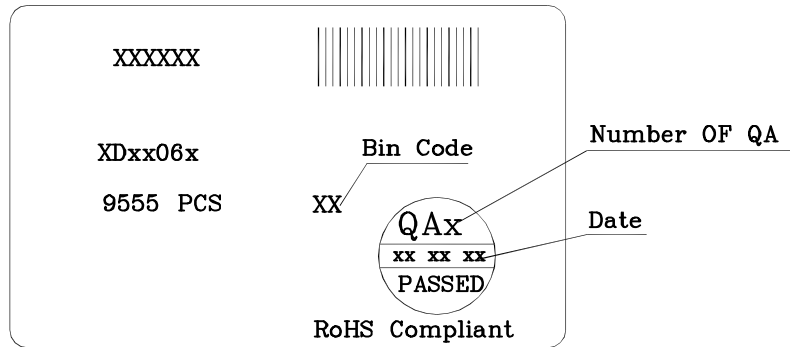
PACKING & LABEL SPECIFICATIONS



Inside Label On IC-tube



Outside Label On Box



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2. Contents within this document are subject to improvement and enhancement changes without notice.
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4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
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