



#### P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BVDSS	Rds(on) Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-20V	15mΩ @ V <sub>GS</sub> = -4.5V	-9.5A
-20V	19mΩ @ V <sub>GS</sub> = -2.5V	-8.5A

# **Description and Applications**

This MOSFET is designed to minimize on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for highefficiency power management applications.

- **Battery Management Application**
- **Power Management Functions**
- **DC-DC Converters**

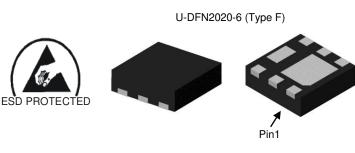
### **Features and Benefits**

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Low On-Resistance
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

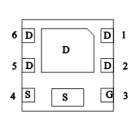
https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

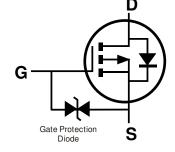
- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.007 grams (Approximate)











**Equivalent Circuit** 

#### **Ordering Information** (Note 4)

Part Number	Case	Packaging		
DMP2016UFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel		
DMP2016UFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



# **Marking Information**

Site1



O6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2017		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Е		- 1	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



O6 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 1 = 2021) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2017	 2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	7	 1	2	3	4	5	6	7	8	9	0

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Χ	Υ	Z



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	Drain-Source Voltage				
Gate-Source Voltage	$V_{GSS}$	±8	V		
Continuous Drain Current (Note 6) VGS = -4.5V	ID	-9.5 -7.5	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-70	Α
Continuous Source-Drain Diode Current (Note 6)	Is	-2.9	Α		
Avalanche Current (Note 7) L = 0.1mH	las	-16.5	Α		
Avalanche Energy (Note 7) L = 0.1mH			Eas	14.5	mJ

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	Do	0.9	W
Total Fower Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.6	VV
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	134	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	Pn	1.8	W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	70	°C/W
Thermal Resistance, Junction to Case (Note 6)	Steady State	R <sub>θ</sub> JC	12.5	°C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

# Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

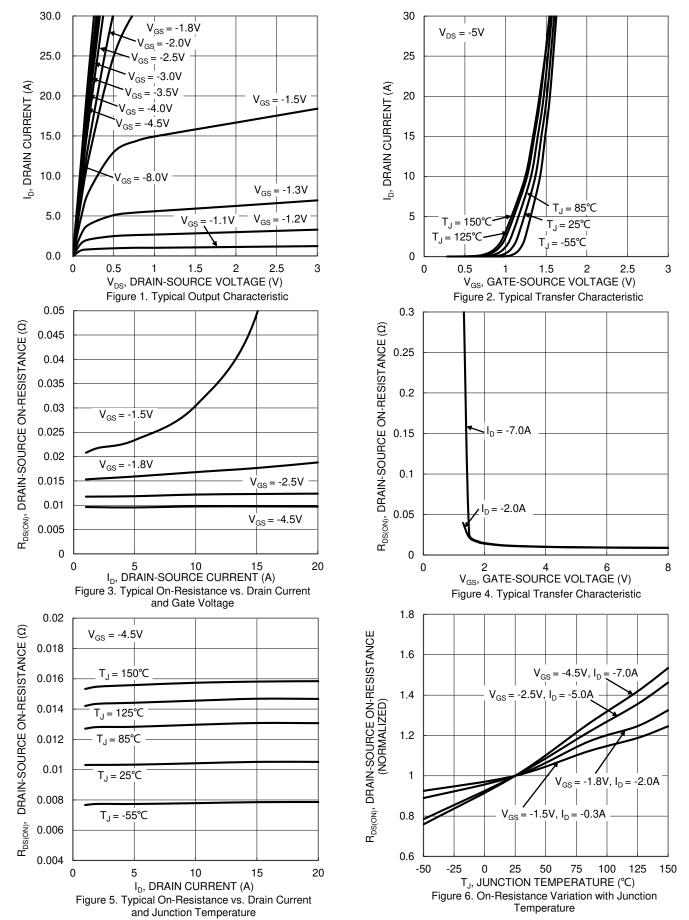
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						•
Drain-Source Breakdown Voltage	BVDSS	-20	_	_	V	$V_{GS} = 0V$ , $I_{D} = -250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	IDSS	_	l	-1	μΑ	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	1	±10	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.35	_	-1.1	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
			9.9	15		$V_{GS} = -4.5V$ , $I_{D} = -7.0A$
Static Drain-Source On-Resistance	Dag (a)		12.5	19	mΩ	$V_{GS} = -2.5V$ , $I_{D} = -5.0A$
Static Diain-Source On-Nesistance	R <sub>DS(ON)</sub>	_	17.5	36	11122	$V_{GS} = -1.8V$ , $I_{D} = -2.0A$
			21.5	80		$V_{GS} = -1.5V$ , $I_{D} = -0.3A$
Diode Forward Voltage	$V_{SD}$	_	-0.6	-1.2	V	$V_{GS} = 0V, I_{S} = -1.0A$
DYNAMIC CHARACTERISTICS (Note 9)						•
Input Capacitance	Ciss	_	1710	_		V 40V V 0V
Output Capacitance	Coss	_	200		рF	$V_{DS} = -10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	112	_		1 – 1.01011 12
Gate Resistance	$R_g$	_	28	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	17	_		
Total Gate Charge (V <sub>GS</sub> = -8V)	$Q_g$	_	30	_	nC	V 10V I 10A
Gate-Source Charge	Qgs	_	2.0	_	IIC	$V_{DS} = -10V, I_{D} = -4.0A$
Gate-Drain Charge	Qgd	_	3.5	_		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.0	_		
Turn-On Rise Time	tr	_	23	_		$V_{DS} = -10V$ , $V_{GS} = -4.5V$ ,
Turn-Off Delay Time	tD(OFF)	_	133	_	ns	$R_G = 1\Omega$ , $I_D = -4.0A$
Turn-Off Fall Time	tF	_	115	_		
Reverse Recovery Time	trr	_	20	_	ns	IF = -1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	_	12	_	nC	$I_F = -1.0A$ , $di/dt = 100A/\mu s$

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.

<sup>7.</sup>  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_{J} = +25^{\circ}C$ .

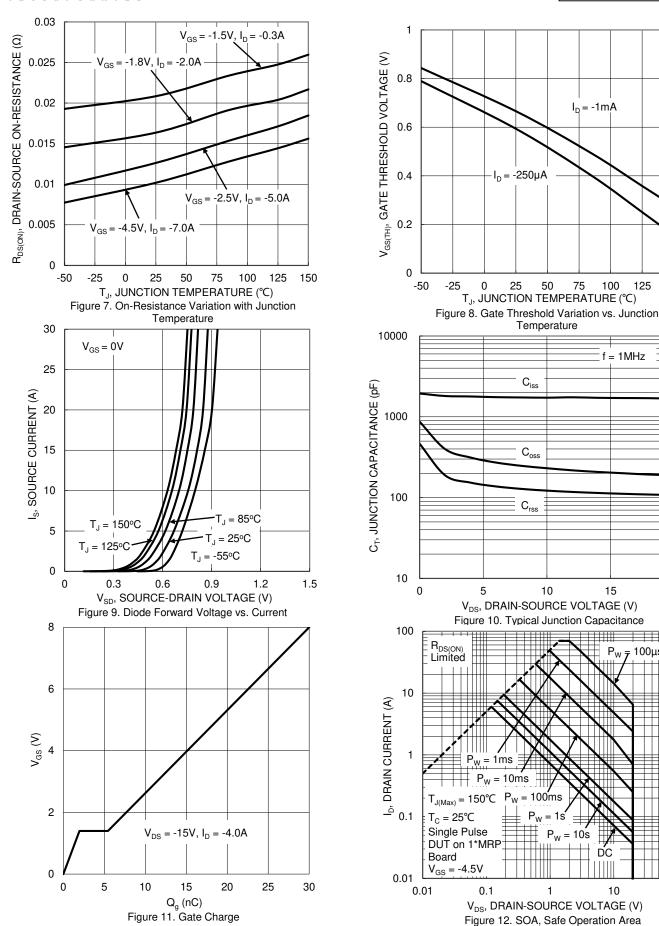
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.





125





100

20

100µs



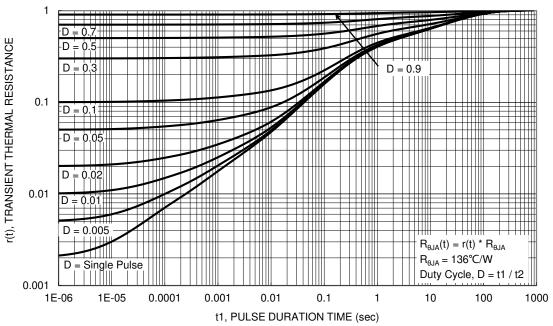


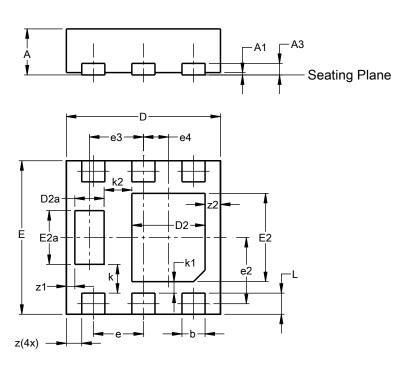
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)

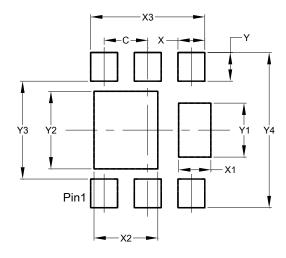


U-DFN2020-6						
	(Ty	oe F)				
Dim	Min	Max	Тур			
Α	0.57	0.63	0.60			
<b>A</b> 1	0.00	0.05	0.03			
A3	-	-	0.15			
b	0.25	0.35	0.30			
D	1.95	2.05	2.00			
D2	0.85	1.05	0.95			
D2a	0.33	0.43	0.38			
Е	1.95	2.05	2.00			
E2	1.05	1.25	1.15			
E2a	0.65	0.75	0.70			
е		0.65 BS	С			
e2	(	).863 BS	SC			
е3		0.70 BS	С			
e4	(	).325 BS	SC			
k		0.37 BS	С			
k1		0.15 BS	_			
k2		0.36 BS	С			
L	0.225 0.325 0.275					
Z	0.20 BSC					
<b>z</b> 1		).110 BS	_			
z2		0.20 BS	С			
All C	imens	ions in	mm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)



Dimensions	Value		
Dimensions	(in mm)		
С	0.650		
X	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Υ	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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