

## DM74LS298 Quad 2-Port Register Multiplexer with Storage

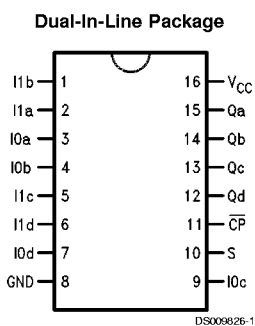
### General Description

The 'LS298 is a quad 2-port register. It is the logical equivalent of a quad 2-input multiplexer followed by a quad 4-bit edge-triggered register. A Common Select input selects between two 4-bit input ports (data sources). The selected data is transferred to the output register synchronous with the HIGH-to-LOW transition of the Clock input.

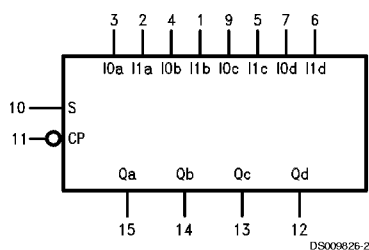
### Features

- Select from two data sources
- Fully edge-triggered operation
- Typical power dissipation of 65 mW

### Connection Diagram



### Logic Symbol



V<sub>CC</sub> = Pin 16  
GND = Pin 8

Order Number DM54LS298J, DM54LS298W,  
DM74LS298M or DM74LS298N  
See Package Number J16A, N16E or W16A

Pin Names	Description
S	Common Select Inputs
$\overline{CP}$	Clock Pulse Input (Active Falling Edge)
I <sub>0a</sub> , I <sub>0d</sub>	Source 0 Data Inputs
I <sub>1a</sub> , I <sub>1d</sub>	Source 1 Data Inputs
Q <sub>a</sub> , Q <sub>d</sub>	Flip-Flip Outputs

## Absolute Maximum Ratings (Note 1)

Supply Voltage	7V	DM54LS	-55°C to +125°C
Input Voltage	7V	DM74LS	0°C to +70°C
Operating Free Air Temperature Range		Storage Temperature Range	-65°C to +150°C

## Recommended Operating Conditions

Symbol	Parameter	DM54LS298			DM74LS298			Units
		Min	Nom	Max	Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage	2			2			V
V <sub>IL</sub>	Low Level Input Voltage			0.7			0.8	V
I <sub>OH</sub>	High Level Output Current			-0.4			-0.4	mA
I <sub>OL</sub>	Low Level Output Current			4			8	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C
t <sub>s</sub> (H)	Setup Time HIGH or LOW	25			25			ns
t <sub>s</sub> (L)	S to $\overline{CP}$	25			25			ns
t <sub>h</sub> (H)	Hold Time HIGH or LOW	0			0			ns
t <sub>h</sub> (L)	S to $\overline{CP}$	0			0			ns
t <sub>s</sub> (H)	Setup Time HIGH or LOW	15			15			ns
t <sub>s</sub> (L)	I <sub>0x</sub> or I <sub>1x</sub> to $\overline{CP}$	15			15			ns
t <sub>h</sub> (H)	Hold Time HIGH or LOW	5.0			5.0			ns
t <sub>h</sub> (L)	I <sub>0x</sub> or I <sub>1x</sub> to $\overline{CP}$	5.0			5.0			ns
t <sub>w</sub> (H)	$\overline{CP}$ Pulse Width HIGH or LOW	20			20			ns
t <sub>w</sub> (L)		20			20			ns

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
V <sub>I</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -18 mA			-1.5	V
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max, V <sub>IL</sub> = Max	DM54	2.5		V
			DM74	2.7	3.4	
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max, V <sub>IH</sub> = Min I <sub>OL</sub> = 4 mA, V <sub>CC</sub> = Min	DM54		0.4	V
			DM74		0.35	
			DM74		0.25	
I <sub>I</sub>	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> = 7V V <sub>I</sub> = 10V			0.1	mA
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 2.7V			20	μA
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 0.4V			-0.4	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	DM54	-20	-100	mA
			DM74	-20	-100	
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max, I <sub>0n</sub> , I <sub>1n</sub> , S = GND, $\overline{CP}$ = $\sim$			21	mA

**Note 2:** All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

**Note 3:** Not more than one output should be shorted at a time, and the duration should not exceed one second.

## Switching Characteristics

at  $V_{CC} = +5V$  and  $T_A = +25^\circ C$

Symbol	Parameter	$R_L = 2\text{ k}\Omega, C_L = 15\text{ pF}$		Units
		Min	Max	
$t_{PLH}$	Propagation Delay Time Low to High Level Output $\overline{CP}$ to $Q_n$		25	ns
$t_{PHL}$	Propagation Delay Time High to Low Level Output $\overline{CP}$ to $Q_n$		25	ns

## Functional Description

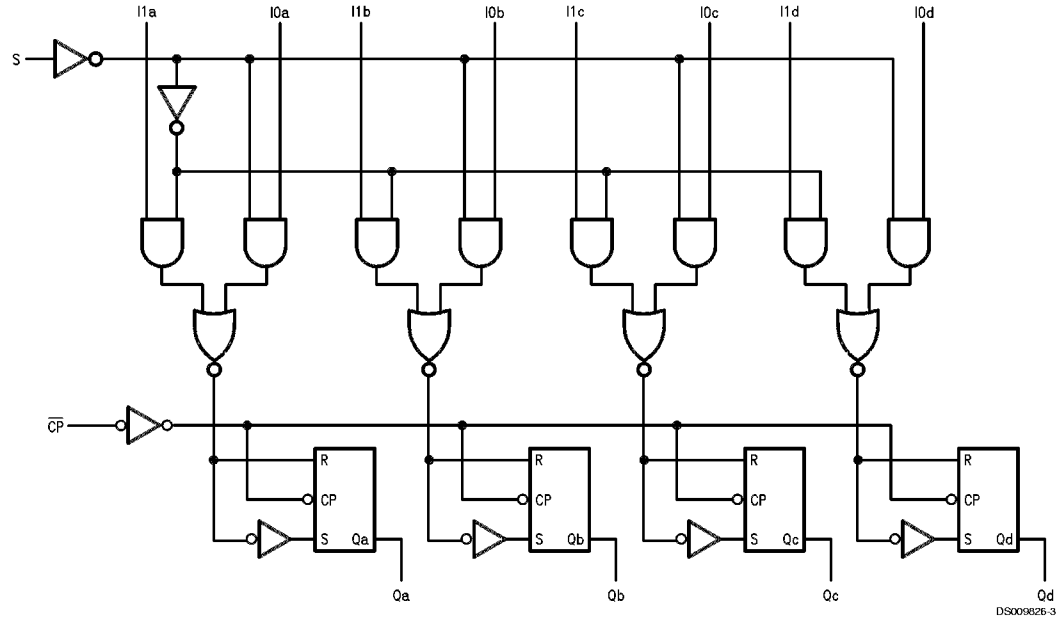
This device is a high speed quad 2-port register. It selects four bits of data from two sources (ports) under the control of a Common Select input (S). The selected data is transferred to the 4-bit output register synchronous with the HIGH-to-LOW transition of the Clock input ( $\overline{CP}$ ). The 4-bit output register is fully edge-triggered. The Data inputs ( $I_{nx}$ ) and Select input (S) need be stable only one setup time prior to the HIGH-to-LOW transition of the clock for predictable operation.

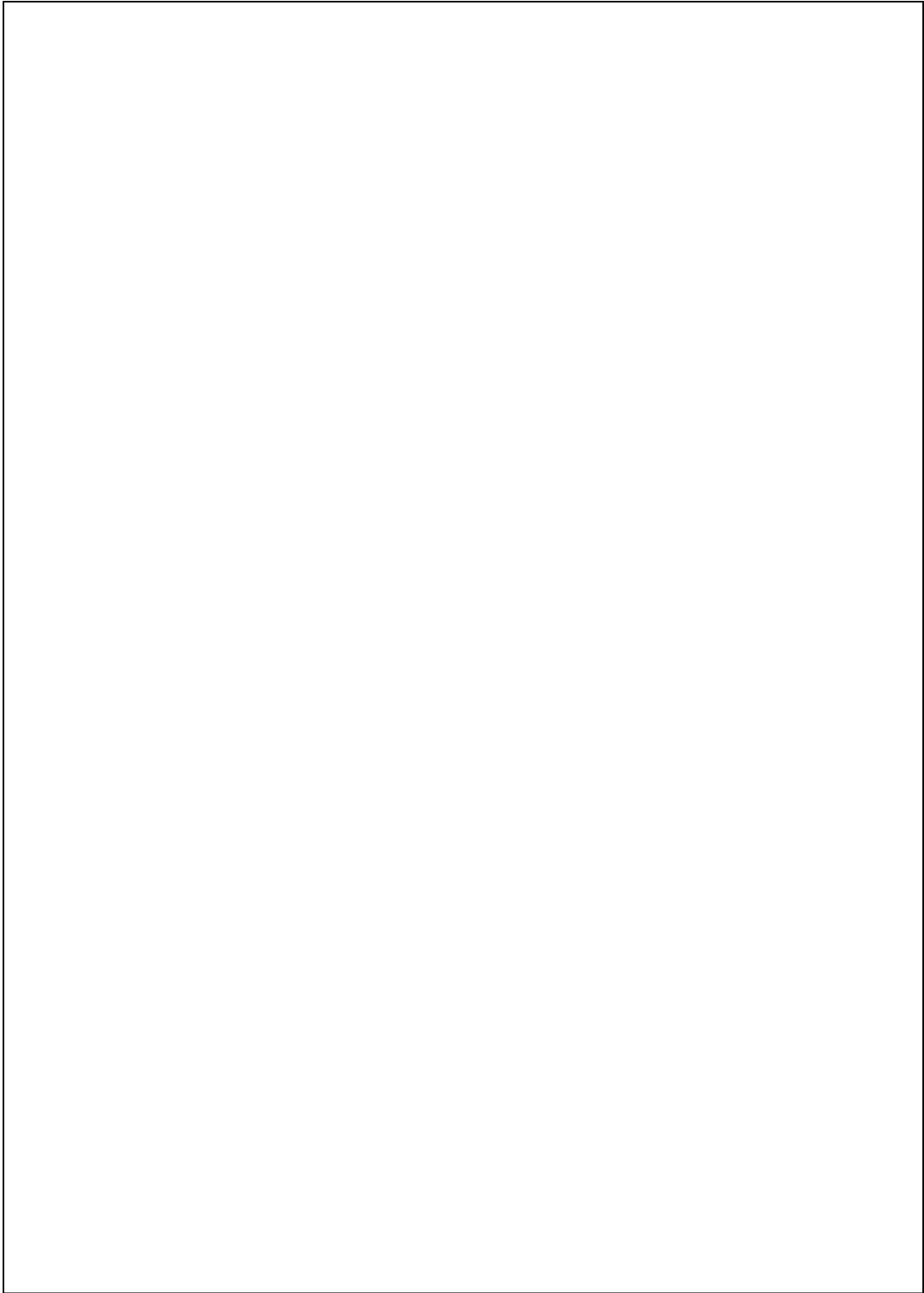
## Truth Table

S	Inputs		Output
	$I0_x$	$I1_x$	
l	l	X	L
l	h	X	H
h	X	l	L
h	X	h	H

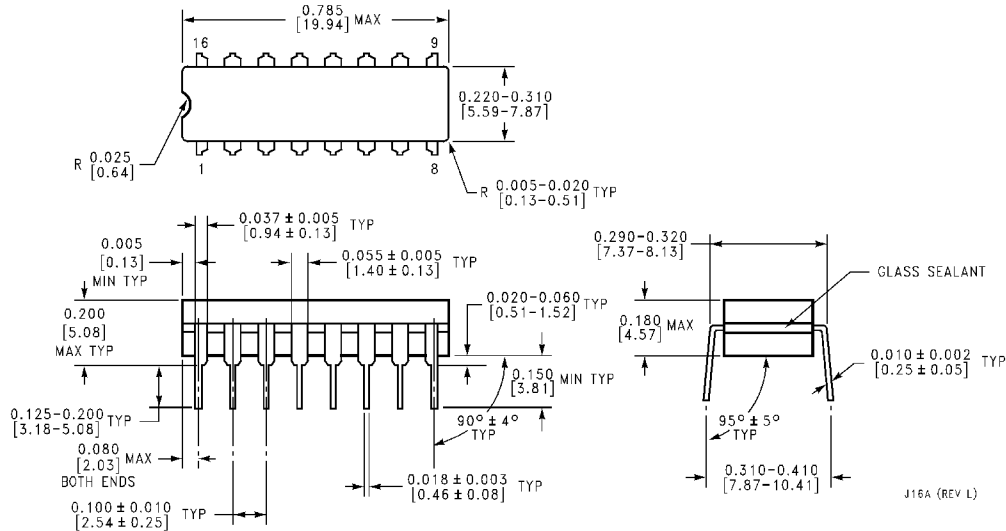
l = LOW Voltage Level one setup time prior to the HIGH-to-LOW clock transition.  
h = HIGH Voltage Level one setup time prior to the HIGH-to-LOW clock transition.  
H = HIGH Voltage Level  
L = LOW Voltage Level  
X = Immaterial

## Logic Diagram

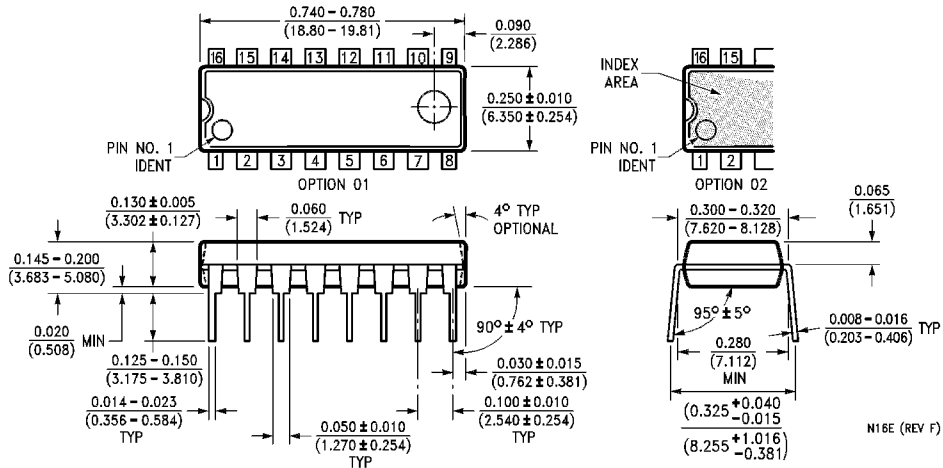




**Physical Dimensions** inches (millimeters) unless otherwise noted

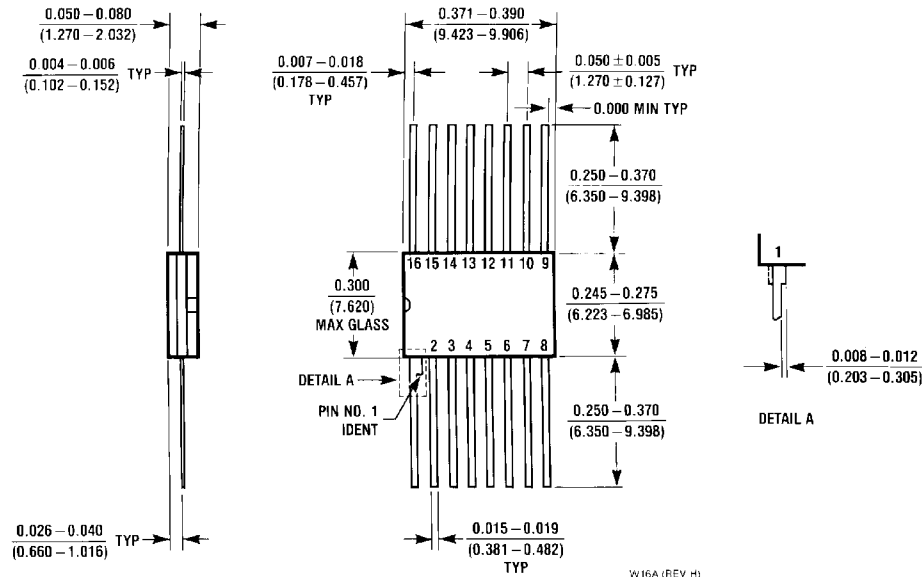


**16-Lead Ceramic Dual-In-Line Package (J)**  
 Order Number DM54LS298J  
 Package Number J16A



**16-Lead Molded Dual-In-Line Package (N)**  
 Order Number DM74LS298N  
 Package Number N16E

**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



**16-Lead Ceramic Flat Package (W)**  
**Order Number DM54LS298W**  
**NS Package Number W16A**

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Fairchild Semiconductor Corporation  
 Americas  
 Customer Response Center  
 Tel: 1-888-522-5372

Fairchild Semiconductor Europe  
 Fax: +49 (0) 1 80-530 85 86  
 Email: europe.support@nsc.com  
 Deutsch Tel: +49 (0) 8 141-35-0  
 English Tel: +44 (0) 1 793-85-68-56  
 Italy Tel: +39 (0) 2 57 5631

Fairchild Semiconductor Hong Kong Ltd.  
 13th Floor, Straight Block,  
 Ocean Centre, 5 Canton Rd.  
 Tsimshatsui, Kowloon  
 Hong Kong  
 Tel: +852 2737-7200  
 Fax: +852 2314-0061

National Semiconductor Japan Ltd.  
 Tel: 81-3-5620-6175  
 Fax: 81-3-5620-6179

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