Product data sheet

1. General description

The 74LVC1G04 provides one inverting buffer.

Input can be driven from either 3.3 V or 5 V devices. These features allow the use of these devices in a mixed 3.3 V and 5 V environment.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall time.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant inputs for interfacing with 5 V logic
- High noise immunity
- Complies with JEDEC standard:
 - JESD8-7 (1.65 V to 1.95 V)
 - ◆ JESD8-5 (2.3 V to 2.7 V)
 - ◆ JESD8-B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- ± 24 mA output drive (V_{CC} = 3.0 V)
- CMOS low power consumption
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C.

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3. Ordering information

Table 1.	Ordering information	
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Type number	Package	Package							
	Temperature range	Name	Description	Version					
74LVC1G04GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1					
74LVC1G04GV	–40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753					
74LVC1G04GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886					
74LVC1G04GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1 \times 0.5 mm	SOT891					
74LVC1G04GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115					
74LVC1G04GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202					
74LVC1G04GX	–40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226					

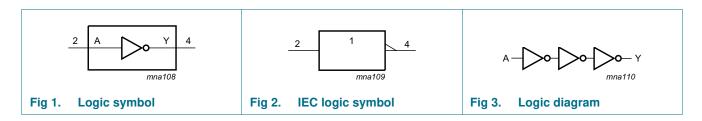
4. Marking

Table 2. Marking

Type number	Marking code ^[1]
74LVC1G04GW	VC
74LVC1G04GV	V04
74LVC1G04GM	VC
74LVC1G04GF	VC
74LVC1G04GN	VC
74LVC1G04GS	VC
74LVC1G04GX	VC

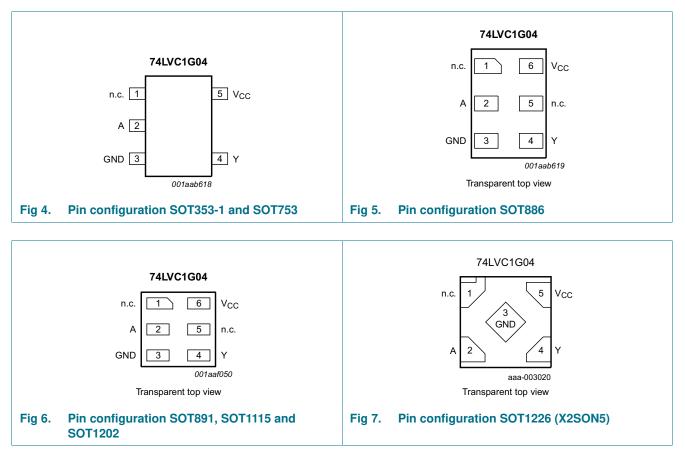
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1 Pinning



6.2 Pin description

Table 3. Pin description			
Symbol	Pin		Description
	TSSOP5 and X2SON5	XSON6	
n.c.	1	1	not connected
A	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

7. Functional description

Table 4.	Function table ^[1]
----------	-------------------------------

Input	Output
Α	Y
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+6.5	V
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
VI	input voltage		<u>[1]</u>	-0.5	+6.5	V
I _{ОК}	output clamping current	$V_{\rm O}$ > $V_{\rm CC}$ or $V_{\rm O}$ < 0 V		-	±50	mA
Vo	output voltage	Active mode	<u>[1][2]</u>	-0.5	$V_{CC} + 0.5$	V
		Power-down mode	<u>[1][2]</u>	-0.5	+6.5	V
I _O	output current	$V_{O} = 0$ to V_{CC}		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
P _{tot}	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	<u>[3]</u>	-	250	mW
T _{stg}	storage temperature			-65	+150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When $V_{CC} = 0$ V (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP5 and SC-74A packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K. For XSON6 and X2SON5 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
V _{CC}	supply voltage		1.65	-	5.5	V	
VI	input voltage		0	-	5.5	V	
Vo	output voltage	Active mode	0	-	V _{CC}	Vo	
		$V_{CC} = 0$ V; Power-down mode	0	-	5.5	Vo	
T _{amb}	ambient temperature		-40	-	+125	°C	
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC} = 1.65 \text{ V to } 2.7 \text{ V}$	-	-	20	ns/V	
		$V_{CC} = 2.7 \text{ V to } 5.5 \text{ V}$	-	-	10	ns/V	

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -	40 °C to +85 °C					
VIH	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	$0.65 imes V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		$V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$	$0.7 \times V_{CC}$	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		$V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$	-	-	$0.3\times V_{CC}$	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_O = $-100~\mu\text{A};~V_{CC}$ = 1.65 V to 5.5 V	$V_{CC}-0.1$	-	-	V
		$I_{O} = -4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.2	-	-	V
		$I_{O} = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	2.2	-	-	V
		$I_{O} = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.3	-	-	V
		$I_{O} = -32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.8	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_O = 100 $\mu A; V_{CC}$ = 1.65 V to 5.5 V	-	-	0.1	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.45	V
		$I_{O} = 8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.3	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.4	V
		$I_{O} = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.55	V
		$I_{O} = 32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.55	V
l _l	input leakage current	V_{CC} = 0 V to 5.5 V; V_{I} = 5.5 V or GND	-	±0.1	±1	μA
I _{OFF}	power-off leakage current	V_{CC} = 0 V; V _I or V _O = 5.5 V	-	±0.1	±2	μA
I _{CC}	supply current	$V_{I} = 5.5 \text{ V or GND};$ $V_{CC} = 1.65 \text{ V to 5.5 V}; I_{O} = 0 \text{ A}$	-	0.1	4	μA
Δl _{CC}	additional supply current	per pin; V_{CC} = 2.3 V to 5.5 V; V ₁ = V _{CC} - 0.6 V; I _O = 0 A	-	5	500	μA
CI	input capacitance	V_{CC} = 3.3 V; V_I = GND to V_{CC}	-	5	-	pF

74LVC1G04

Single inverter

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = –	40 °C to +125 °C			1		
V _{IH}	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	$0.7\times V_{CC}$	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	$0.3\times V_{CC}$	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = $-100~\mu\text{A};~V_{CC}$ = 1.65 V to 5.5 V	$V_{CC}-0.1$	-	-	V
		$I_{O} = -4 \text{ mA}; V_{CC} = 1.65 \text{ V}$	0.95	-	-	V
		$I_{O} = -8 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.7	-	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	1.9	-	-	V
		$I_{O} = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.0	-	-	V
		$I_{O} = -32 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.4	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 100 µA; V_{CC} = 1.65 V to 5.5 V	-	-	0.1	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.70	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.60	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.80	V
		I _O = 32 mA; V _{CC} = 4.5 V	-	-	0.80	V
l _l	input leakage current	$V_{CC} = 0$ V to 5.5 V; $V_{I} = 5.5$ V or GND	-	-	±1	μA
I _{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}; \text{ V}_{1} \text{ or } \text{ V}_{O} = 5.5 \text{ V}$	-	-	±2	μA
I _{CC}	supply current	$V_{I} = 5.5 V \text{ or GND};$ $V_{CC} = 1.65 V \text{ to } 5.5 V; I_{O} = 0 \text{ A}$	-	-	4	μA
Δl _{CC}	additional supply current	per pin; $V_{CC} = 2.3 \text{ V}$ to 5.5 V; $V_I = V_{CC} - 0.6 \text{ V}$; $I_O = 0 \text{ A}$	-	-	500	μA

Table 7. Static characteristics ...continued

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

[1] All typical values are measured at V_{CC} = 3.3 V and T_{amb} = 25 °C.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Figure 9.

Symbol	Parameter	Conditions	-40 °C to +85 °C -40 °C to +1		+125 °C	Unit		
			Min	Typ <mark>[1]</mark>	Max	Min	Max	
t _{pd}	propagation delay	A to Y; see Figure 8 [2]						
		V _{CC} = 1.65 V to 1.95 V	1.0	3.0	7.5	1.0	9.5	ns
		V _{CC} = 2.3 V to 2.7 V	0.5	2.0	5.0	0.5	6.5	ns
		V _{CC} = 2.7 V	0.5	2.3	5.2	0.5	7.0	ns
		V _{CC} = 3.0 V to 3.6 V	0.5	2.0	4.2	0.5	5.5	ns
		V _{CC} = 4.5 V to 5.5 V	0.5	1.6	3.7	0.5	5.0	ns
C _{PD}	power dissipation capacitance	$V_{I} = GND \text{ to } V_{CC}; V_{CC} = 3.3 \text{ V}$ [3]	-	14	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_{D} = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \sum (C_L \times V_{CC}{}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 $f_o = output frequency in MHz;$

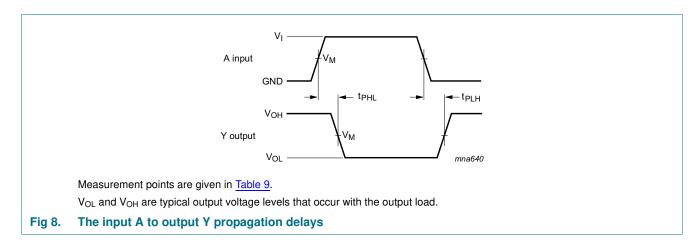
 C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}{}^2 \times f_o)$ = sum of outputs.

12. AC waveforms

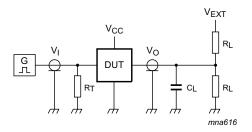


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Single inverter

Tuble 5. Medisarement points					
Supply voltage	Input	Output			
V _{cc}	V _M	V _M			
1.65 V to 1.95 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$			
2.3 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$			
2.7 V	1.5 V	1.5 V			
3.0 V to 3.6 V	1.5 V	1.5 V			
4.5 V to 5.5 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$			





Test data is given in <u>Table 10</u>.

Definitions for test circuit:

R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

Fig 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Input		Load	
V _{CC}	VI	$t_r = t_f$	CL	RL	t _{PLH} , t _{PHL}
1.65 V to 1.95 V	V _{CC}	\leq 2.0 ns	30 pF	1 kΩ	open
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	30 pF	500 Ω	open
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	50 pF	500 Ω	open

13. Package outline

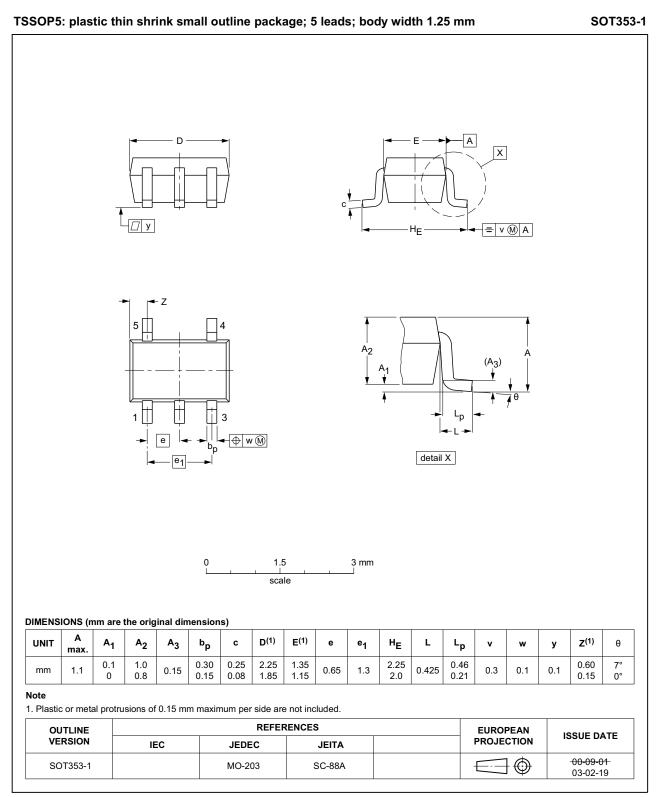


Fig 10. Package outline SOT353-1 (TSSOP5)

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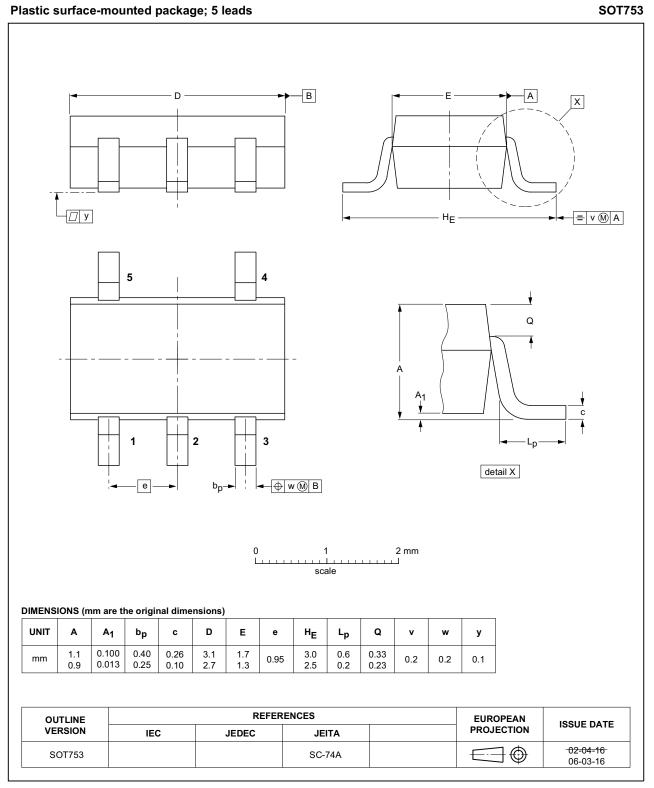
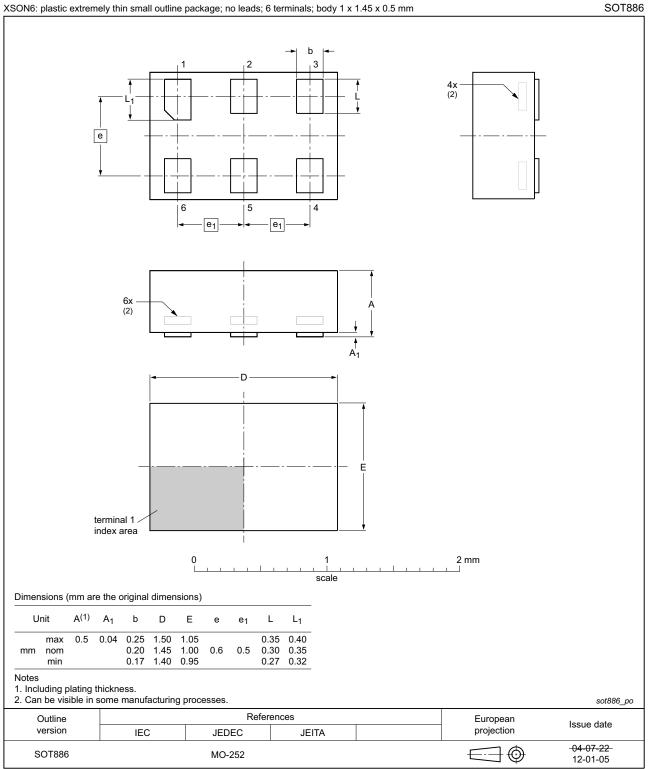


Fig 11. Package outline SOT753 (SC-74A)



XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

Fig 12. Package outline SOT886 (XSON6)

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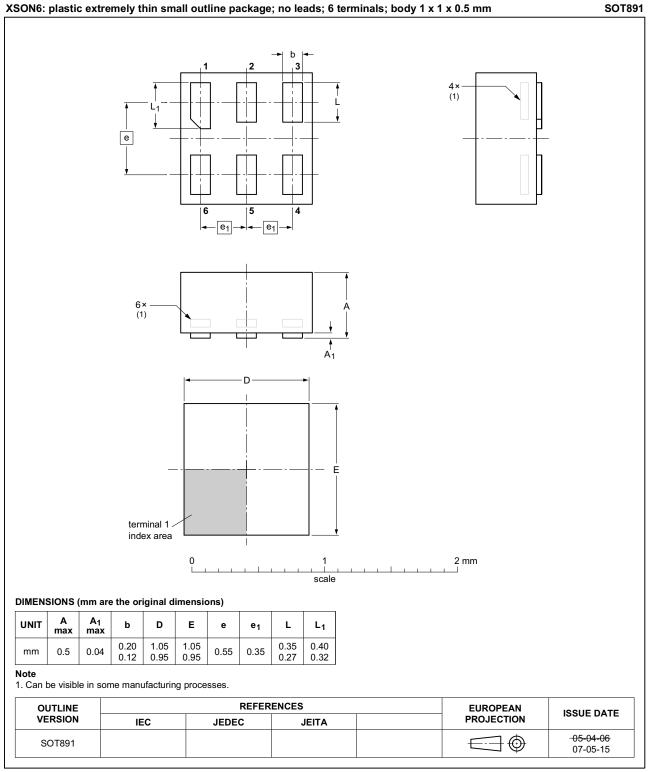
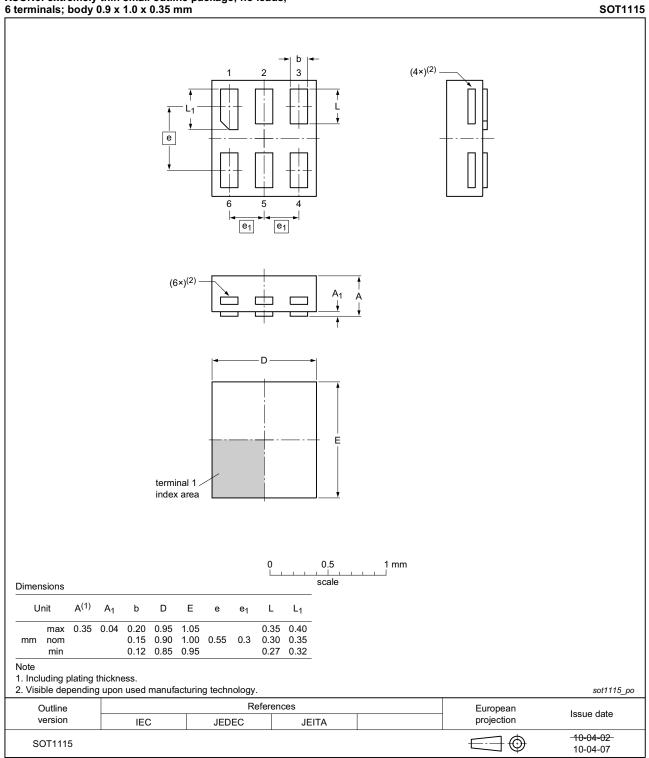


Fig 13. Package outline SOT891 (XSON6)

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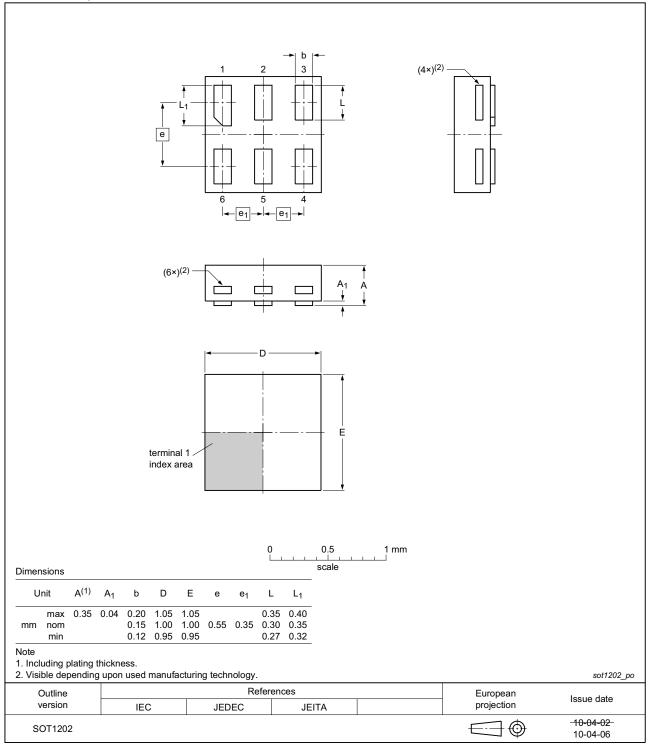
XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 14. Package outline SOT1115 (XSON6)

74LVC1G04 **Product data sheet**

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SOT1202

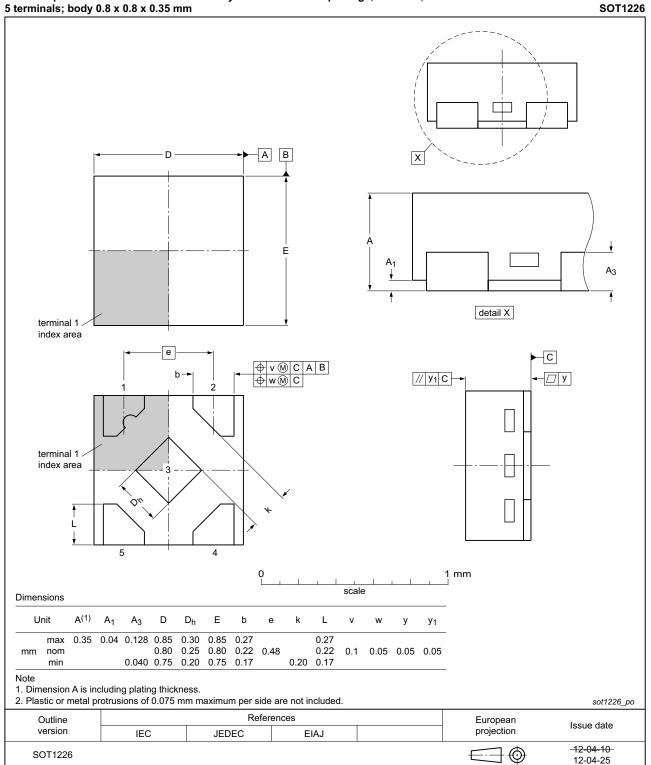


XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 15. Package outline SOT1202 (XSON6)

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74LVC1G04 Single inverter



X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm

Fig 16. Package outline SOT1226 (X2SON5)

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14. Abbreviations

Table 11. Abbreviations		
Acronym	Description	
CMOS	Complementary Metal Oxide Semiconductor	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
HBM	Human Body Model	
MM	Machine Model	
TTL	Transistor-Transistor Logic	

15. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74LVC1G04 v.13	20161128	Product data sheet	-	74LVC1G04 v.12	
Modifications:	• <u>Table 7</u> : The maximum limits for leakage current and supply current have changed.				
74LVC1G04 v.12	20120806	Product data sheet	-	74LVC1G04 v.11	
Modifications:	 Package outli 	ne drawing of SOT1226 (Figure	e 16) modified.		
74LVC1G04 v.11	20120412	Product data sheet	-	74LVC1G04 v.10	
Modifications:	 Added type n 	umber 74LVC1G04GX (SOT122	26)		
	 Package outli 	ne drawing of SOT886 (Figure	12) modified.		
74LVC1G04 v.10	20111207	Product data sheet	-	74LVC1G04 v.9	
Modifications:	 Legal pages ι 	updated.			
74LVC1G04 v.9	20101026	Product data sheet	-	74LVC1G04 v.8	
74LVC1G04 v.8	20090427	Product data sheet	-	74LVC1G04 v.7	
74LVC1G04 v.7	20070827	Product data sheet	-	74LVC1G04 v.6	
74LVC1G04 v.6	20070202	Product data sheet	-	74LVC1G04 v.5	
74LVC1G04 v.5	20040907	Product specification	-	74LVC1G04 v.4	
74LVC1G04 v.4	20021002	Product specification	-	74LVC1G04 v.3	
74LVC1G04 v.3	20020513	Product specification	-	74LVC1G04 v.2	
74LVC1G04 v.2	20010119	Product specification	-	74LVC1G04 v.1	
74LVC1G04 v.1	20011121	Product specification	-	-	

Single inverter

16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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Single inverter

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74LVC1G04

18. Contents

1	General description 1
2	Features and benefits 1
3	Ordering information 2
4	Marking 2
5	Functional diagram 2
6	Pinning information 3
6.1	Pinning
6.2	Pin description 3
7	Functional description 4
8	Limiting values 4
9	Recommended operating conditions 4
10	Static characteristics 5
11	Dynamic characteristics 7
12	AC waveforms 7
13	Package outline 9
14	Abbreviations 16
15	Revision history 16
16	Legal information 17
16.1	Data sheet status 17
16.2	Definitions 17
16.3	Disclaimers
16.4	Trademarks 18
17	Contact information 18
18	Contents 19