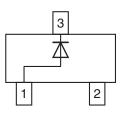
# IMBD4448-G

**Vishay Semiconductors** 



## **Small Signal Switching Diode**





### DESIGN SUPPORT TOOLS click logo to get started



### MECHANICAL DATA

Case: SOT-23

Weight: approx. 8.1 mg

#### Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

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- Silicon epitaxial planar diode
- Fast switching diode in case SOT-23, especially suited for automatic insertion.
- AEC-Q101 qualified available (part number on request)
- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





COMPLIANT HALOGEN

GREEN (5-2008)

PARTS TABLE						
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS		
IMBD4448-G	IMBD4448-G3-08 or IMBD4448-G3-18	Single	AJ	Tape and reel		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Peak reverse voltage		V <sub>RM</sub>	100	V	
Rectified current (average) half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge forward current	$t < 1$ s and $T_j = 25 \text{ °C}$	I <sub>FSM</sub>	500	mA	
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	350	mW	

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	450	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

#### Note

<sup>(1)</sup> Device on fiberglass substrate, see layout on next page

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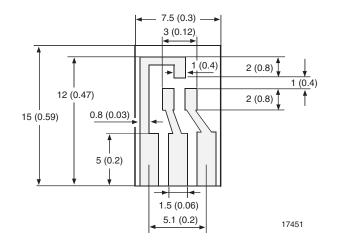
# IMBD4448-G

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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 5 mA	V <sub>F</sub>	0.62		0.72	V
r orward voltage	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1	V
	V <sub>R</sub> = 70 V	I <sub>R</sub>			2500	nA
Leakage current	$V_{R} = 70 \text{ V}, \text{ T}_{j} = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	μA
	$V_R = 25 V, T_j = 150 \ ^\circ C$	I <sub>R</sub>			30	μA
Diode capacitance	$V_F = V_R = 0 V$	CD			4	pF
Reverse recovery time	$I_{F} = 10 \text{ mA}, i_{R} = 1 \text{ mA}, V_{R} = 6 \text{ V}, R_{L} = 100 \Omega$	t <sub>rr</sub>			4	ns

### LAYOUT FOR R<sub>thJA</sub> TEST

Thickness: Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)



### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

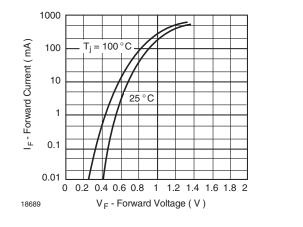


Fig. 1 - Forward Current vs. Forward Voltage

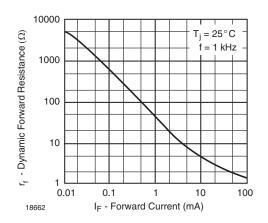
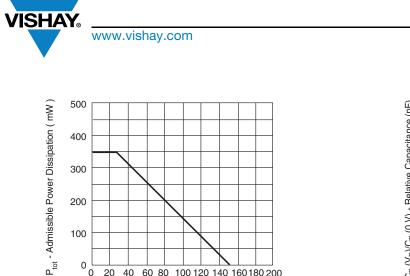


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

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20 60 80 100 120 140 160 180 200 40  $T_{amb}$  - Ambient Temperature (  $^\circ C$  )

Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

200

100

18663

0 ∟ 0

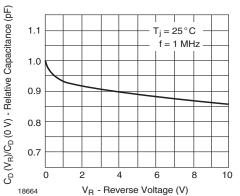


Fig. 4 - Relative Capacitance vs. Reverse Voltage

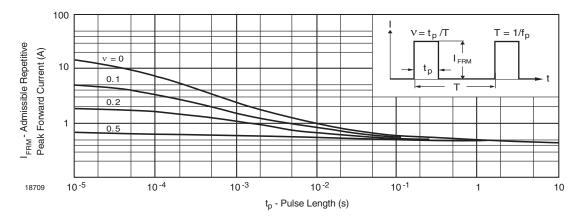


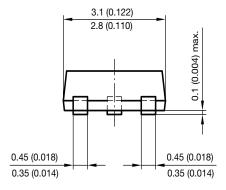
Fig. 5 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

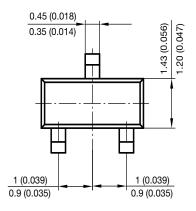
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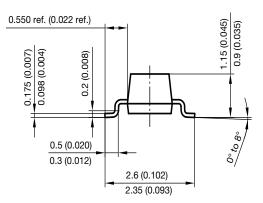


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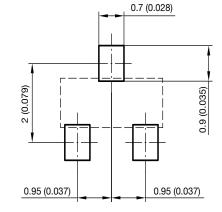
### PACKAGE DIMENSIONS in millimeters (inches): SOT-23







Foot print recommendation:



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