ineup

2SJ0164 (2SJ164)

Silicon P-channel junction FET

For switching circuits Complementary to 2SK1104

Features

- Low ON resistance
- Low-noise characteristics

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Absolute Maximum Rat	ince $T = 2^{4}$	5°C		Pin Name
Parameter	Symbol	Rating	Unit	1: Source 2: Gate
Gate-drain surrender voltage	V _{GDS}	65	V	- 3: Dram
Drain current	ID	-20	mA	001.07
Gate current	I _G	-10	mA	ં હોં હોં
Power dissipation	PD	300	mW	A A AN
Channel temperature	T _{ch}	150	S.₀C	N° . O`
Storage temperature	T _{stg}	-55 to +150	C C	and los
		S	C	

	X									
Electrical Characteristics T, +25°C ±3°C										
Parameter Symbol Symbol	Min	Тур	Max	Unit						
Gate-drain surrender whate V_{GDS} $V_{G} = 10$ K , $V_{DS} = 0$	65	196	max	V						
Drain-source current I_{DS} $V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0$	- 0.6		-6.0	mA						
Gate-source cutoff current V_{GSS} , $V_{GS} = 30 \text{ V}$, $V_{DS} = 0$			10	nA						
Gate-source cutoff voltage $V_{DS} = -10 \text{ V}, \text{ I}_D = -10 \mu\text{A}$		1.5	3.5	V						
Mutual conductance $V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}, f = 1 \text{ kHz}$	1.8	2.5		mS						
Short-circuit forware nansfer capacitance C_{iss} $V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		10		pF						
(Common source)										
Reverse transfer capacitance C_{rss}		3		pF						
(Common source)										
Drain-source ON resistance $R_{DS(on)}$ $V_{DS} = -10 \text{ mV}, V_{GS} = 0$		300		Ω						

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- 2. Observe precautions for handling. Electrostatic sensitive devices.
- 3. *: Rank classification

Rank	Р	Q	R	
I _{DSS} (mA)	- 0.6 to -1.5	-1.0 to -3.0	-2.5 to -6.0	

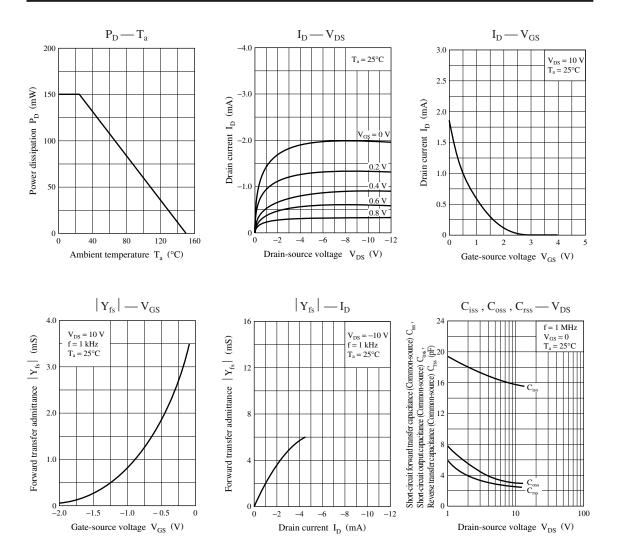
Note) The part number in the parenthesis shows conventional part number.

Package

Pin Name

 Code NS-A1

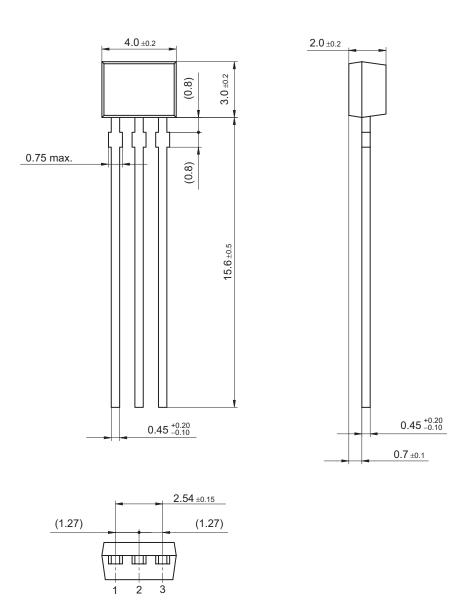
2SJ0164



Panasonic

NS-A1

Unit: mm



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