

Schottky Limiter DC - 6 GHz

Rev. V1

#### **Features**

- 3 Terminal LPF Broadband Shunt Structure
- Low Slope Resistance, 7 Ω
- +30 dBm Peak and CW Power Handling
- 0.6 dB Shunt Insertion Loss
- +20 dBm Flat Leakage Power
- Lead-Free 1.5 x 1.2 mm 6-lead TDFN Package
- RoHS\* Compliant and 260°C Reflow Compatible

### **Description**

The MADS-011010 is a Schottky limiter assembled in a lead-free 1.5 x 1.2 mm TDFN surface mount plastic package. This device provides broadband performance as well as exceptional lower flat leakage power.

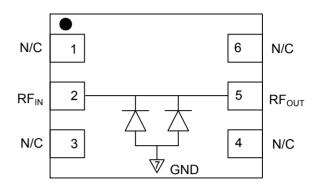
The MADS-011010 is ideally suitable for higher frequency and lower flat leakage limiter applications where higher performance surface mount diode assemblies are required.

## Ordering Information<sup>1,2</sup>

Part Number	Package
MADS-011010-14150T	3000 piece reel
MADS-011010-000SMB	Sample board

- 1. Reference Application Note M513 for reel size information.
- 2. All RF Sample boards include 5 loose parts.

#### **Functional Schematic**



Top view

## Pin Configuration<sup>3</sup>

Pin No.	Pin Name	Description	
1	N/C	No Connection	
2	RF <sub>IN</sub>	RF Input	
3	N/C	No Connection	
4	N/C	No Connection	
5	RF <sub>OUT</sub>	RF Output	
6	N/C	No Connection	
7	Paddle⁴	Ground	

- MACOM recommends connecting unused package pins to ground
- The exposed pad centered on the package bottom must be connected to RF, DC, and thermal ground.

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  China Tel: +86.21.2407.1588

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<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.



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## Electrical Specifications: $T_A = +25^{\circ}C$ , $Z_0 = 50 \Omega$ (unless otherwise defined)

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	P <sub>IN</sub> = -20 dBm @ 1.25 GHz	dB	_	0.5	0.75
P1dB Input Compression Power	@ 1 GHz	dBm	_	-2	_
Peak & CW Incident Power <sup>5</sup>	1 μs, 0.1% duty @ 1 GHz	dBm	_	+30	_
Flat Leakage Power <sup>6</sup>	+30 dBm, 1 µs, 0.1% duty @ 1 GHz	dBm	_	+22	_
Spike Leakage Power <sup>6</sup>	+30 dBm, 1 µs, 0.1% duty @ 1 GHz	dBm	_	+22	
Spike Leakage Energy <sup>6</sup>	+30 dBm, 1 µs, 0.1% duty @ 1 GHz	ergs	_	0.01	_
Recovery Time <sup>6</sup> (1 dB of Insertion Loss)	+30 dBm, 1 µs, 0.1% duty @ 1 GHz	ns	_	50	_
Input 3rd Order <sup>6</sup> Intermodulation Products	P <sub>IN</sub> = -20 dBm, F1 = 1.000 GHz, F2 = 1.010 GHz	dBm	_	+15	_
Forward Voltage (Vf)	Vf @ 1 mA	mV	_	100	_
Voltage Breakdown (Vb)	Vb @ 1 mA	V	_	3	_
Slope Resistance (Rd)	Rd @ 9.5 –10.5 mA	Ohms	_	7	_
Capacitance (Ct)	Ct @ -0.5 volts	pF	_	0.34	_

<sup>5.</sup> Both Source and Load VSWR <1.2:1 at Peak and CW Incident Power.

## **Absolute Maximum Ratings**<sup>7,8</sup>

Parameter	Absolute Maximum
Peak & CW Incident Power 1 μs pulse, 0.1% duty @ +85°C	+27 dBm
Junction Temperature	175°C
Operating Temperature	-65°C to +125°C
Storage Temperature	-65°C to +150°C

<sup>7.</sup> Exceeding any one or combination of these limits may cause permanent damage to this device.

## **Handling Procedures**

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

These devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 devices.

<sup>6.</sup> High power output parameters are tested with RF evaluation board component values defined on PCB schematic, pg 4.

MACOM does not recommend sustained operation near these survivability limits.

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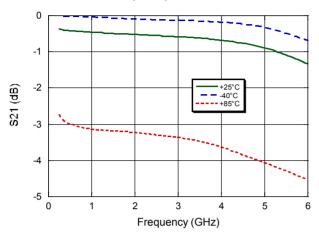


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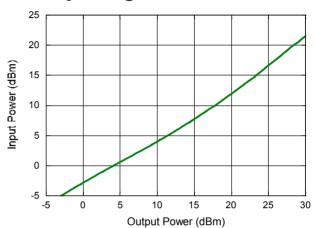
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## **Typical Performance Curves**

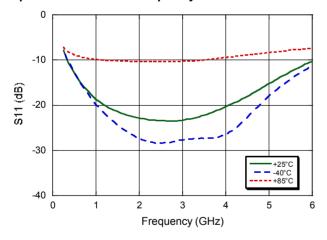
#### Insertion Loss vs. Frequency



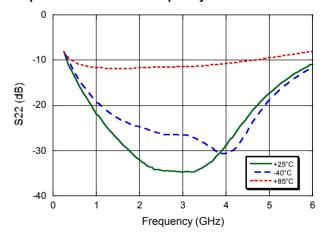
#### Flat Leakage Power @ CW, 1 GHz



#### Input Return Loss vs. Frequency



#### Output Return Loss vs. Frequency

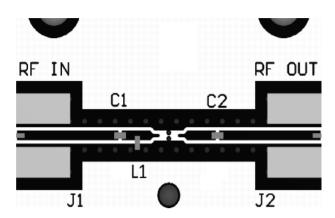




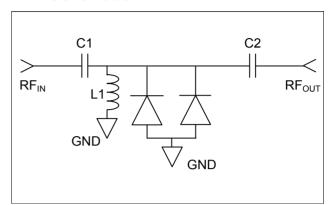
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### **PCB Layout**



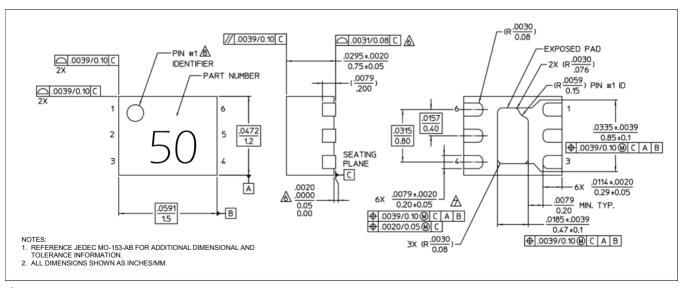
#### **PCB Schematic**



#### **Parts List**

Part	Value	Case Style
C1, C2 ( DC Blocks )	100 pF	0402
L1	27 nH	0402

### Lead-Free 1.5 x 1.2 mm 6-Lead TDFN<sup>†</sup>



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Reference Application Note \$\frac{\text{S2083}}{2083}\$ for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

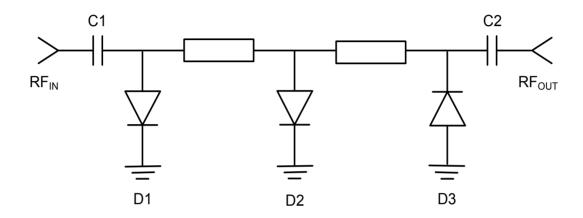


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## **Applications Section**

Schematic of 3 Stage Limiter using MADS-011010-14150T F = 1 - 4 GHz,  $P_{in} = +40$  dBm CW, +43 dBm,  $5 \mu s$ , 1% duty



#### **Parts List**

Part	Part # / Value	Case Style	Description	Quantity
D1	MADP-011029-14150T	ODS-1415	Input PIN Diode	1
D2	MADL-011021-14150T	ODS-1415	2 <sup>nd</sup> Stage PIN Diode	1
D3	MADS-011010-14150T	ODS-1415	3 <sup>rd</sup> Stage Schottky Limiter	1
C1, C2	22 pF	0402	DC Block	2