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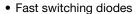
## Vishay Semiconductors

## **Small Signal Fast Switching Diode**



# FEATURES

• Silicon epitaxial planar diode



• AEC-Q101 qualified available

Base P/N-E3 - RoHS-compliant, commercial grade



 Base P/N-HE3 - RoHS-compliant, AEC-Q101 qualified

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

## MARKING (example only)



22610

Bar = cathode marking XY = type code

# **DESIGN SUPPORT TOOLS** click logo to get started



#### **MECHANICAL DATA**

Case: SOD-323

Weight: approx. 4.3 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	ORDERING CODE CIRCUIT CONFIGURATION		REMARKS	
1N4148WS	1N4148WS-E3-08 or 1N4148WS-E3-18	Single	۸۵	Tape and reel	
	1N4148WS-HE3-08 or 1N4148WS-HE3-18	Single	A2		

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	.,,	
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	v	
Average rectified current half wave rectification with resistive load (1)	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge forward current	t < 1 s and T <sub>j</sub> = 25 °C	I <sub>FSM</sub>	350		
Power dissipation (1)		P <sub>tot</sub>	200	mW	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	650	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 10 \text{ mA}$	V <sub>F</sub>			1	٧
Forward voitage	I <sub>F</sub> = 100 mA	$V_{F}$			1.2	V
	$V_R = 20 \text{ V}$	I <sub>R</sub>			25	nA
Leakage current	V <sub>R</sub> = 75 V	$I_{R}$			5	μΑ
Leakage current	V <sub>R</sub> = 100 V	$I_{R}$			100	
	$V_R = 20 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	
Diode capacitance	$V_F = V_R = 0 V$	C <sub>D</sub>			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	V
Reverse recovery time	$I_F$ = 10 mA, $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			4	ns

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

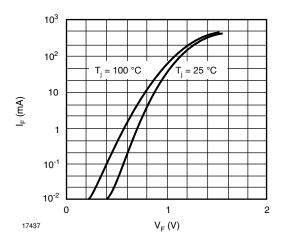


Fig. 1 - Forward Characteristics

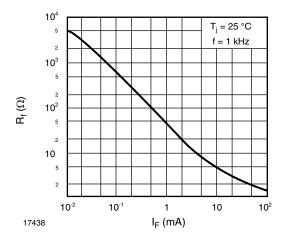


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

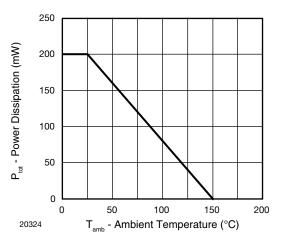


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

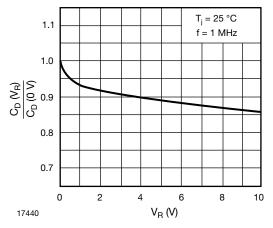


Fig. 4 - Relative Capacitance vs. Reverse Voltage



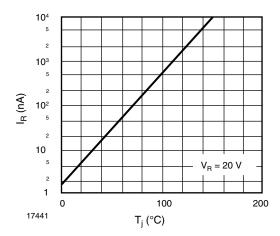


Fig. 5 - Leakage Current vs. Junction Temperature

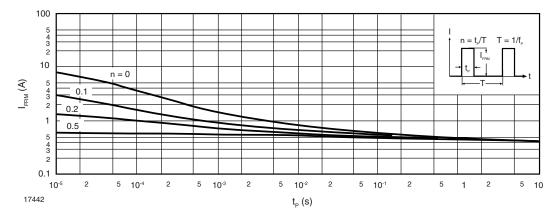
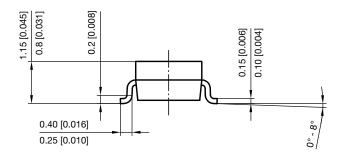


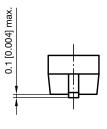
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

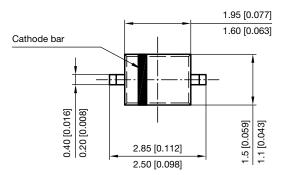


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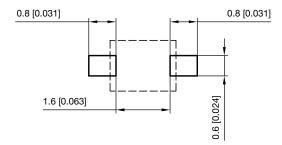
### PACKAGE DIMENSIONS in millimeters (inches): SOD-323







#### Footprint recommendation:



Document no.: S8-V-3910.02-001 (4) Created - Date: 24.August.2004 Rev. 6 - Date: 23.Sept.2016



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