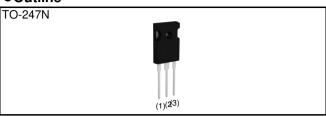
# SCT4018KE N-channel SiC power MOSFET

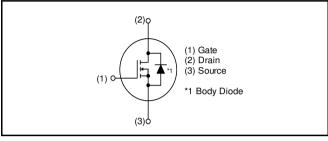
## Datasheet

V <sub>DSS</sub>	1200V
R <sub>DS(on)</sub> (Typ.)	18mΩ
	81A
P <sub>D</sub>	312W

## Outline



## Inner circuit



#### Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT4018KE

## ●Absolute maximum ratings (T<sub>vj</sub> = 25°C unless otherwise specified.)

	= · · )				
Parameter		Symbol	Value	Unit	
Drain - source voltage		V <sub>DSS</sub>	1200	V	
Continuous drain		$T_c = 25^{\circ}C$	ı ı *1	81	Α
and source current	$V_{GS} = V_{GS_{on}}$	$T_c = 100^{\circ}C$	· I <sub>D</sub> , I <sub>S</sub> *1	57	Α
Pulsed drain current	$V_{GS} = V_{GS\_on}$	$T_c = 25^{\circ}C$	I <sub>D,pulse</sub> *2	179	Α
Body diode pulsed forward current $T_c = 25^{\circ}C$		$T_c = 25^{\circ}C$	1,*3 <sup>*1,*3</sup>	81	Α
Body diode surge forward current		$V_{GS} = 0 V$	I <sub>S,pulse</sub> *1,*4	179	Α
Gate - source voltage (DC)		$V_{GSS_{DC}}$	-4 to +21	V	
Gate - source surge vol	tage (t <sub>surge</sub> < 300	)ns)	$V_{GSS\_surge}$ *5	-4 to +23	V
Recommended turn-on	gate - source dr	ive voltage	V <sub>GS_on</sub> *6	+15 to +18	V
Recommended turn-off gate - source drive voltage		V <sub>GS_off</sub>	0	V	
Virtual junction temperature		T <sub>vj</sub>	175	°C	
Range of storage temperature		T <sub>stg</sub>	-40 to +175	°C	

## 5) Simple to drive

4) Easy to parallel

Features

1) Low on-resistance

2) Fast switching speed
3) Fast reverse recovery

6) Pb-free lead plating ; RoHS compliant

## Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

## •Electrical characteristics ( $T_{vj}$ = 25°C unless otherwise specified)

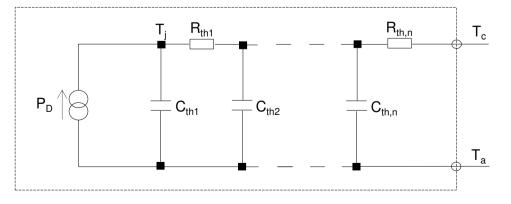
Deremeter	Cumphal	Conditions	Values			Linit	
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Drain - Source breakdown	V	$V_{GS} = 0 V, I_{D} = 18.6 mA$				V	
voltage	♥ (BR)DSS	$T_{vj} = 25^{\circ}C$	1200	-	-	v	
		$V_{GS} = 0 V, V_{DS} = 1200V$					
Zero Gate voltage Drain current	$I_{DSS}$	T <sub>vj</sub> = 25°C	-	1	80	μA	
		T <sub>vj</sub> = 150°C	-	10	-		
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +21V$ , $V_{DS} = 0V$	-	-	100	nA	
Gate - Source leakage current		$V_{GS} = -4V  , V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	$V_{GS(th)}{}^{*7}$	$V_{DS} = 10V, I_{D} = 22.2mA$	2.8	-	4.8	V	
		$V_{GS} = 18V, I_{D} = 42A$					
Static Drain - Source on - state resistance	${\sf R}_{\sf DS(on)}$ *8	T <sub>vj</sub> = 25°C	-	18.0	23.4	mΩ	
		$T_{vj} = 150^{\circ}C$	-	36.0	-		
Gate input resistance	R <sub>G</sub>	f = 1MHz, open drain	-	1	-	Ω	

#### Thermal resistance

Paramotor	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	${\sf R_{thJC}}^{*9}$	-	0.37	0.48	K/W

## •Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R <sub>th1</sub>	4.0 ×10 <sup>-2</sup>		C <sub>th1</sub>	1.2 ×10 <sup>-3</sup>	
R <sub>th2</sub>	1.6 ×10 <sup>-1</sup>	K/W	C <sub>th2</sub>	4.6 ×10 <sup>-3</sup>	Ws/K
R <sub>th3</sub>	1.7 ×10 <sup>-1</sup>		C <sub>th3</sub>	2.6 ×10 <sup>-2</sup>	





# •Electrical characteristics ( $T_{vj} = 25^{\circ}C$ unless otherwise specified)

Deveneter	Oursels al	Quaditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Transconductance	g <sub>fs</sub> *8	$V_{DS}=10V,I_{D}=42A$	-	22	-	S	
Input capacitance	$C_{iss}$	$V_{GS} = 0V$	-	4532	-		
Output capacitance	$C_{oss}$	V <sub>DS</sub> = 800V	-	129	-	pF	
Reverse transfer capacitance	$C_{rss}$	f = 1MHz	-	9	-		
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0V$ $V_{DS} = 0V$ to 800V	-	156	-	pF	
Total Gate charge	Q <sub>g</sub> *8	$V_{DS} = 800V$ $I_{D} = 42A$	-	170	-		
Gate - Source charge	Q <sub>gs</sub> *8	$V_{GS} = 18V$	-	32	-	nC	
Gate - Drain charge	Q <sub>gd</sub> *8	See Fig. 1-1, 1-2.	-	52	-		
Turn - on delay time	t <sub>d(on)</sub> *8	$V_{DS} = 800V$ $I_{D} = 42A$	-	15	-		
Rise time	t <sub>r</sub> *8	V <sub>GS</sub> = +18V / 0V	-	43	-	ns	
Turn - off delay time	t <sub>d(off)</sub> *8	$R_G = 3.3\Omega$ , L = 250µH E <sub>on</sub> includes diode	-	50	-	115	
Fall time	t <sub>f</sub> *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	14	-		
Turn - on switching loss	E <sub>on</sub> *8	See Fig. 2-1, 2-2, 2-3.	-	1100	-		
Turn - off switching loss	E <sub>off</sub> *8		-	180	-	μJ	



## •Body diode electrical characteristics (Source-Drain) (T<sub>vj</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Values	Unit	
Farameler	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	$V_{SD}^{*8}$	$V_{GS} = 0V, I_S = 42A$	-	3.3	-	V
Reverse recovery time	t <sub>rr</sub> *8	I <sub>F</sub> = 42A V <sub>B</sub> = 800V	-	37	-	ns
Reverse recovery charge	Q <sub>rr</sub> *8	v <sub>R</sub> = 800ν di/dt = 2400A/μs	-	320	-	nC
Peak reverse recovery current	I <sup>rrm *8</sup>	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	17	-	А

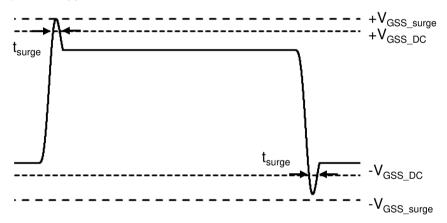
\*1 Limited by maximum  $T_{vj}$  and for Max.  $R_{thJC}$ .

\*2 Pulse width and duty cycle are limited by  $T_{vj,\text{max}}$ 

\*3 Only for body-diode, Repititive pulse, PW  $\leq$  1.5µs, Duty cycle  $\leq$  5%

\*4 When used as a protective function, PW  $\leq$  10  $\mu s$ 

\*5 Example of acceptable V<sub>GS</sub> waveform



- \*6 Please be advised not to use SiC-MOSFETs with V<sub>GS</sub> below 10V as doing so may cause thermal runaway.
- \*7 Tested after applying  $V_{GS} = 21V$  for 100ms.
- \*8 Pulsed
- \*9 Measured conformable to JESD51-14.

See the application note "rthjc\_measurement\_and\_usage\_an-e.pdf". Link

 $URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc\_measurement\_and\_usage\_an-e.pdf$ 



PW

<100ns\*

1µs\*

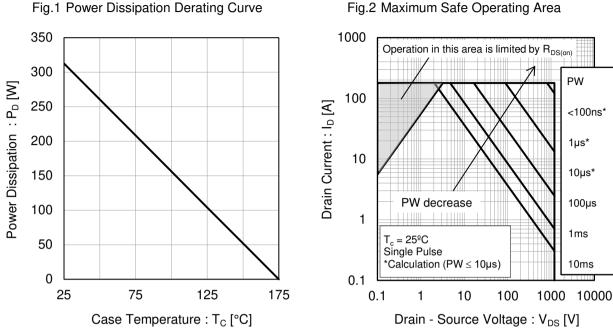
10µs\*

100µs

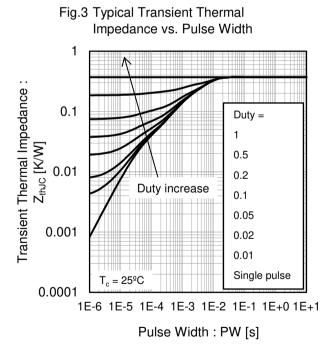
1ms

10ms

#### •Electrical characteristic curves

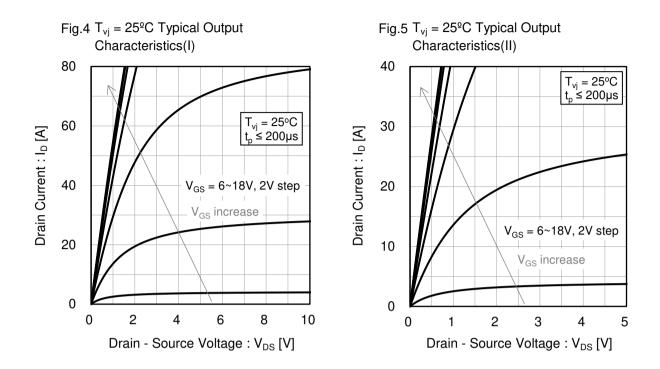


#### Fig.2 Maximum Safe Operating Area

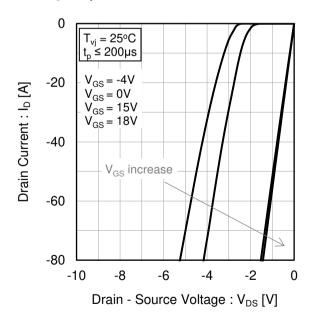


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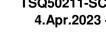
#### Fig.6 T<sub>vi</sub> = 25°C 3rd Quadrant Characteristics

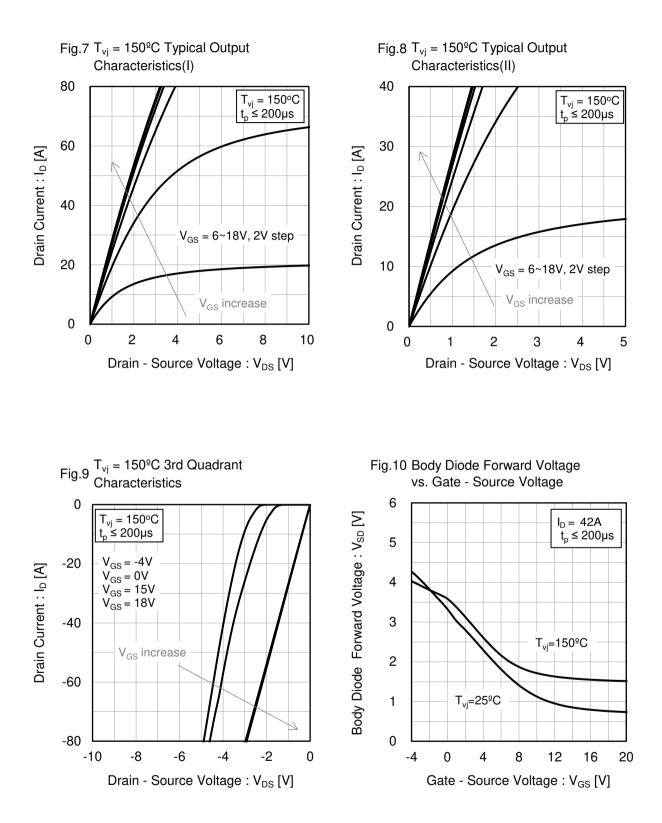




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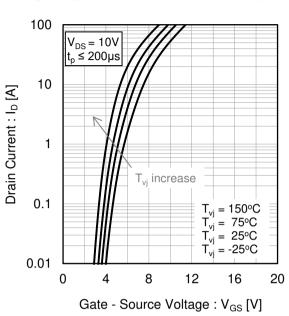
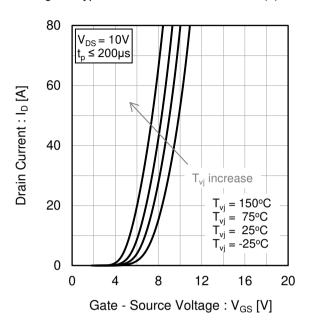


Fig.11 Typical Transfer Characteristics (I)

Fig.12 Typical Transfer Characteristics (II)



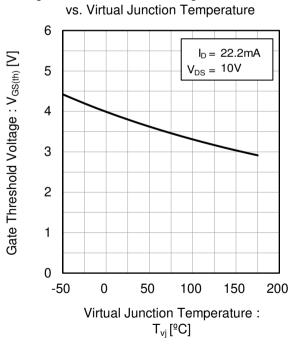


Fig.14 Transconductance vs. Drain Current

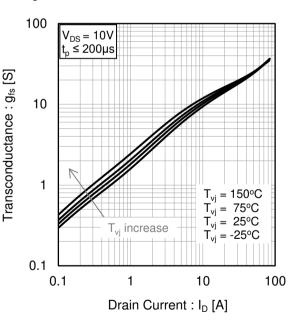
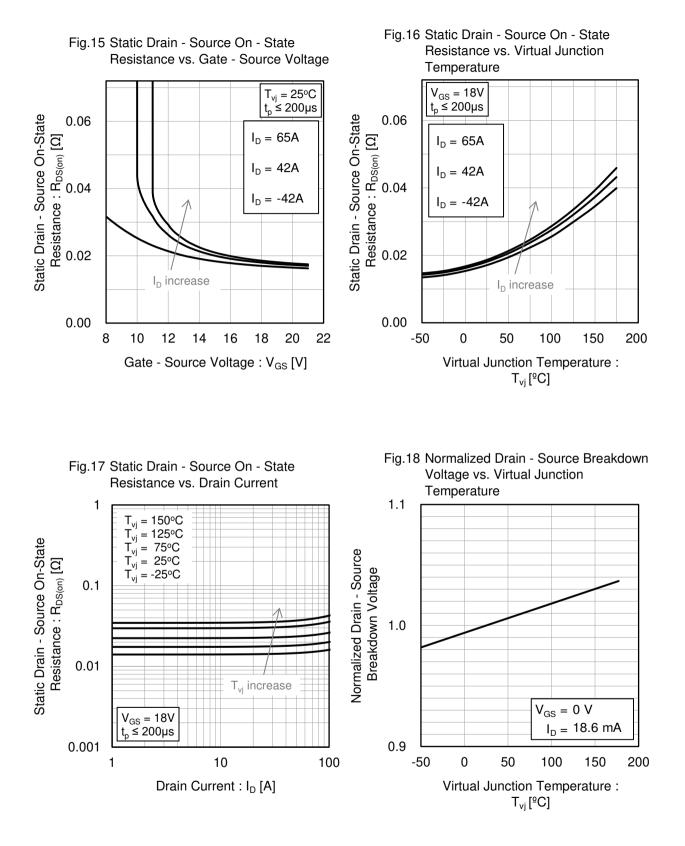


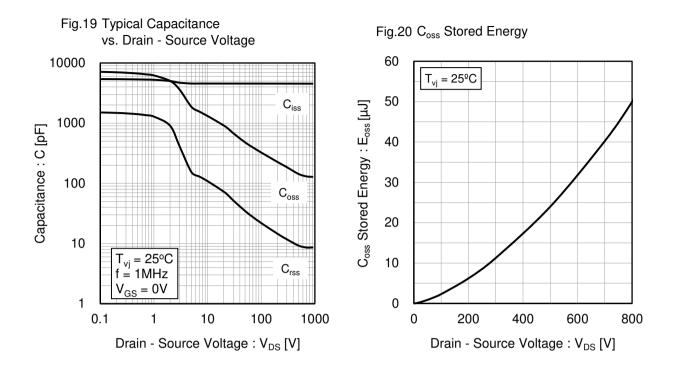
Fig.13 Gate Threshold Voltage

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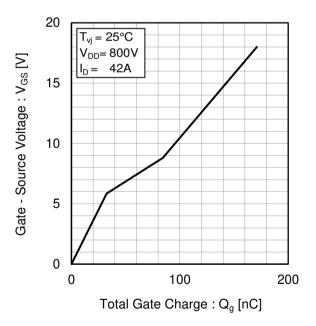




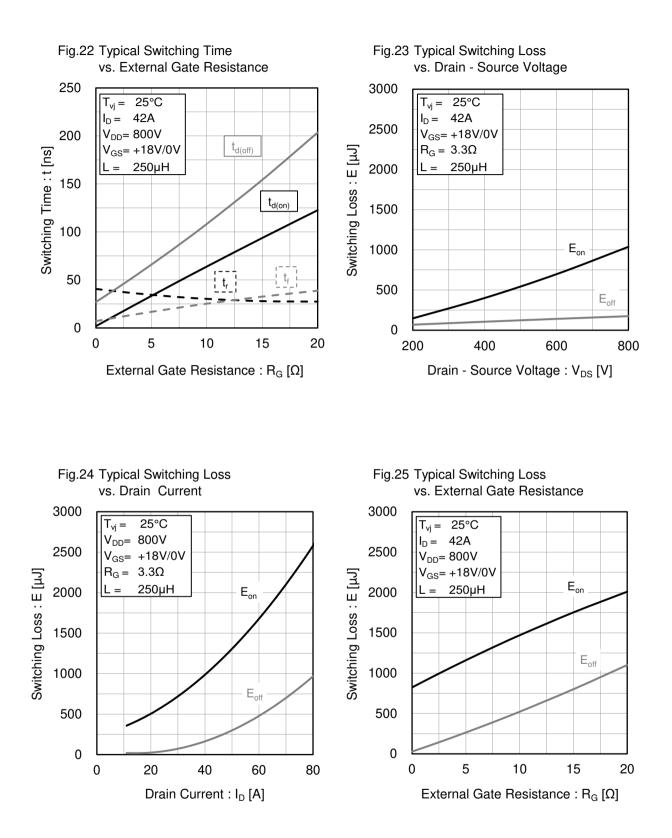




#### Fig.21 Dynamic Input Characteristics









## Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

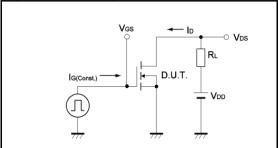
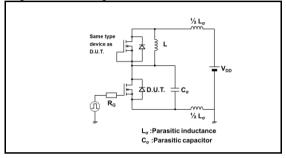


Fig.2-1 Switching Characteristics Measurement Circuit



## Fig.2-3 Waveforms for Switching Energy Loss

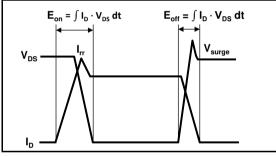
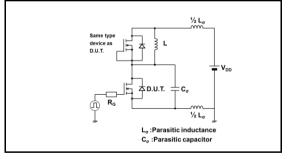
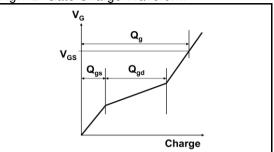


Fig.3-1 Reverse Recovery Time Measurement Circuit



#### Fig.1-2 Gate Charge Waveform





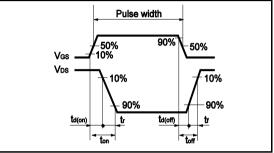
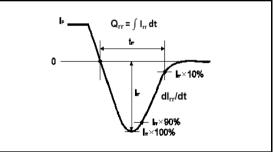
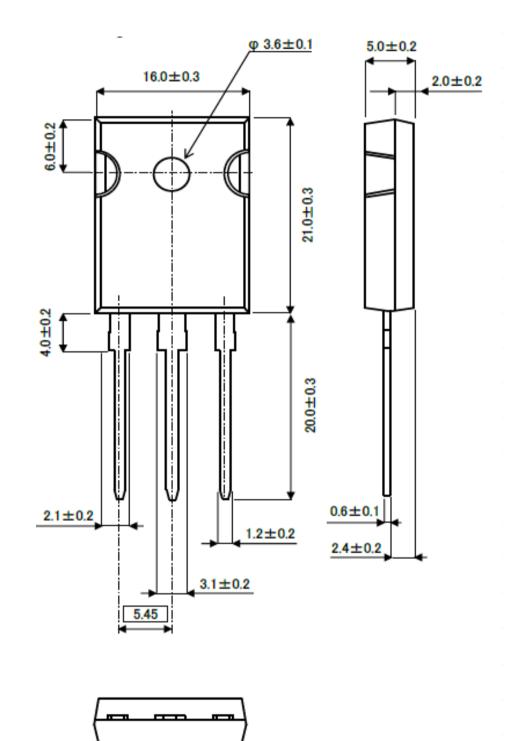


Fig.3-2 Reverse Recovery Waveform





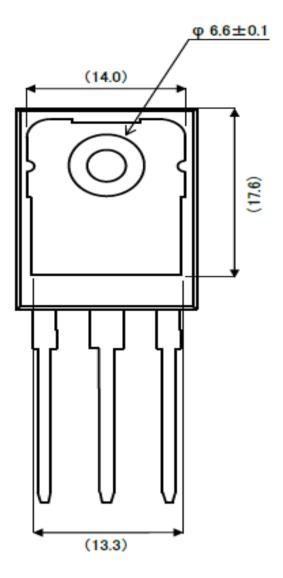
#### • Package Dimensions



Unit: mm





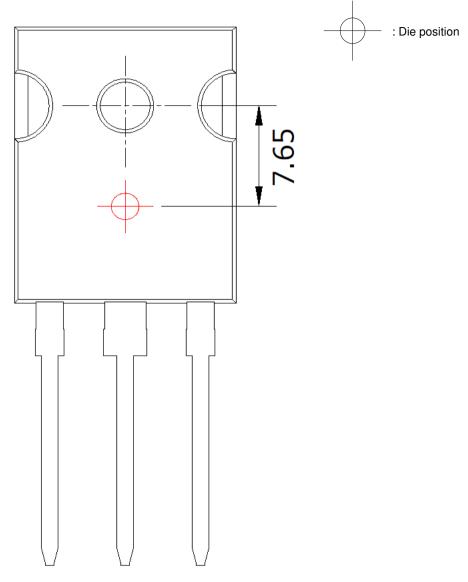


Unit: mm





#### Die Bonding Layout



•Front view of the packaging.

•Dimensions are design values.

·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm





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