Old Company Name in Catalogs and Other Documents

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RENESAS

2SK3234

Silicon N Channel MOS FET High Speed Power Switching

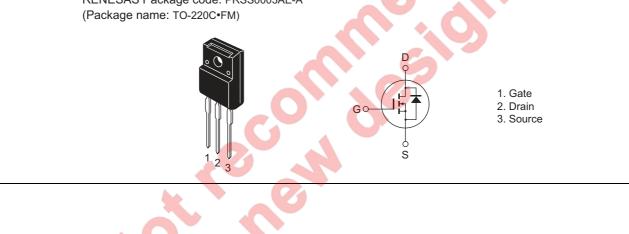
> REJ03G1097-0200 (Previous: ADE-208-1370) Rev.2.00 Sep 07, 2005

Features

- Low on-resistance: $R_{DS (on)} = 0.65 \Omega$ typ.
- Low leakage current: $I_{DSS} = 1 \propto A \max (at V_{DS} = 500 V)$
- High speed switching: $t_f = 25$ ns typ (at $V_{GS} = 10$ V, $V_{DD} = 250$ V, $I_D = 4$ A)
- Low gate charge: Qg = 25 nC typ (at $V_{DD} = 400 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 8 \text{ A}$)
- Avalanche ratings

Outline

RENESAS Package code: PRSS0003AE-A (Package name: TO-220C•FM)





Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	500	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	ID	8	А
Drain peak current	I _{D (pulse)} Note 1	32	А
Body-drain diode reverse drain current	I _{DR}	8	А
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note 1	32	А
Avalanche current	I _{AP} Note 3	8	А
Channel dissipation	Pch Note 2	35	W
Channel to case thermal impedance	θ ch-c	3.57	°C/W
Channel temperature	Tch	150	٥°
Storage temperature	Tstg	-55 to +150	°C

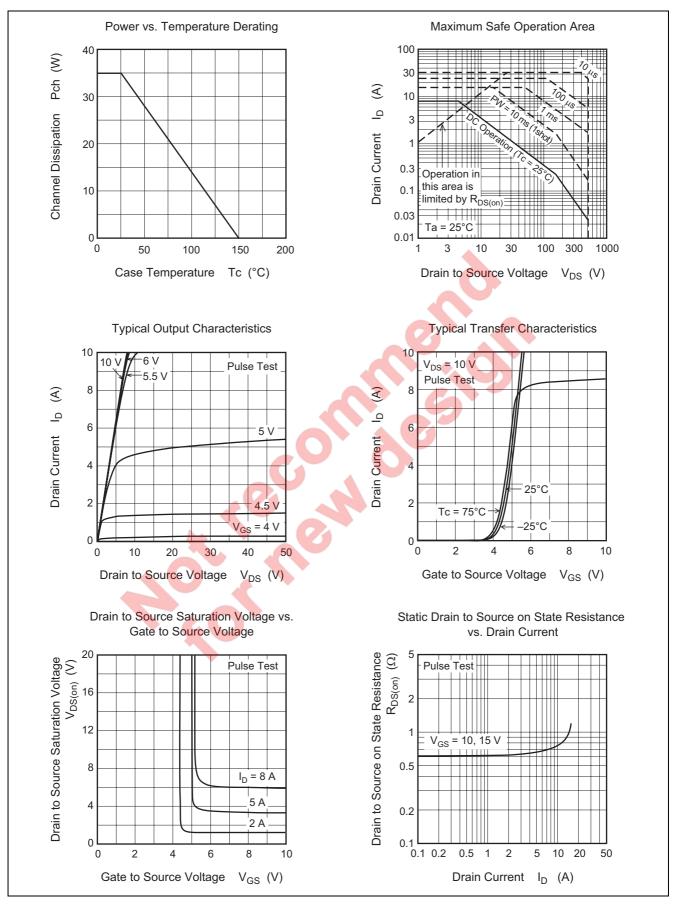
Electrical Characteristics

Notes: 1. $PW \le 10 \propto s$, duty cycle $\le 1\%$						
2. Value at $Tc = 25^{\circ}C$						
3. Tch ≤ 150°C						
Electrical Characteristics						
lie	Cumhal	Min	TIN	Mari	- I lait	$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	500	, , ,	7	V	$M_{\rm D} = 10 \text{ mA}, \text{ V}_{\rm GS} = 0$
Gate to source leak current	I _{GSS}		<u> </u>	±0.1	∝A	$V_{GS} = \pm 30 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	IDSS		_	- 1	∝A	$V_{DS} = 500 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	3.0	_	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS} (on)	_	0.65	0.85	Ω	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
Forward transfer admittance	y _{fs}	4.0	7.0	—	S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss		970	—	pF	$V_{DS} = 25 V$
Output capacitance	Coss		110	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss 🕜		18	—	pF	f = 1 MHz
Turn-on delay time	td (on)	_	25	—	ns	$I_D = 4 A$
Rise time	tr	_	21	—	ns	$V_{GS} = 10 V$
Turn-off delay time	td (off)	_	80	—	ns	$R_L = 62.5 \ \Omega$
Fall time	tr		25	—	ns	Rg = 10 Ω
Total gate charge	Qg		25	—	nC	V _{DD} = 400 V
Gate to source charge	Qgs	—	4	—	nC	V _{GS} = 10 V
Gate to drain charge	Qgd		11	_	nC	I _D = 8 A
Body-drain diode forward voltage	V _{DF}		0.9	1.35	V	$I_F = 8 A, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}		360	—	ns	$I_F = 8 A, V_{GS} = 0$
Body-drain diode reverse recovery charge	Q _{rr}	—	1.7	—	S∝C	di _F /dt = 100 A/∝s

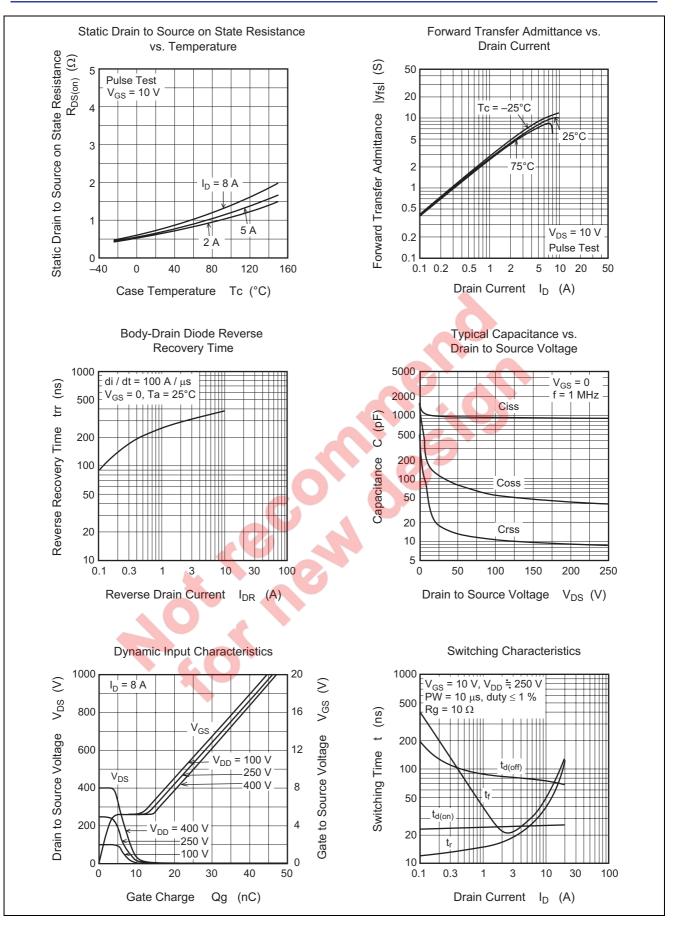
Note: 4. Pulse test



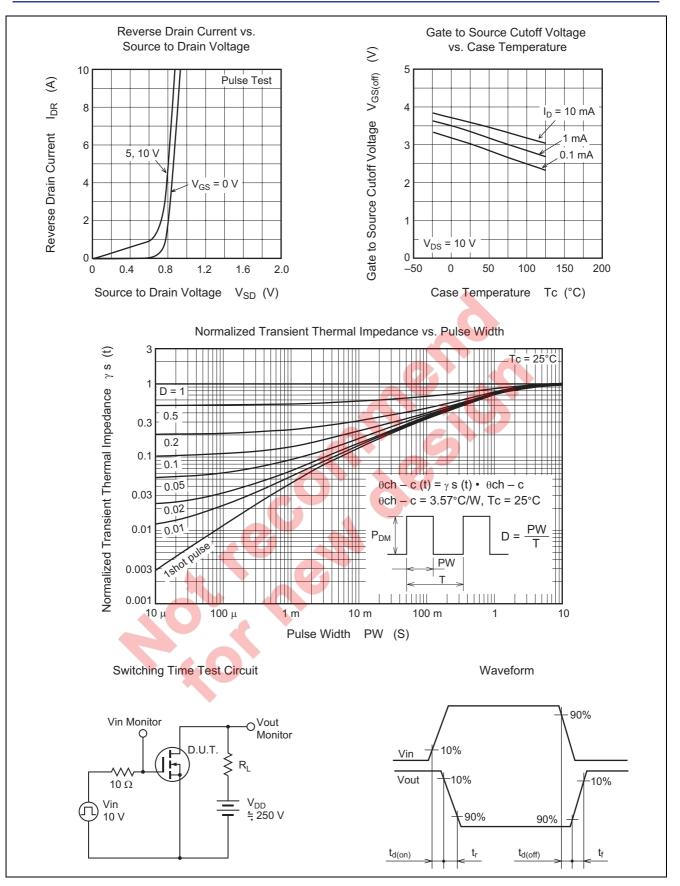
Main Characteristics



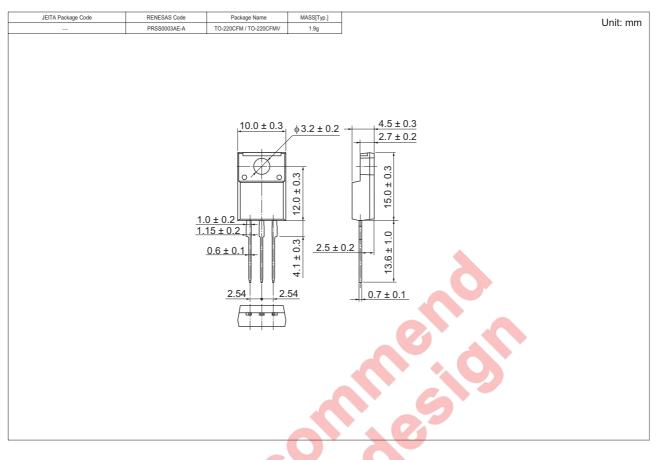








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK3234-E	600 pcs	Box (Tube)

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