VS-U5FH150FA60

Vishay Semiconductors



FRED Pt[®] Gen 5 Ultrafast Rectifier Diode, 600 V, 150 A



PRIMARY CHARACTERISTICS								
V _R	600 V							
V _F (typical) at 75 A, per diode	1.34 V							
t _{rr} (typical) at 75 A, per diode	70 ns							
$I_{F(DC)}$ per module at $T_C = 117 \ ^{\circ}C$	150 A							
Туре	Modules - diode, FRED Pt [®]							
Package	SOT-227							
Circuit configuration	Two separate diodes, parallel pin-out							

FEATURES

- Ultrafast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996 😱
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FH150FA60 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current per diode	I _F	T _C = 117 °C	75	۸		
Single pulse forward current per diode	I _{FSM}	$T_J = 25 \ ^{\circ}C$	575	A		
Maximum power dissipation per module	PD	T _C = 117 °C	215	W		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	600	-	-	
Forward voltage	V _{FM}	I _F = 75 A	-	1.34	1.7	V
		I _F = 75 A, T _J = 150 °C	-	1.12	-	
		V _R = 600 V	-	0.2	50	
Reverse leakage current	I _{RM}	T _J = 125 °C, V _R = 600 V	-	31	-	μA
		T _J = 150 °C, V _R = 600 V	-	139	-	

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T _J = 25 °C		-	70	-	20
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 75 A, di _F /dt = 1000 A/μs, V _B = 400 V	-	99	-	ns
Pool recovery ourrept		T _J = 25 °C		-	19	-	А
Peak recovery current	I _{RRM}	T _J = 125 °C		-	40	-	
		T _J = 25 °C	iii	-	0.6	-	
Reverse recovery charge Q _{rr}		T _J = 125 °C		-	2.5	-	μC
Junction capacitance	CT	V _R = 600 V, f = 1 MHz		-	64.4	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	Р		-	-	0.54	
Thermal resistance junction to case, per module	R _{thJC}		-	-	0.27	°C/W
Thermal resistance case to heatsink, per module	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	-227	

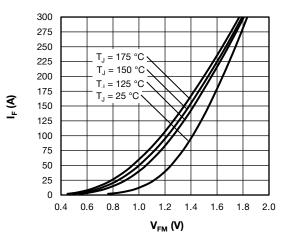


Fig. 1 - Typical Forward Voltage Drop Characteristics

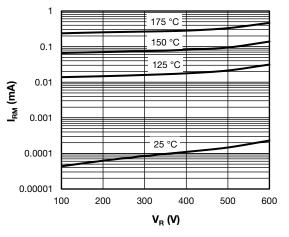


Fig. 2 - Typical Values of Reverse Current



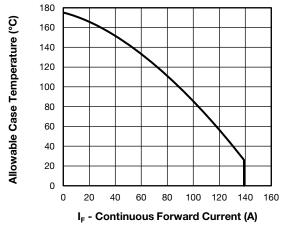


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

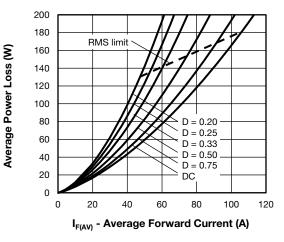


Fig. 4 - Average Power Loss vs. Average Forward Current

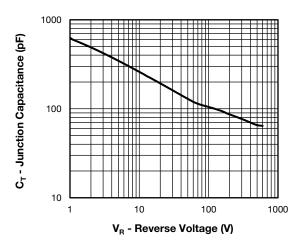


Fig. 5 - Typical Junction Capacitance vs. Reverse Voltage

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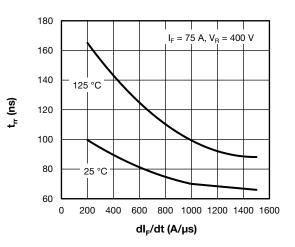


Fig. 6 - Diode Reverse Recovery Time vs. dl_Fdt

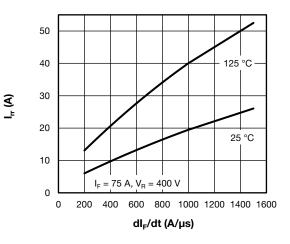


Fig. 7 - Diode Reverse Recovery Current vs. dl_Fdt

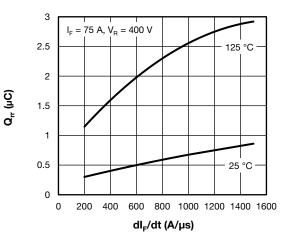


Fig. 8 - Diode Reverse Recovery Charge vs. dl_Fdt

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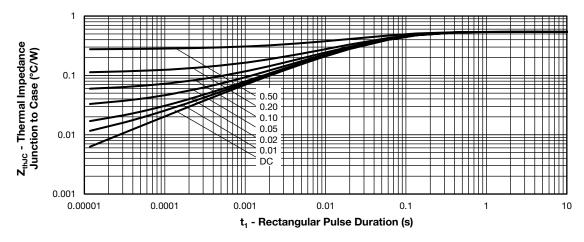


Fig. 9 - Maximum Thermal Impedance Junction to Case

ORDERING INFORMATION TABLE

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SHAY

Device code	VS-	U5F	н	150	F	Α	60	
	1	2	3	4	5	6	(7)	
	1 - 2 - 3 - 4 - 5 - 6 - 7 -	 U5F H = Cur F = Pac 	= Gen Ultrafas rent rati circuit c kage in	0	Pt [®] far Pt [®] dic nodule ation (tv (SOT-22	nily ode (150 = 1 vo sepa	rate dio	odes, parallel pin-out) ulated base)

CIRCUIT CONFI	GURATION	
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two separate diodes, parallel pin-out	F	Lead Assignment 4 1 1 1 1 1 1 1 1 1 1 1 1 1

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

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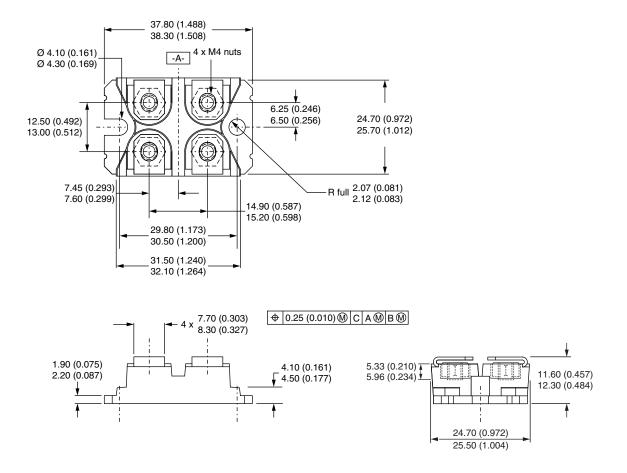
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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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