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Old Company Name in Catalogs and Other Documents

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April 1st, 2010
Renesas Electronics Corporation

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H7N1002LD, H7N1002LS, H7N1002LM

Silicon N Channel MOS FET
High Speed Power Switching

REJ03G1131-0800

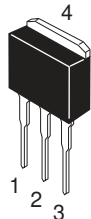
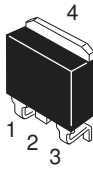
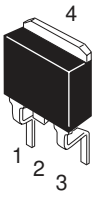
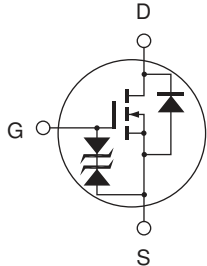
Rev.8.00

Nov 13, 2009

Features

- Low on-resistance
 $R_{DS(on)} = 8\text{ m}\Omega$ typ.
- Low drive current
- Available for 4.5 V gate drive

Outline

RENESAS Package code: PRSS0004AE-A (Package name: LDKPAK (L))	: PRSS0004AE-B : LDKPAK (S)-(1))	: PRSS0004AE-C : LDKPAK (S)-(2))	
			
H7N1002LD	H7N1002LS	H7N1002LM	1. Gate 2. Drain 3. Source 4. Drain

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	75	A
Drain peak current	$I_{D(pulse)}$ ^{Note 1}	300	A
Body to drain diode reverse drain current	I_{DR}	75	A
Avalanche current	I_{AP} ^{Note 3}	50	A
Avalanche energy	E_{AR} ^{Note 3}	166	mJ
Channel dissipation	P_{ch} ^{Note 2}	100	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Notes: 1. $PW \leq 10\ \mu\text{s}$, duty cycle $\leq 1\%$

2. Value at $T_c = 25^\circ\text{C}$

3. Value at $T_{ch} = 25^\circ\text{C}$, $R_g \geq 50\ \Omega$

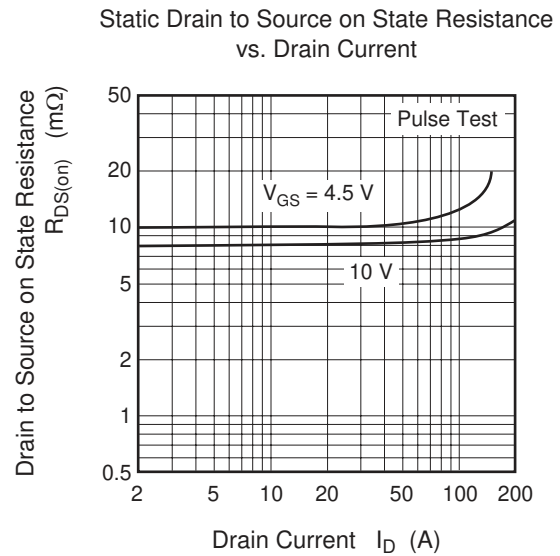
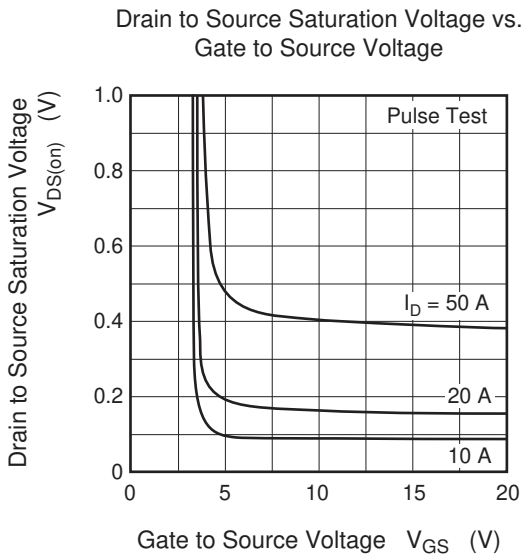
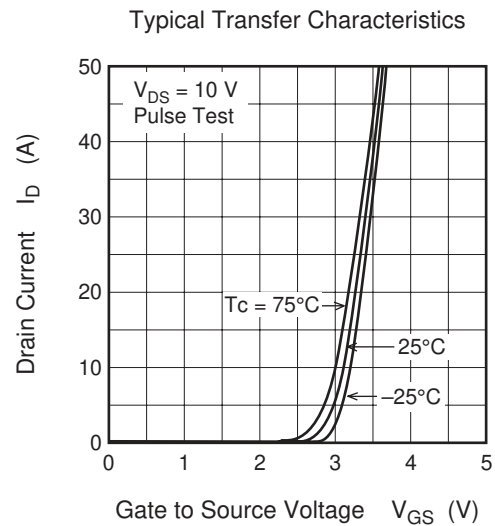
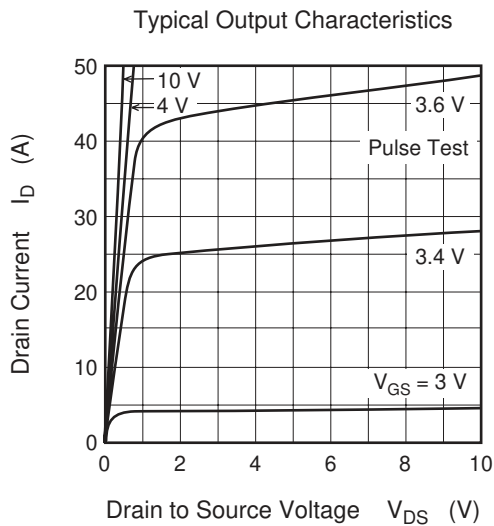
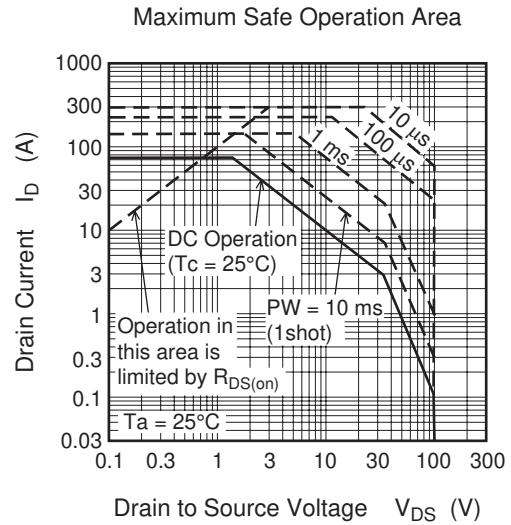
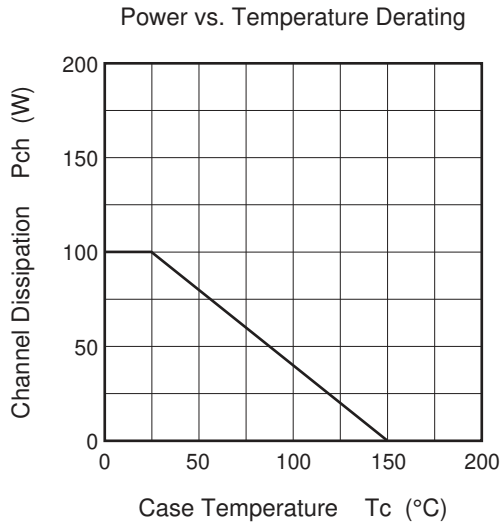
Electrical Characteristics

(Ta = 25°C)

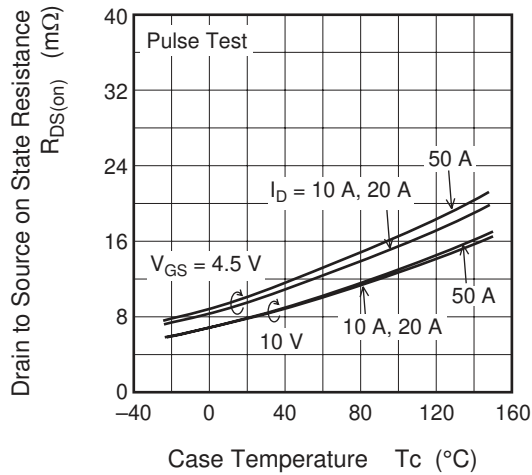
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}, V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$ ^{Note 4}
Static drain to source on state resistance	$R_{DS(on)}$	—	8	10	m Ω	$I_D = 37.5 \text{ A}, V_{GS} = 10 \text{ V}$ ^{Note 4}
		—	10	15	m Ω	$I_D = 37.5 \text{ A}, V_{GS} = 4.5 \text{ V}$ ^{Note 4}
Forward transfer admittance	$ y_{fs} $	57	95	—	S	$I_D = 37.5 \text{ A}, V_{DS} = 10 \text{ V}$ ^{Note 4}
Input capacitance	C_{iss}	—	9700	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	C_{oss}	—	740	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	330	—	pF	$f = 1 \text{ MHz}$
Total gate charge	Q_g	—	155	—	nC	$V_{DD} = 50 \text{ V}$
Gate to source charge	Q_{gs}	—	35	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Q_{gd}	—	33	—	nC	$I_D = 75 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	43	—	ns	$V_{GS} = 10 \text{ V}, I_D = 37.5 \text{ A}$
Rise time	t_r	—	245	—	ns	$R_L = 0.8 \text{ }\Omega$
Turn-off delay time	$t_{d(off)}$	—	130	—	ns	$R_g = 4.7 \text{ }\Omega$
Fall time	t_f	—	25	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.93	—	V	$I_F = 75 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	70	—	ns	$I_F = 75 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Note: 4. Pulse test

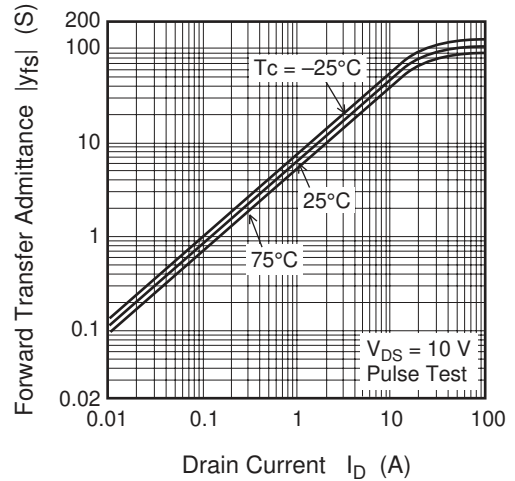
Main Characteristics



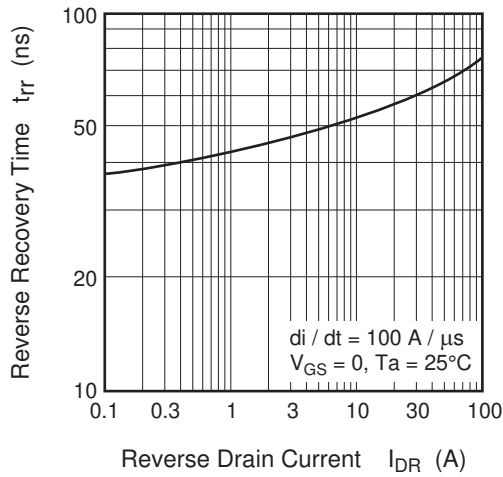
Static Drain to Source on State Resistance vs. Temperature



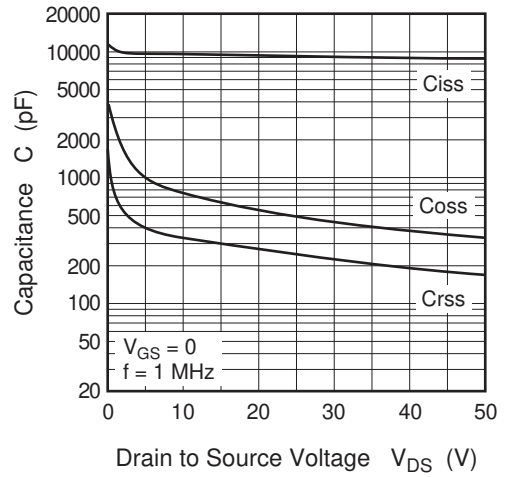
Forward Transfer Admittance vs. Drain Current



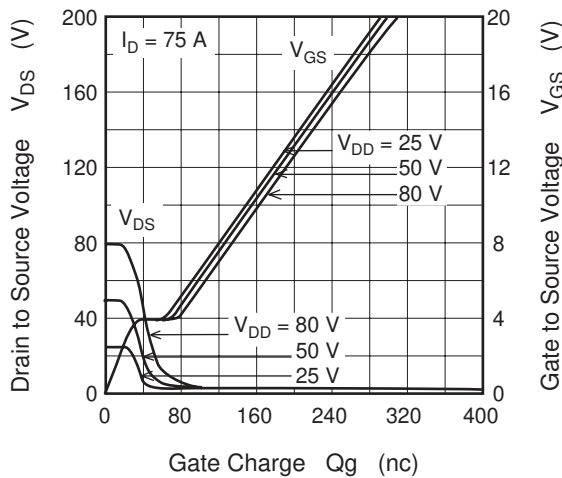
Body to Drain Diode Reverse Recovery Time



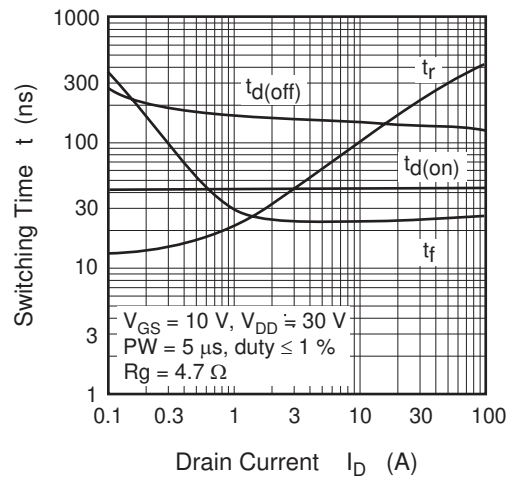
Typical Capacitance vs. Drain to Source Voltage

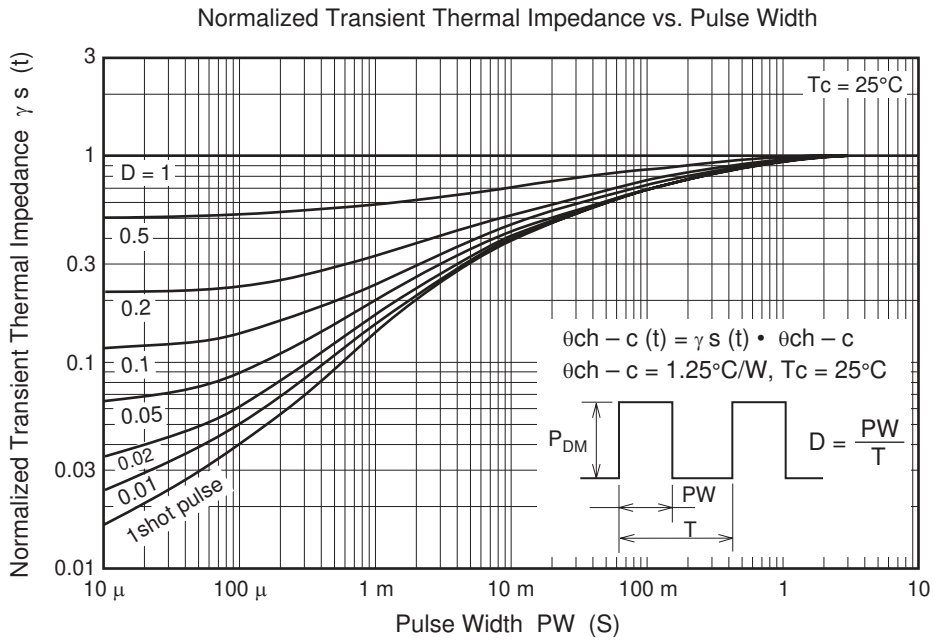
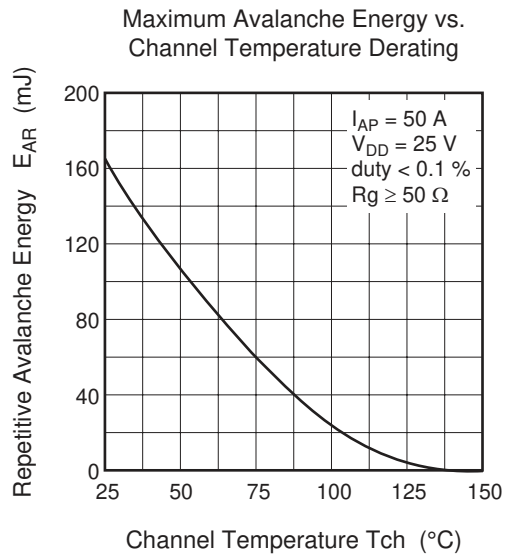
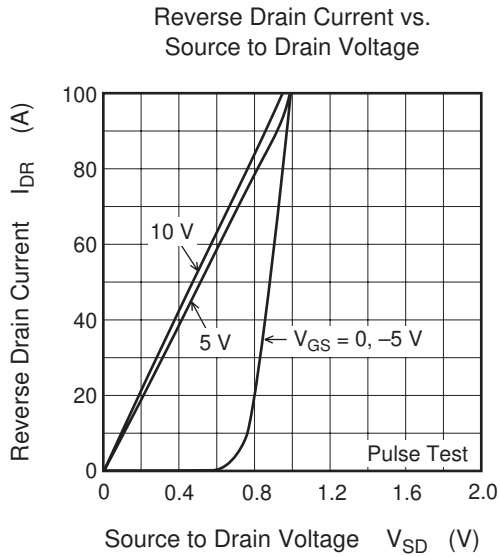


Dynamic Input Characteristics

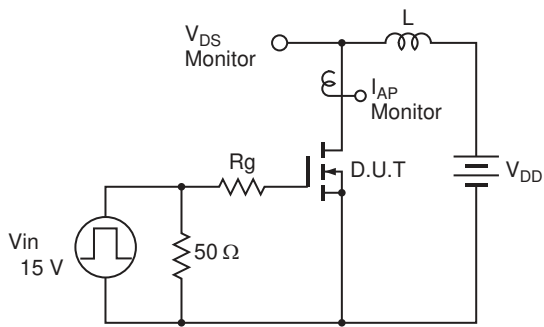


Switching Characteristics



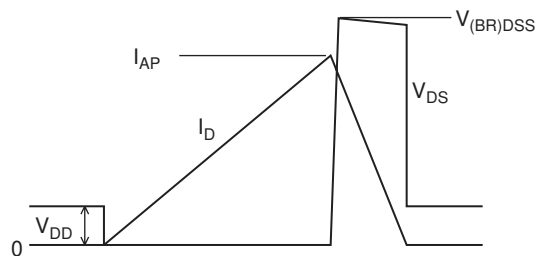


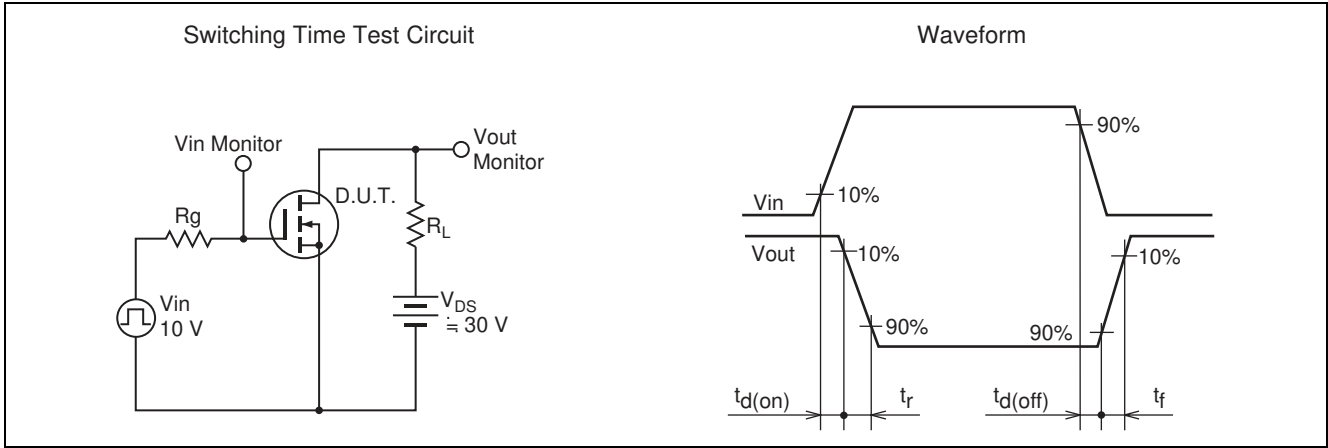
Avalanche Test Circuit



Avalanche Waveform

$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$

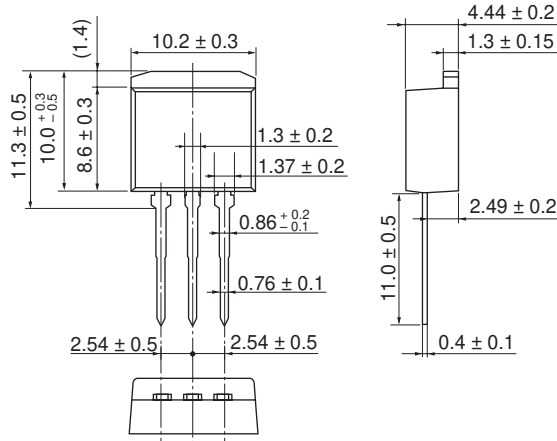




Package Dimensions

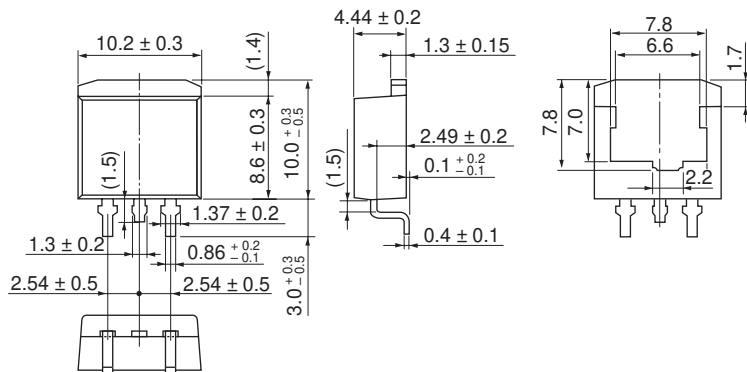
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBPAK(L)	—	PRSS0004AE-A	LDBPAK(L) / LDBPAK(L)V	1.40g

Unit: mm



Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBPAK(S)-(1)	SC-83	PRSS0004AE-B	LDBPAK(S)-(1) / LDBPAK(S)-(1)V	1.30g

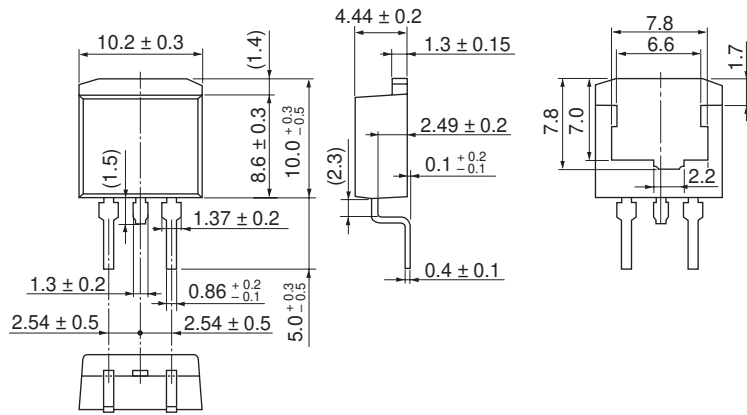
Unit: mm



H7N1002LD, H7N1002LS, H7N1002LM

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBPAK(S)-(2)	—	PRSS0004AE-C	LDBPAK(S)-(2) / LDBPAK(S)-(2)V	1.35g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
H7N1002LD-E	500 pcs	Box (Conductive Sack)
H7N1002LSTL-E	1000 pcs	Taping
H7N1002LMTL-E	1000 pcs	Taping

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450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510