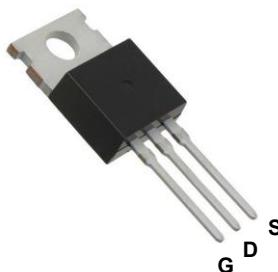
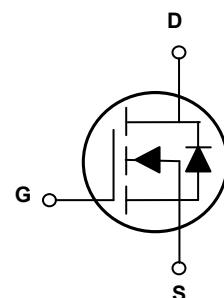


Main Product Characteristics

BV _{DSS}	500V
R _{DS(ON)}	0.8Ω
I _D	9A



TO-220



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFH5010 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V _{DS}	500	V
Gate-Source Voltage	V _{GS}	±30	V
Drain Current-Continuous (T _C =25°C)	I _D	9	A
Drain Current-Continuous (T _C =100°C)		5.7	
Drain Current-Pulsed ¹	I _{DM}	36	A
Single Pulse Avalanche Energy ²	E _{AS}	550	mJ
Single Pulse Avalanche Current ²	I _{AS}	10.5	A
Power Dissipation (T _C =25°C)	P _D	89	W
Power Dissipation-Derate above 25°C		0.71	W/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC}	1.4	°C/W
Operating Junction Temperature Range	T _J	-55 To +150	°C
Storage Temperature Range	T _{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	500	-	-	V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=500\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^\circ\text{C}$	-	-	1	μA
		$\text{V}_{\text{DS}}=400\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=100^\circ\text{C}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A}$	-	0.65	0.8	Ω
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}, \text{I}_D=250\mu\text{A}$	2	3	4	V
Forward Transconductance	g_{fs}	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=2\text{A}$	-	10	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$\text{V}_{\text{DS}}=400\text{V}, \text{I}_D=4\text{A}, \text{V}_{\text{GS}}=10\text{V}$	-	28	42	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	5.4	10	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	10.2	15	
Turn-On Delay Time ^{3,4}	$\text{t}_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=250\text{V}, \text{R}_G=25\Omega, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A}$	-	21	42	nS
Rise Time ^{3,4}	t_r		-	27	54	
Turn-Off Delay Time ^{3,4}	$\text{t}_{\text{d}(\text{off})}$		-	54	108	
Fall Time ^{3,4}	t_f		-	20	40	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1\text{MHz}$	-	920	1800	pF
Output Capacitance	C_{oss}		-	103	200	
Reverse Transfer Capacitance	C_{rss}		-	15	30	
Gate Resistance	R_g	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, \text{F}=1\text{MHz}$	-	2.4	4.8	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_s	$\text{V}_G=\text{V}_D=0\text{V}, \text{Force Current}$	-	-	9	A
Pulsed Source Current	I_{SM}		-	-	18	A
Diode Forward Voltage	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=1\text{A}, \text{T}_J=25^\circ\text{C}$	-	-	1	V

Note:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. $\text{V}_{\text{DD}}=50\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{L}=10\text{mH}, \text{I}_{\text{AS}}=10.5\text{A}, \text{R}_G=25\Omega$, starting $\text{T}_J=25^\circ\text{C}$.
3. Pulse test: pulse width $\leqslant 300\text{us}$, duty cycle $\leqslant 2\%$.
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

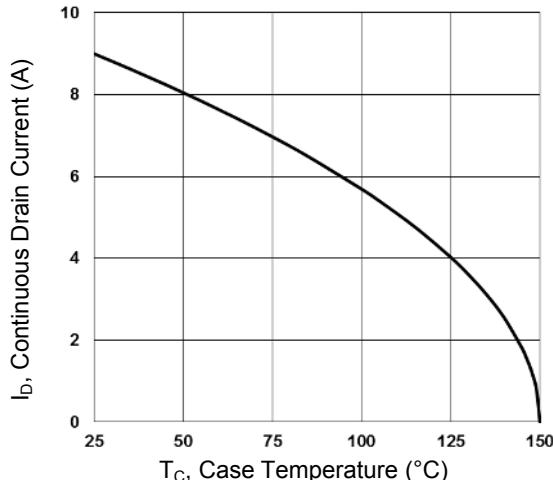


Figure 1. Continuous Drain Current vs. T_C

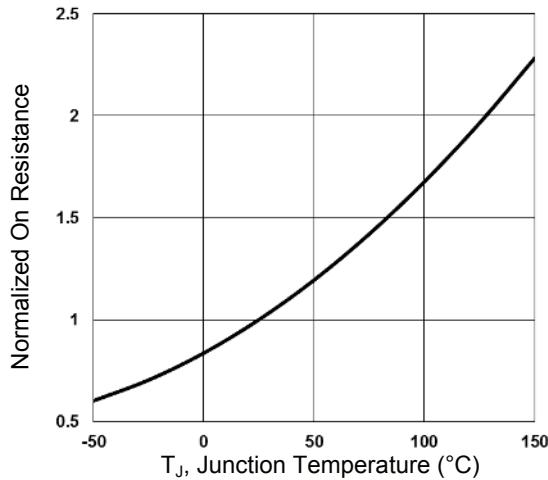


Figure 2. Normalized R_{DS(on)} vs. T_J

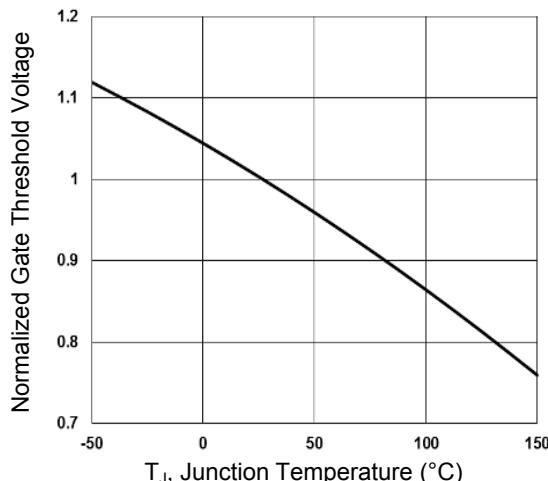


Figure 3. Normalized V_{th} vs. T_J

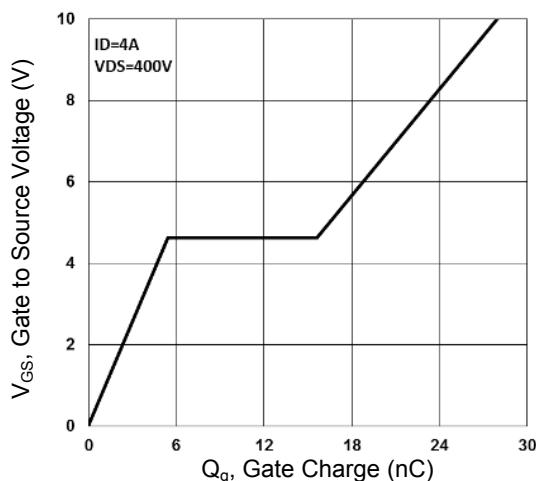


Figure 4. Gate Charge Characteristics

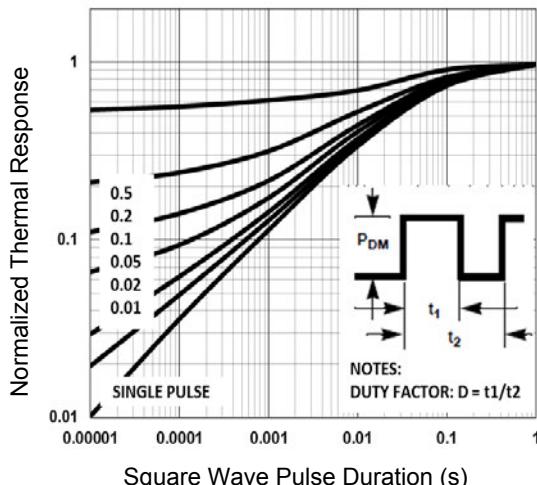


Figure 5. Normalized Transient Impedance

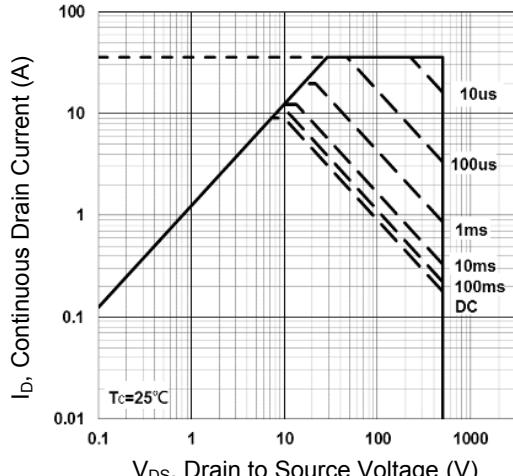


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

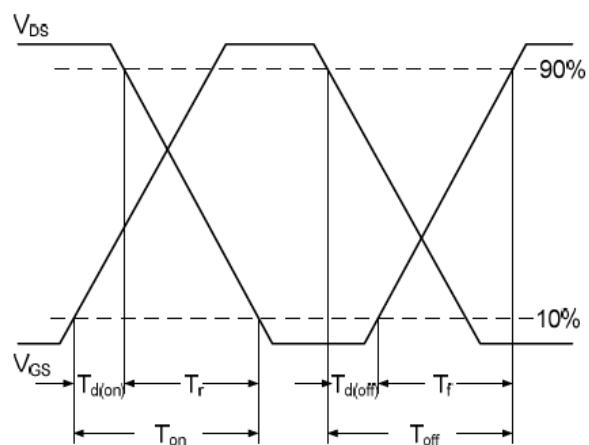


Figure 7. Switching Time Waveform

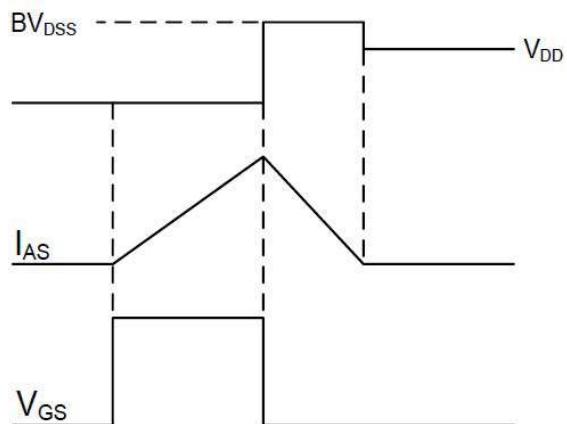
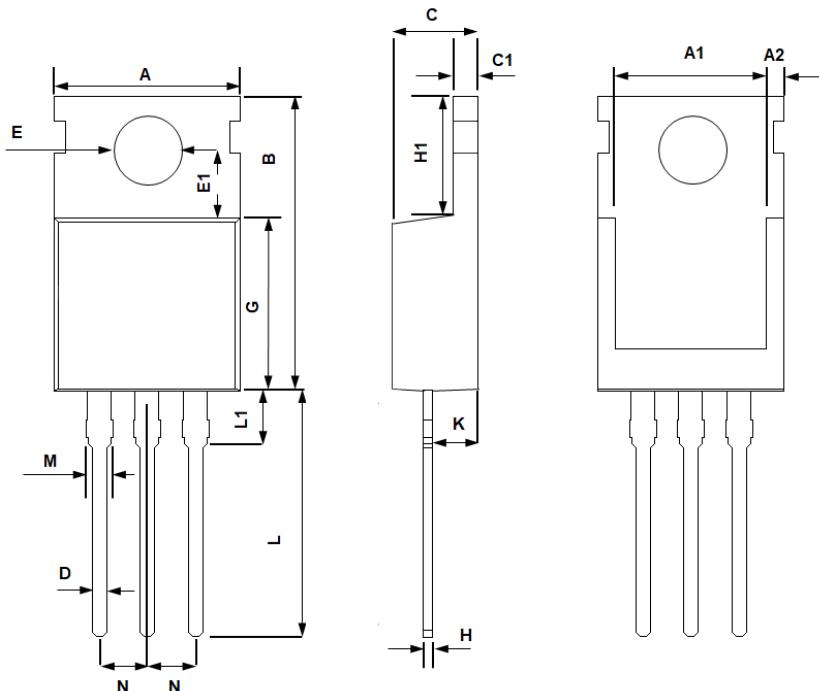


Figure 8. EAS Waveform

Package Outline Dimensions TO-220



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	9.700	10.400	0.382	0.409
A1	7.400	8.900	0.291	0.350
A2	0.800	1.400	0.031	0.055
B	14.500	16.500	0.571	0.650
C	4.200	4.750	0.165	0.187
C1	1.100	1.500	0.043	0.059
D	0.600	1.000	0.024	0.039
E	3.300	4.000	0.130	0.157
E1	3.400	3.800	0.134	0.150
G	8.400	9.400	0.331	0.370
H	0.200	0.600	0.008	0.024
H1	6.200	6.850	0.244	0.270
K	2.100	2.850	0.083	0.112
L	12.500	14.000	0.492	0.551
L1	2.700	4.000	0.106	0.157
M	1.100	1.750	0.043	0.069
N	2.440	2.640	0.096	0.104