

## AEAT-6600-T16

### 10-Bit to 16-Bit Programmable Angular Magnetic Encoder IC

#### Description

The Broadcom<sup>®</sup> AEAT-6600 angular magnetic encoder IC is a contactless magnetic rotary encoder for accurate angular measurement over a full turn of 360 degrees.

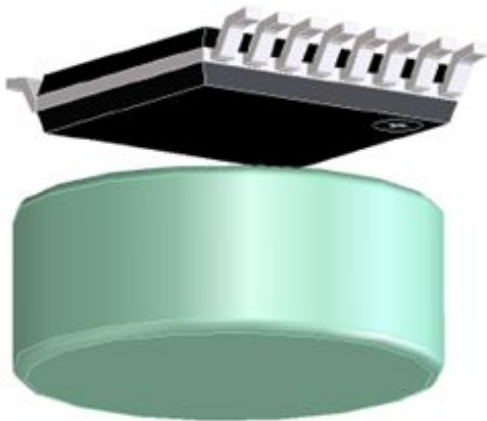
It is a system-on-a-chip, combining integrated Hall elements, an analog front end, and digital signal processing in a single device.

To measure the angle, only a simple two-pole magnet, rotating over the center of the chip, is required. The magnet may be placed above or below the IC.

The absolute angle measurement provides an instant indication of the magnet's angular position with a resolution of  $0.005^\circ = 65,536$  positions per revolution. This digital data is available as a serial bit stream and as a PWM signal.

An internal voltage regulator allows the AEAT-6600 to operate at either 3.3V or 5V supplies.

**Figure 1: AEAT-6600 Series TSSOP-16 IC Package**



#### Features

- 5V or 3.3V operation
- 3-wire or 2-wire SSI interface mode for absolute output
- Incremental ABI or UVW, and PWM output modes
- User-programmable zero position, direction, and index pulse width
- Easy magnet alignment with magnetic field strength output and alignment mode
- Power-down mode to reduce current consumption
- TSSOP-16 IC package
- RoHS compliant

#### Specifications

- Absolute 10-bit to 16-bit resolution
- Incremental output resolutions 8 to 1024 CPR
- $-40^\circ\text{C}$  to  $125^\circ\text{C}$  operating temperature range

#### Applications

- 3-phase commutation for brushless DC motor
- Resolver and potentiometer replacement
- Industrial automation and robotics

**NOTE:** This product is not specifically designed or manufactured for use in any specific device. Customers are solely responsible for determining the suitability of this product for its intended application and are solely liable for all loss, damage, expense, or liability in connection with such use. The part is not suitable for those servo motors or applications that require a fast response clockwise to counterclockwise, or vice versa.

## Definitions

**Electrical Degree (°e):** Resolution x 360 electrical degrees = 360 mechanical degrees.

**Cycle (C):** One cycle of the incremental signal is 360 mechanical degrees/resolution and is equal to 360 electrical degrees (°e).

**Cycle Error (ΔC):** The difference between the actual cycle width and the ideal cycle width corresponding to a shaft angle displacement of 1/resolution. The accumulated cycle error leads to position error.

**Pulse Width (P):** The number of electrical degrees that an output is high during one cycle, nominally 180°e or ½ a cycle.

**Pulse Width Error (ΔP):** The deviation in electrical degrees of the pulse width from its ideal value of 180°e.

**State Width (S):** The number of electrical degrees between a transition in the output of channel A and the neighboring transition in the output of channel B. There are 4 states per cycle, each nominally 90°e.

**State Width Error (ΔS):** The deviation in electrical degrees of each state width from its ideal value of 90°e.

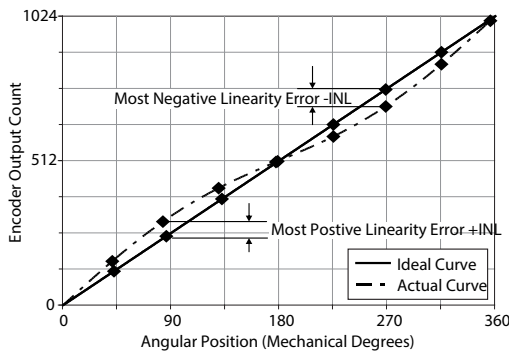
**Phase (θ):** The number of electrical degrees between the center of the high state on channel A and the center of the high state on channel B.

**Phase Error (Δθ):** The deviation in electrical degrees of the phase from its ideal value of 90°e.

**Index Pulse Width (P<sub>O</sub>):** The number of electrical degrees that an index pulse is active within the cycle that coincides with the absolute zero position. The index pulse width is also expressed in terms of LSB (least significant bit) counts corresponding to the encoder resolution.

**Integral Non-Linearity (INL):** The maximum deviation between the actual angular position and the position indicated by the encoder's output count, over one revolution. It is defined as the most positive linearity error +INL or the most negative linearity error -INL from the best fit line, whichever is larger.

Figure 2: Integral Non-Linearity Example



## Functional Description

The AEAT-6600 is manufactured with a CMOS standard process and uses Hall technology for sensing the magnetic field distribution across the surface of the chip. The integrated Hall elements are placed around the center of the device and deliver a voltage representation of the magnetic field at the surface of the IC. The digital signal processing (DSP) circuit converts the data from the Hall sensor into absolute angular position (DO/DI pin) as an absolute output or converted into digital output (A/U, B/V, I/W pins) by the incremental circuit.

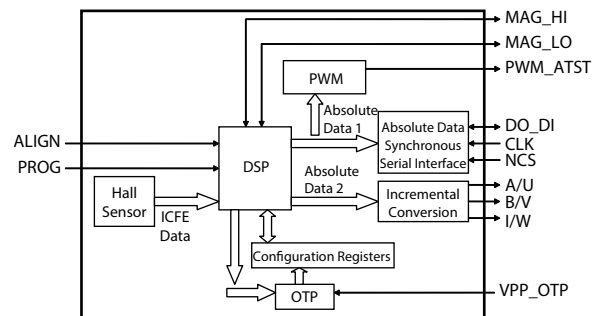
The DSP circuit also provides digital information at the outputs MagHi and MagLo that indicates movements of the used magnet toward or away from the device's surface. A small low-cost diametrically magnetized (two-pole) standard magnet provides the angular position information.

The AEAT-6600 senses the orientation of the magnetic field and calculates a 10-bit to 16-bit binary code. This code can be accessed via a Synchronous Serial Interface (SSI). In addition, an absolute angular representation is given by a pulse-width modulated signal at pin 8 (PWM). The AEAT-6600 is tolerant to magnet misalignment and magnetic stray fields due to a local measurement technique and Hall sensor conditioning circuitry.

The OTP block provides access to program to a specific resolution and output modes through a PROG pin (pin 13).

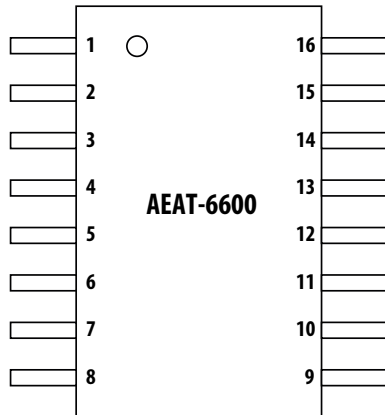
**NOTE:** For further information regarding the operating mode and application, refer to the application note (AV02-2791EN). For the programming tool and software application, refer to the user manual (AV02-2803EN).

Figure 3: Polaris Block Diagram



## Pin Assignments

Figure 4: Pin Configuration TSSOP-16



## Pinout Description

Pin	Symbol	I/O Type	Description
1	A/U	Output	Incremental A output (ABI mode) U Commutation output (UVW mode)
2	B/V	Output	Incremental B output (ABI mode) V Commutation output (UVW mode)
3	I/W	Output	Index output (ABI mode) W Commutation output (UVW mode)
4	MAG_HI/OTP_ERR	Output	1 indicates the magnetic field strength is too high (normal operation mode) 1 indicates an OTP programming error (OTP program mode)
5	MAG_LO/OTP_PROG_STAT	Output	1 indicates the magnetic field strength is too low (normal operation mode) 1 indicates OTP programming completed (OTP program mode)
6	GND	Ground	Supply Ground
7	ALIGN	Input (internal pull-down)	0: Normal operation mode 1: Alignment mode
8	PWM	Output	PWM output
9	VDD	Supply	5V Supply input (connected to VDD_F for 3.3V operation)
10	VDD_F	Supply	Filtered VDD
11	PWRDOWN	Input	0: Normal operation mode 1: Power-down mode
12	VPP	High Supply	6.5V voltage supply for OTP programming. VDD at normal operation mode
13	PROG	Input (internal pull-down)	0: Normal operation mode 1: OTP programming mode
14	NCS	Input (internal pull-up)	SSI data strobe input
15	CLK	Input	SSI clock input
16	DO/DI	Input/Output (tristate)	SSI data output (Absolute Output mode) Serial data input (OTP Program mode)

**Table 1: Absolute Maximum Ratings**

Parameter	Symbol	Min.	Max.	Units	Notes
Storage Temperature	T <sub>S</sub>	-40	125	°C	—
DC Supply Voltage					
VDD Pin	VDD	-0.3	7	Volts	—
VPP Pin	VPP	-0.3	7		
Input Voltage Range	V <sub>in</sub>	-0.25	VDD + 0.25	Volts	—

**CAUTION!** Subjecting the product to stresses beyond those listed under this section may cause permanent damage to the devices. These are stress ratings only and do not imply that the devices will function beyond these ratings. Exposure to the extremes of these conditions for extended periods may affect product reliability.

**Table 2: Recommended Operating Conditions**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Operating Ambient Temperature	T <sub>A</sub>	-40	—	125	°C	—
DC Supply Voltage to VDD Pin						
5V Operation	VDD	4.5	5.0	5.5	Volts	VDD pin tied to VDD_F pin for 3.3V operation.
3.3V Operation		3.0	3.3	3.6		
OTP Programming Voltage at VPP Pin	VPP	6.3	6.5	6.7	Volts	VPP tied to VDD during normal operation mode
Incremental Output Frequency	f <sub>MAX</sub>	—	—	512	kHz	Frequency = Velocity (RPM) x Resolution/60 Max RPM = 30,000 RPM
Load Capacitance	C <sub>L</sub>	—	—	50	pF	—

**Table 3: Electrical Characteristics**

Condition: Electrical characteristics over the recommended operating conditions. Typical values specified at VDD = 5.0V and 25°C.

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
<b>Current Consumption</b>						
Supply Current						
Normal Operation Mode	I <sub>DD</sub>	—	17	21	mA	—
Power-Down Mode	I <sub>PD</sub>	—	—	100	µA	—
OTP Programming Current	I <sub>PP</sub>	—	—	2	mA	VPP supply pin
<b>Digital Outputs (DO)</b>						
High-Level Output Voltage	V <sub>OH</sub>	VDD - 0.5	—	—	Volts	Normal operation
Low-Level Output Voltage	V <sub>OL</sub>	—	—	GND + 0.4	Volts	—
Output Leakage Current	I <sub>OZ</sub>	-1	—	1	µA	—
Power-Up Time:	t <sub>PwrUp</sub>	—	—	11	ms	—
Absolute Output: 10-bits				11		
Incremental Output: 12-bits				11		
PWM Output: 14-bits				11		
PWM Output: 16-bits				11		
<b>Digital Inputs (DI)</b>						
Input High Level	V <sub>IH</sub>	0.7 x VDD	—	—	Volts	—
Input Low Level	V <sub>IL</sub>	—	—	0.3 x VDD	Volts	—
Input Leakage Current	I <sub>LEAK</sub>	-1	—	1	µA	CLK, DI pins
Pull-Up Low-Level Input Current	I <sub>IL</sub>	—	—	30	µA	NCS pin
Pull-Down High-Level Input Current	I <sub>IH</sub>	—	—	30	µA	ALIGN, PROG

**Table 4: Encoding Characteristics**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
<b>Absolute Output</b>						
Resolution	RES	10	—	16	Bit	10 and 16 bits (Slow Mode) 10 and 14 bits (Fast Mode)
Integral Non-Linearity (optimum)	INL <sub>nom</sub>	—	±0.4	±0.9	Deg.	Maximum error with respect to the best line fit. Verified at the nominal mechanical magnet placement. Tamb = 25°C
Integral Non-Linearity	INL	—	—	±1.9	Deg.	Best line fit = (Err <sub>max</sub> – Err <sub>min</sub> )/2 Over displacement tolerance with 9-mm diameter magnet, Tamb = –40 to +125°C
Output Sampling Rate	f <sub>s</sub>	—	12	—	kHz	See Table 5 for the AEAT-6600-T16 internal sampling time
<b>Incremental Output (Channel ABI)</b>						
Resolution	R <sub>INC</sub>	8	—	1024	CPR	Options 8, 16, 32, 64, 128, 256, 512, or 1024 CPR
Index Pulse Width	P <sub>O</sub>	90	—	360	°e	Options: 90, 180, 270, or 360 °e
Cycle Error	ΔC	—	7	60 80 100	°e	8, 16, 32, 64, 128 CPR 256 CPR 512, 1024 CPR
Pulse Width Error	ΔP	—	5	40 50 60	°e	8, 16, 32, 64, 128 CPR 256 CPR 512, 1024 CPR
State Width Error	ΔS	—	3	40 50 60	°e	8, 16, 32, 64, 128 CPR 256 CPR 512, 1024 CPR
Phase Error	Δθ	—	2	20 25 30	°e	8, 16, 32, 64, 128 CPR 256 CPR 512, 1024 CPR
Index Pulse Width Error	P <sub>O</sub>	60 150 240 330	90 180 270 360	120 210 300 390	°e	Index Pulse Width Gated 90°e Index Pulse Width Gated 180°e Index Pulse Width Gated 270°e Index Pulse Width Gated 360°e
Velocity	—	1	—	30,000	RPM	—
<b>NOTE:</b> The encoding characteristics above are based on 12-bit resolution.						
<b>Commutation Characteristic (Channel U, V, W)</b>						
Commutation Format	Four-phase 1, 2, 4, or 8 pole pairs					
Commutation Accuracy	ΔUVW	–2	—	+2	°mechanical	—
Velocity	1, 2, 4, 8 poles	1	—	30,000	RPM	—
<b>PWM Output</b>						
PWM Frequency 10 bits	f <sub>PWM</sub>	3040	3800	4560	Hz	—
Minimum Pulse Width 10 bits	PW <sub>MIN</sub>	0.8	1	1.2	μs	—
Maximum Pulse Width 10 bits	PW <sub>MAX</sub>	210	263	315	μs	—
<b>NOTE:</b> The encoding characteristics are over the recommended operating range unless otherwise specified.						

**Table 5: Encoding Timing Characteristics**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
<b>Absolute Output</b>						
System Refresh Time						
10-bit	$t_{\text{Refresh}}$	—	—	111	$\mu\text{s}$	First SSI absolute output upon power-up
12-bit		—	—	111	$\mu\text{s}$	
14-bit		—	—	111	$\mu\text{s}$	
16-bit		—	—	111	$\mu\text{s}$	
System Reaction Time (Fast Mode)						
10-bit	$t_{\text{Fast}}$	—	—	111	$\mu\text{s}$	No averaging reaction time
12-bit		—	—	111	$\mu\text{s}$	
14-bit		—	—	111	$\mu\text{s}$	
System Reaction Time (Slow Mode)						
10-bit	$t_{\text{Slow}}$	—	—	111	$\mu\text{s}$	Averaging reaction time
12-bit		—	—	442	$\mu\text{s}$	
14-bit		—	—	7.1	ms	
16-bit		—	—	113	ms	
<b>Incremental Output (ABI &amp; UVW)</b>						
System Reaction Time (Fast Mode)	$t_{\text{Inc.}}$	—	—	720	$\mu\text{s}$	(for 400 to 1800 RPM)
		—	—	310	$\mu\text{s}$	(for 1801 RPM and above)

**NOTES:**

The  $t_{\text{Refresh}}$ ,  $t_{\text{Fast}}$ ,  $t_{\text{Slow}}$ ,  $t_{\text{Inc.}}$  are the AEAT-6600-T16 internal sampling time.

Slow Mode is not recommended for incremental output. Contact the factory for Slow Mode application on incremental output.

Contact the factory for Fast Mode 16-bit application.

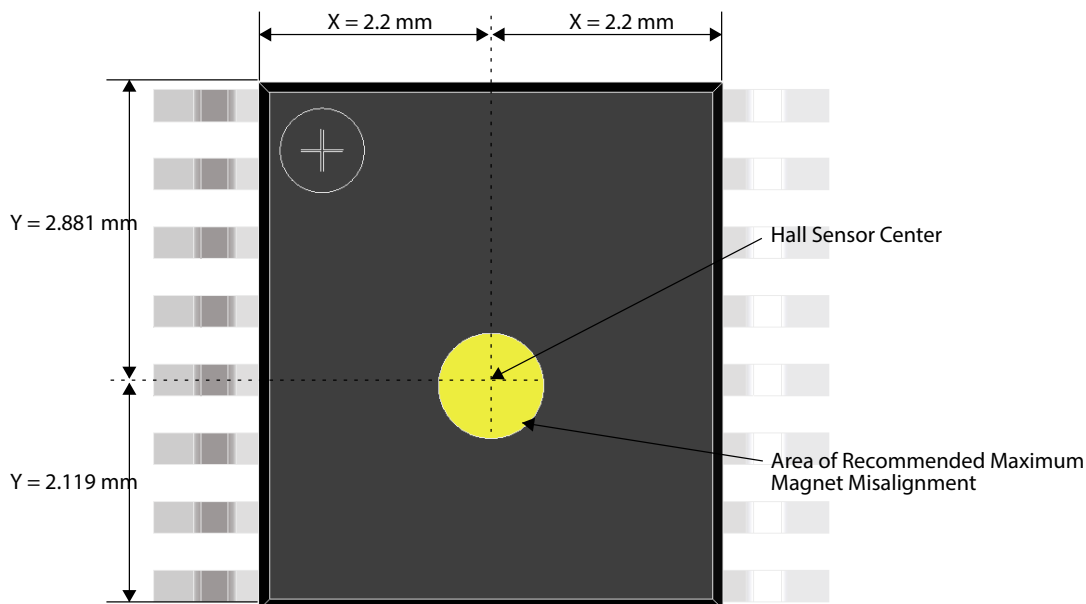
**Table 6: Recommended Magnetic Input Specifications**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Diameter	d	—	9	—	mm	Recommended magnet: Cylindrical magnet, diametrically magnetized and one pole pair.
Thickness	t	—	3	—	mm	
Magnet Radial Magnetic Flux Density	B_radial	188	198	208	mT	Measured at 1.3 mm away from the center of the magnet radial surface. Magnet validation purpose.
Magnet Plane Magnetic Flux Density	B_plane	106	112	118	mT	B_plane at 1.3 mm from the magnet flat surface. Hall sensor required plane components magnetic field.
Magnetization Vector Tilt	Mag_Vec	—	—	± 5	—	Magnet magnetization vector tilt.
Magnet Displacement Radius	R_m	—	—	0.1	mm	Displacement between the magnet axis and the rotational axis.
Hall Sensor Displacement Radius	R_s	—	—	0.5	mm	Displacement between the Hall sensor axis and the rotational axis.
Recommended Magnet Material and Temperature Drift	—	—	-0.11	—	%/°C	NdFeB (Neodymium Iron Boron), grade N35SH.

**DISCLAIMER:** The above information is based on the specification provided by the supplier of the magnet used for product characterization. The supplier of the magnet is solely responsible for the specification and performance of the magnet used.

## Magnet and IC Package Placement

**Figure 5: Magnet and IC Package Placement**



The magnet's center axis should be aligned within a displacement radius of 0.5 mm from the defined Hall sensor center.

# Defined Chip Sensor Center and Magnet Displacement Radius

Figure 6: Defined Chip Sensor Center and Magnet Displacement Radius

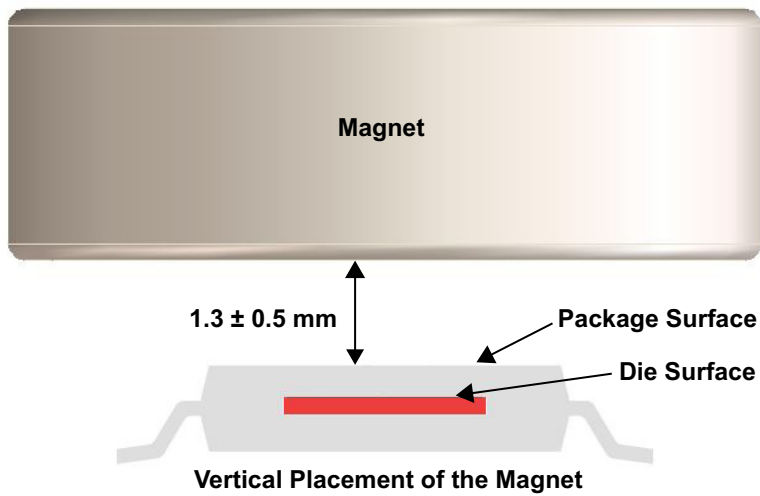


Table 7: SSI Timing Characteristics

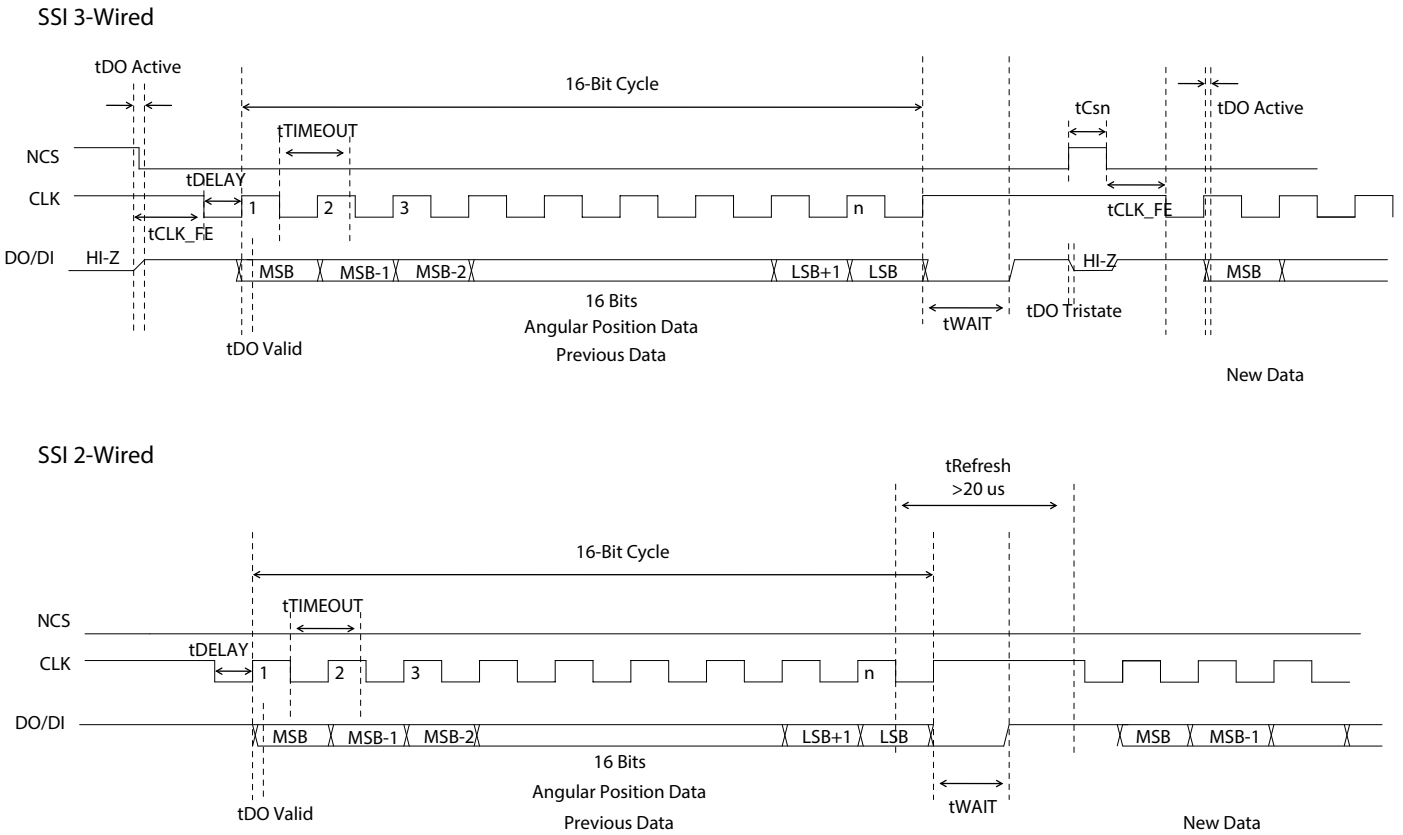
Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
fclk	—	—	—	1000	kHz	—
tCLK FE	—	—	—	500	ns	Minimum time required for the encoder to freeze data and prepare shift registers before receiving the first rising edge to prompt the MSB.
tDO Active	—	—	100	—	ns	—
tDO Valid	—	—	50	—	ns	—
tCSn	—	—	500	—	ns	—
tDO Tristate	—	—	100	—	ns	—
tDELAY	—	—	500	—	ns	Minimum time required for the encoder to freeze data and prepare shift registers before receiving the first rising edge to prompt the MSB.
tRefresh	—	20	—	—	$\mu\text{s}$	Required waiting time to refresh position data between subsequent position reads.
tTIMEOUT	—	—	—	20	$\mu\text{s}$	Every falling edge of the clock.
tWAIT	—	—	—	10	$\mu\text{s}$	Maximum time to hold DO to low.

**NOTE:** SSI timing characteristics are over the recommended operating range unless otherwise specified.



# SSI Timing Diagram

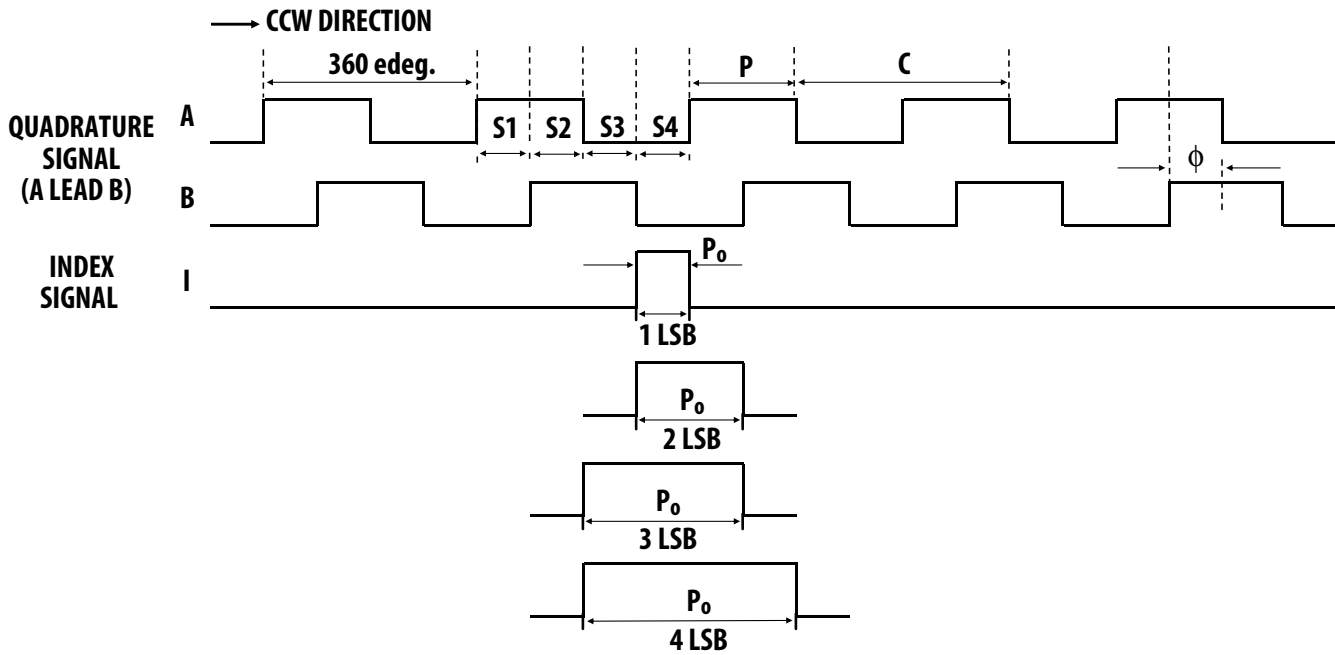
Figure 7: SSI Timing Diagram: 3-Wire and 2-Wire SSI Mode



Generally, the SSI protocol uses an initiator/receiver relationship, in which the initiator initiates the data frame. CLK is generated by the initiator (controller) and input to all receivers. In AEAT-6600-T16, position data is continually updated by the encoder (AEAT-6600-T16) and made available to the shift register.

# Incremental ABI Output

Figure 8: Incremental ABI Signals



With Incremental ABI output enabled, AEAT-6600-T16 is able to provide position data and direction data with the resolution 8 to 1024 CPR. The index signal marks the absolute angular position and typically occurs once per revolution, with the options 90, 180, 270, 360. Lastly, the index signal clears the counter after each full rotation.

# UVW Commutation Output

Figure 9: UVW Commutation Signals – 12-Bit Resolution, One Pole Pair

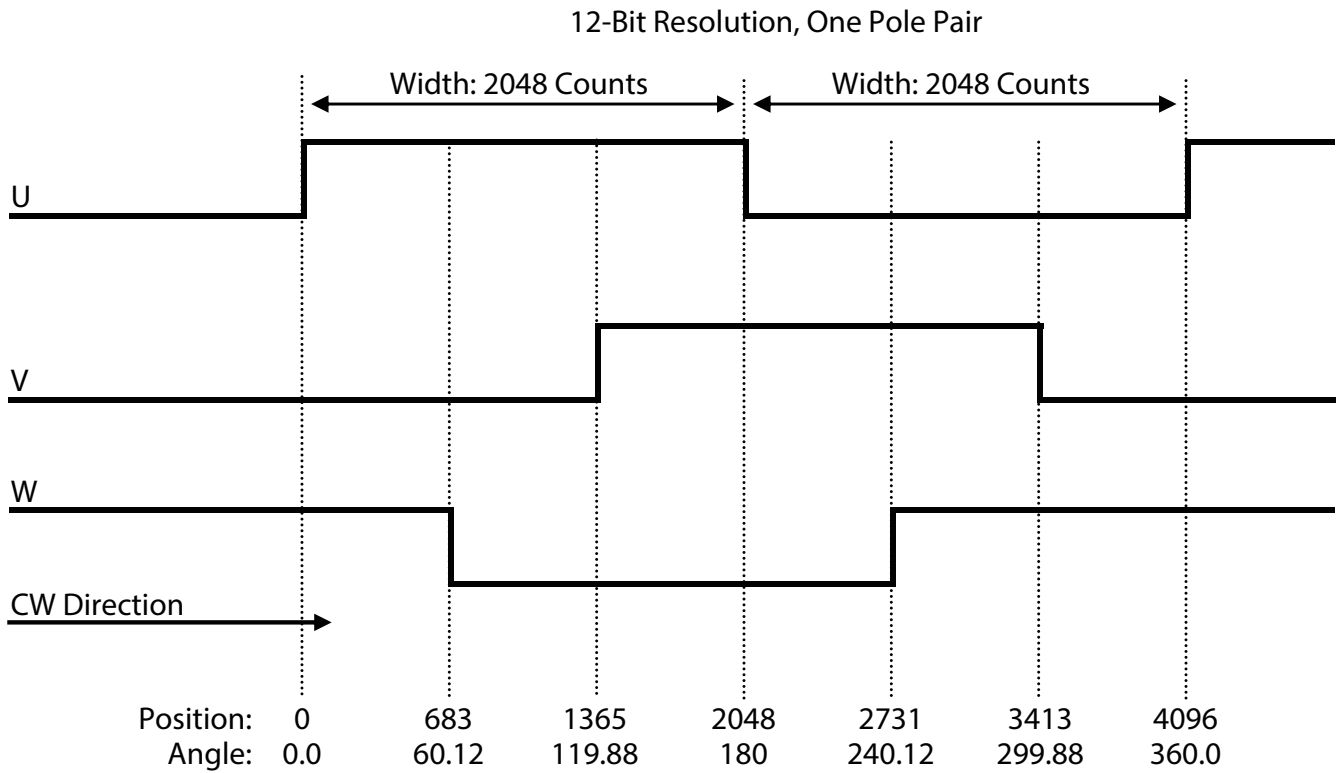
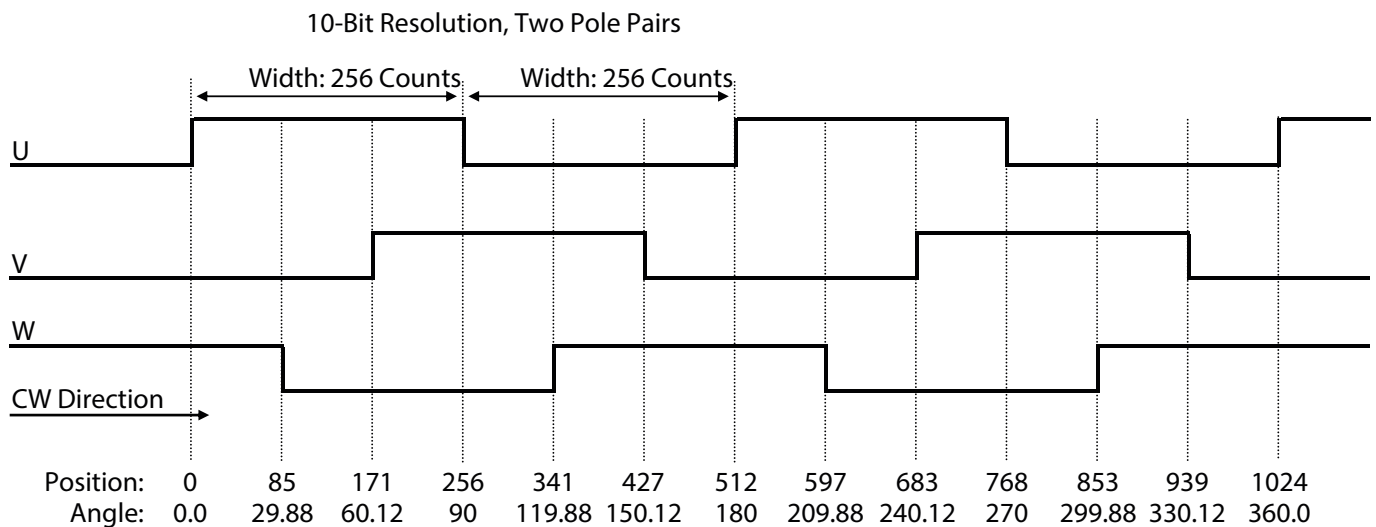


Figure 10: UVW Commutation Signals – 10-Bit Resolution, Two Pole Pairs

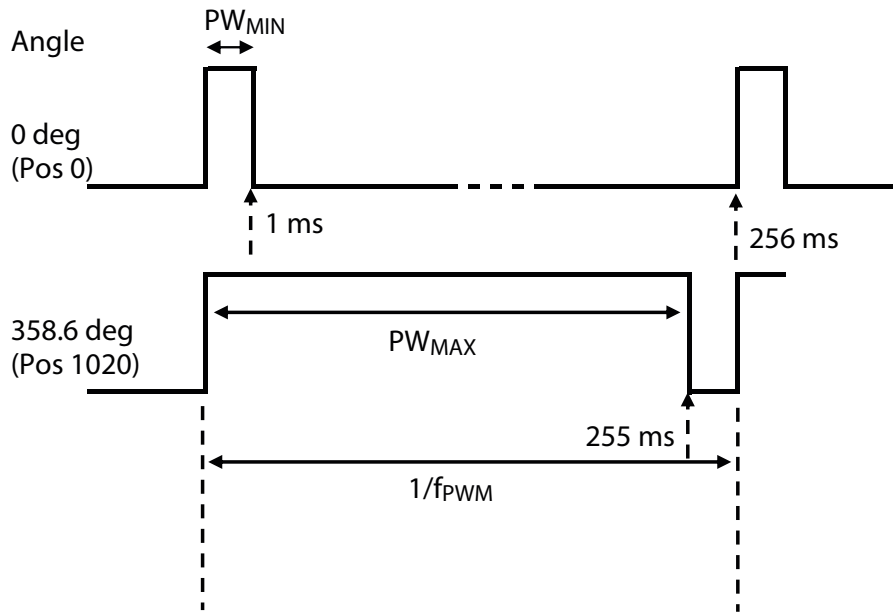


In this option, three-channel integrated commutation output (U, V, W) serves the purpose of emulating Hall sensor feedback. With this, AEAT-6600-T16 is able to align the commutation encoder signal to the correct phase of the motor. Generally, the more the pole pairs, the finer the commutation steps (AEAT-6600 up to 1, 2, 4, 8 pole pairs).

## PWM Output

Figure 11: PWM Signals – 12-Bit Resolution

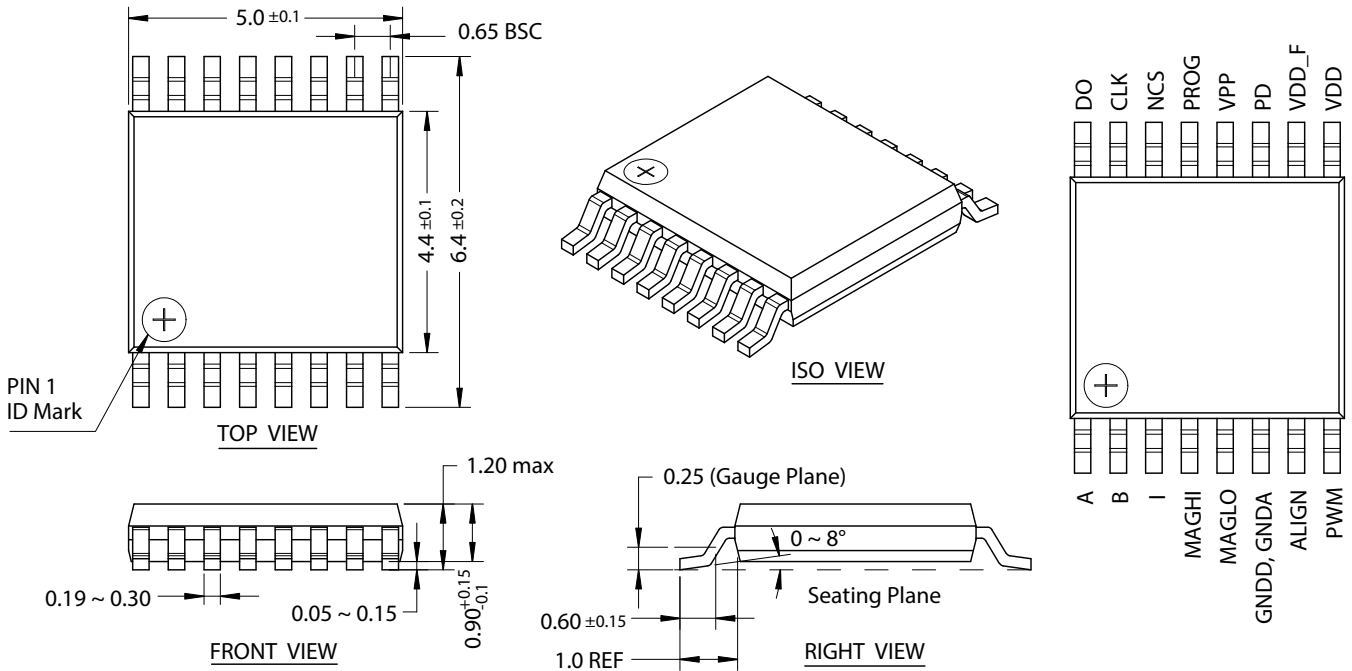
$$\text{Position} = \frac{t_{\text{on}} \times 1025}{(t_{\text{on}} + t_{\text{off}})} - 1$$



PWM output is considered as another absolute output besides SSI. In PWM mode, the duty cycle is proportional to the measured angle. For full rotation angle, 360 degrees is equivalent to position 0 to 1023. For instance, an angle position of  $358.6^\circ$  generates a pulse width  $t_{\text{on}} = 255 \mu\text{s}$  and a pause  $t_{\text{off}}$  of  $1 \mu\text{s}$ , resulting in Position = 1020 after the calculation:  $255 \times 1025 / (255 + 1) - 1 = 1020$

# Package Drawings

Figure 12: AEAT-6600, 16-Lead TSSOP Dimensions



All Dimensions Unit: mm

# Ordering Information

AEAT-6600-T16

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