

45 V, 5 A low VF MEGA Schottky barrier rectifier

26 January 2015

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 5 A
- Reverse voltage: V_R ≤ 45 V
- Extremely low forward voltage
- · High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{sp} ≤ 170 °C; square wave	-	-	5	A
V _R	reverse voltage	T _j = 25 °C	-	-	45	V
V _F	forward voltage	$\begin{split} I_{\text{F}} &= 5 \text{ A}; t_{\text{p}} \leq 300 \; \mu\text{s}; \overline{\delta} \leq 0.02; \\ T_{\text{j}} &= 25 \; ^{\circ}\text{C}; \text{pulsed} \end{split}$	-	425	490	mV
I _R	reverse current	$\label{eq:VR} \begin{split} V_{R} &= 10 \; V; \; t_{p} \leq 3 \; ms; \; \delta \leq 0.3; \\ T_{j} &= 25 \; ^{\circ}C; \; pulsed \end{split}$	-	10	30	μA
		V_R = 45 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C; pulsed	-	120	300	μA

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	2 A anode	anode		A aaa-009063
3	к	cathode	(2) CFP15 (SOT1289)	

6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PMEG045V050EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: $5.8 \times 4.3 \times 0.78 \text{ mm}$	SOT1289

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG045V050EPD	045V 050E

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	45	V
I _F	forward current	T _{sp} = 165 °C; δ = 1		-	7	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; T _{sp} ≤ 170 °C; square wave		-	5	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	160	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
			[3]	-	3.75	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[3] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

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9. Thermal characteristics

Table 6. T	hermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1][2]	-	-	90	K/W
	from junction to ambient		[1][3]	-	-	70	K/W
	ambient		[1][4]	-	-	40	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	3	K/W

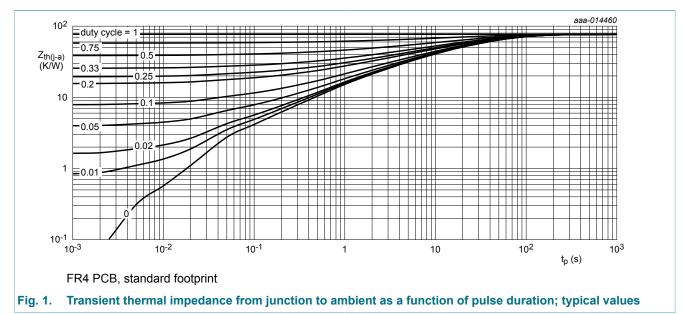
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

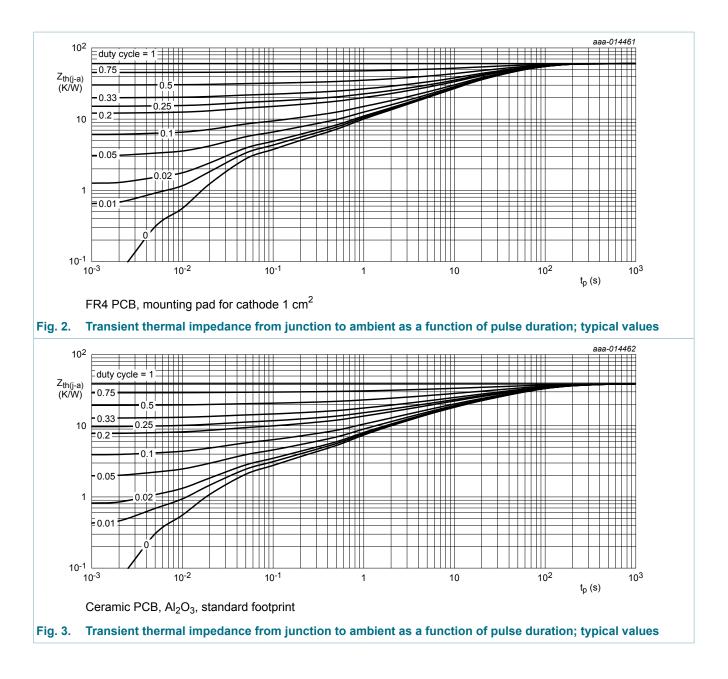
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

[5] Soldering point of cathode tab.



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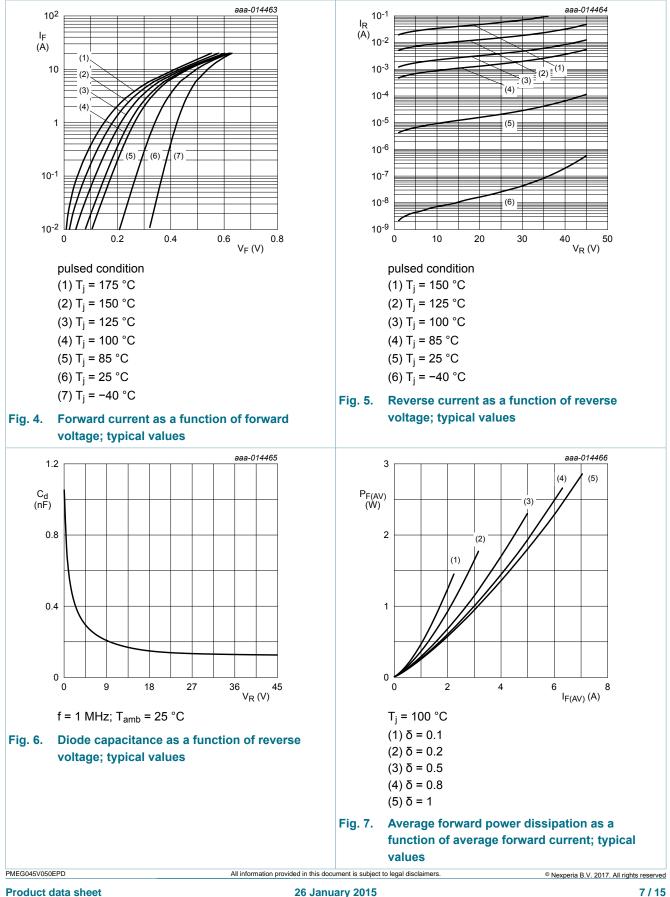
10. Characteristics

Symbol	Parameter	Conditions	Mir	า Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_R = 5 mA; T_j = 25 °C; $t_p \le$ 1.2 ms; $\delta \le$ 0.12; pulsed	45	-	-	V
V _F	forward voltage	I _F = 1 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C; pulsed	-	340	390	mV
		I_F = 2 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C; pulsed	-	370	-	mV
		I _F = 5 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C; pulsed	-	425	490	mV
		I _F = 5 A; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 125 °C; pulsed	-	340	-	mV
I _R	reverse current	V_{R} = 5 V; t_{p} ≤ 3 ms; δ ≤ 0.3; T_{j} = 25 °C; pulsed	-	7	-	μA
		V_R = 10 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C; pulsed	-	10	30	μA
		V_R = 30 V; $t_p \le$ 3 ms; $\delta \le$ 0.3; T _j = 25 °C; pulsed	-	30	-	μA
		V_R = 45 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C; pulsed	-	120	300	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	580	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	190	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	19	-	ns
t _{rr}	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; T _j = 25 °C; I _F = 6 A; V _R = 26 V	-	12	-	ns
V _{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A}/\mu\text{s}; T_j = 25 \text{ °C}$	-	331	-	mV

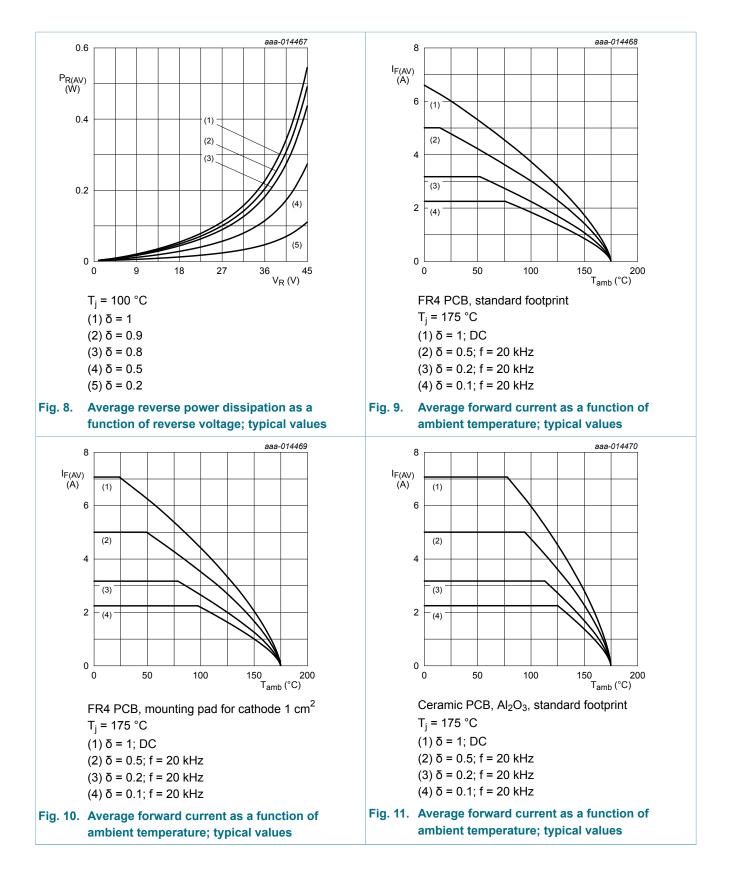
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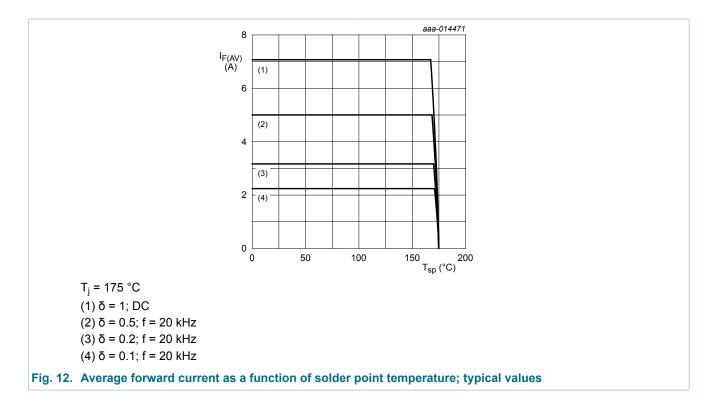
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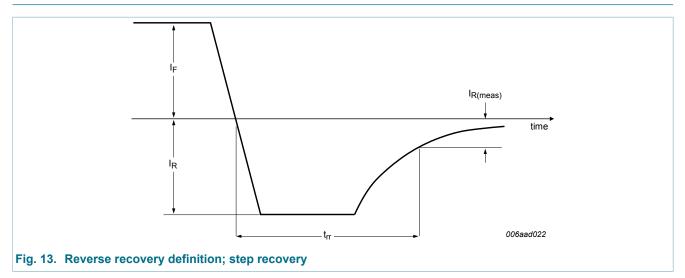
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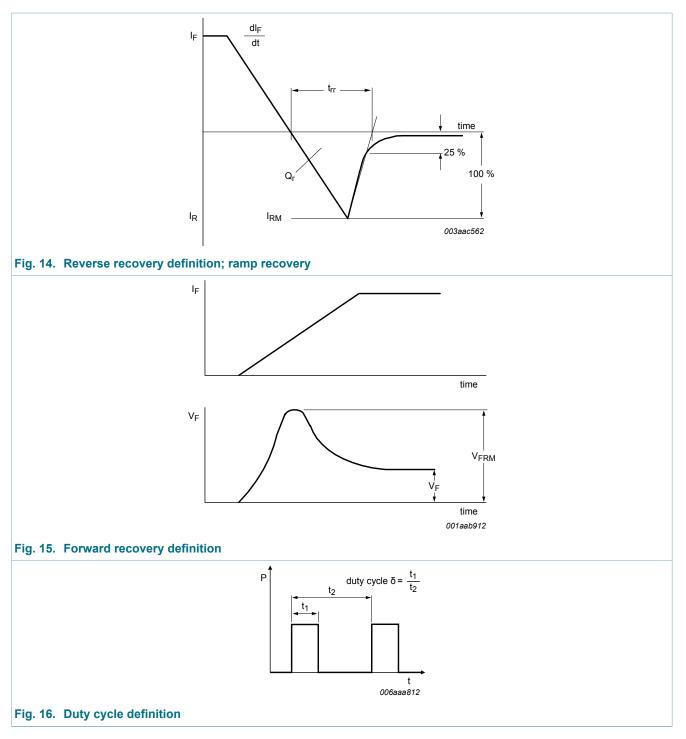
11. Test information



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The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

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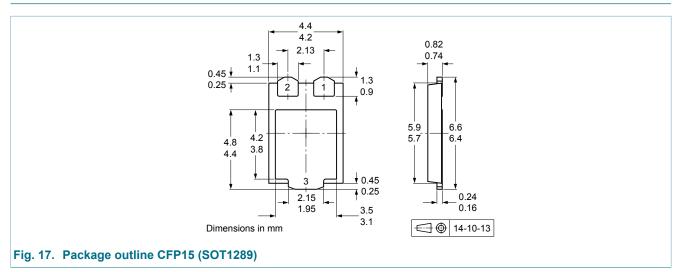
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11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering

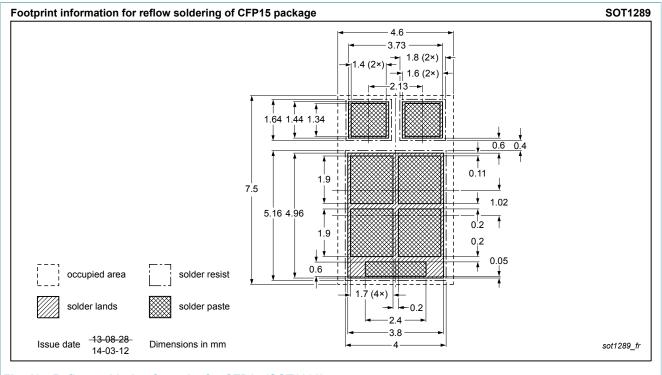


Fig. 18. Reflow soldering footprint for CFP15 (SOT1289)

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14. Revision history

Table 8. Revision hist	ory					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG045V050EPD v.2	20150126	Product data sheet	-	PMEG045V050EPD v.1		
Modifications: • Table limiting values: enhanced with the latest measurements • Table thermal characteristics: updated table with the latest measurements • Table characteristics: enhanced table with the latest measurements • Table characteristics: enhanced table with the latest measurements • Figures 1 to 12: added • Section test information: updated • Package outline replaced by minimized package outline						
PMEG045V050EPD v.1	20140703	Preliminary data sheet	-	-		

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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