



DMN10H170SFDE

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	160mΩ @ V _{GS} = 10V	2.9A
100V	200mΩ @ V _{GS} = 4.5V	2.6A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

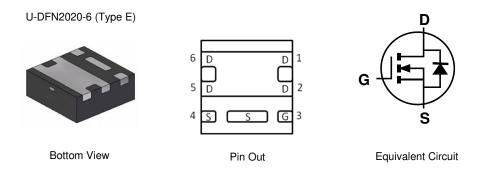
100V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ()
- Weight: 0.0065 grams (Approximate)



Ordering Information (Note 4)

Part Number	Compliance	Case	Quantity Per Reel
DMN10H170SFDE-7	Standard	U-DFN2020-6 (Type E)	3,000
DMN10H170SFDE-13	Standard	U-DFN2020-6 (Type E)	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1:



7H = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Year	2012		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Z		Н		J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2:



7H = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: H = 2020)

W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key												
Year	2012		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	2		0	1	2	3	4	5	6	7	8	9
Week		1	-26			27	-52				53	
Code	A-Z			ode A-Z a-z z								
Internal Code	Su	ın	Mor	n	Tue		Wed	Thu	1	Fri		Sat
Code	Т	-	U		V		W	Х		Y		Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage			V _{DSS}	100	V
Gate-Source Voltage			Vgss	±20	V
	Steady State	T _A = +25°C T _A = +70°C	ID	2.9 2.3	А
Continuous Drain Current (Note 6) VGS = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	3.4 2.7	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%		ldм	10	А	
Maximum Body Diode Continuous Current		ls	2.5	А	
Avalanche Current (Note 7)	las	4.7	А		
Avalanche Energy (Note 7)			Eas	16	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Bower Dissipation (Note 5)	TA = +25°C	D-	0.66	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.42	vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	189	°C/W	
Thermal Resistance, Junction to Amblent (Note 5)	t<10s	t<10s		C/W	
Total Bower Dissinction (Note 6)	$T_A = +25^{\circ}C$	Da	2.03	W	
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.31		
Thermal Desistance, Junction to Ambient (Note 6)	Steady State	D	61		
Thermal Resistance, Junction to Ambient (Note 6)	RθJA	43	°C/W		
Thermal Resistance, Junction to Case (Note 6)		Rejc	9.3		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		-	-		-	
Drain-Source Breakdown Voltage	BVDSS	100	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	—	_	1	μΑ	$V_{DS} = 100V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	2.0	3.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	Deserve		116	160	mΩ	VGS = 10V, ID = 5.0A
	RDS(ON)		126	200	11122	V _{GS} = 4.5V, I _D =5.0A
Diode Forward Voltage	Vsd	_	0.9	1.0	V	V _{GS} = 0V, I _S = 10A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	—	1167	—	pF	
Output Capacitance	Coss	—	36	_	pF	− V _{DS} = 25V, V _{GS} = 0V, − f = 1.0MHz
Reverse Transfer Capacitance	Crss		25	-	pF	
Gate Resistance	Rg	_	1.3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg		4.9	-	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.7	—	nC	VDS = 80V. ID = 12.8A
Gate-Source Charge	Qgs		2.0	-	nC	VDS = 60V, ID = 12.6A
Gate-Drain Charge	Q _{gd}	_	2.0	—	nC	
Turn-On Delay Time	td(on)		10.5	-	ns	
Turn-On Rise Time	tR	_	11.1	—	ns	V _{DS} = 50V, I _D = 12.8A
Turn-Off Delay Time	t _{D(OFF)}		42.6	—	ns	$V_{GS} = 10V, R_G = 25\Omega$
Turn-Off Fall Time	tF		12.8	—	ns	
Reverse Recovery Time	trr		30.3	_	ns	
Reverse Recovery Charge	QRR		35.2	—	nC	- I _F = 12.8A, di/dt = 100A/μs

Notes: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

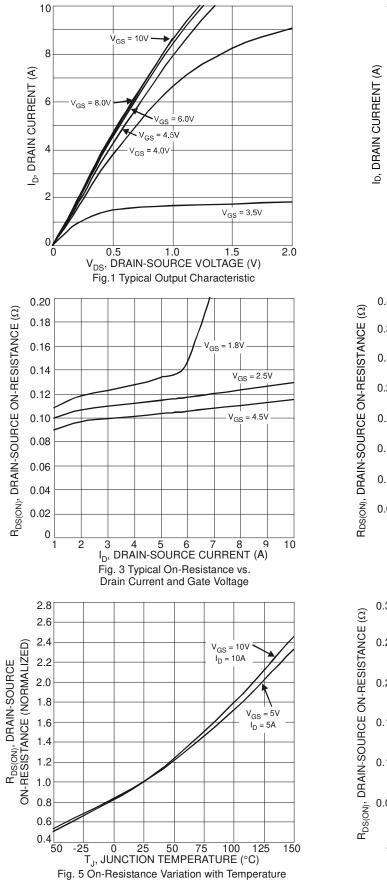
7. UIS in production with L = 1.43mH, T_J = +25°C.

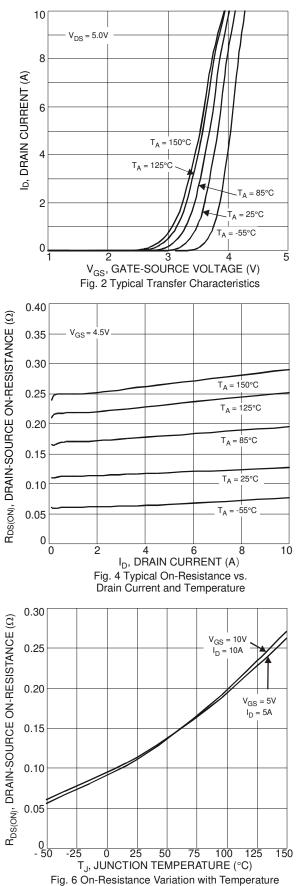
8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



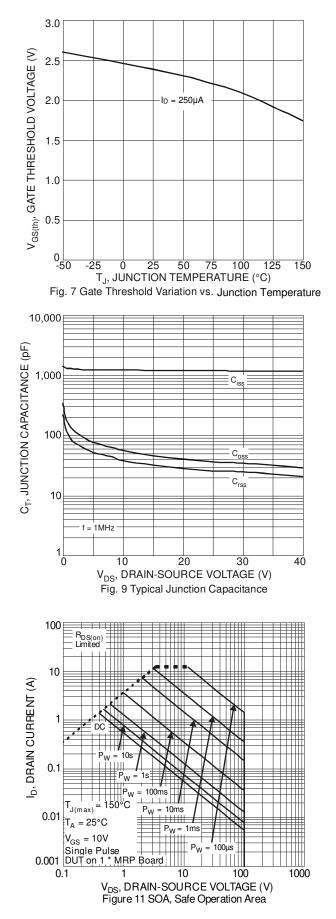
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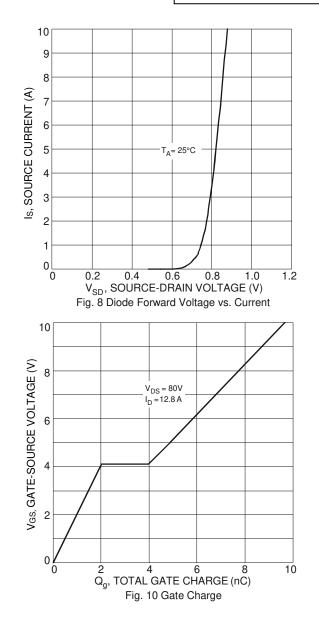




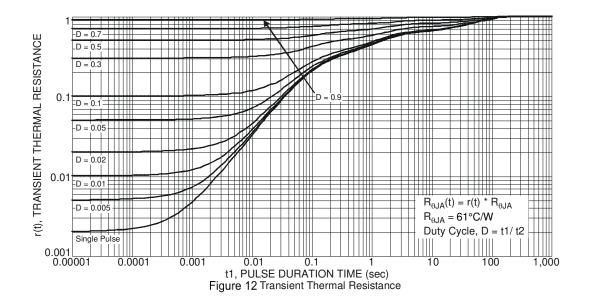


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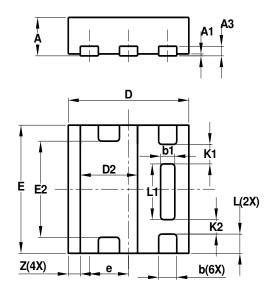




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)

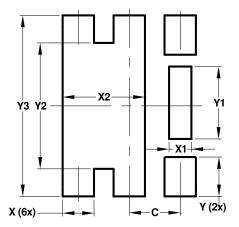


	U-DFN2020-6 Type E						
Dim	Min	Min Max Ty					
Α	0.57	0.63	0.60				
A1	0	0.05	0.03				
A3		_	0.15				
b	0.25	0.35	0.30				
b1	0.185	0.285	0.235				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
Е	1.95	2.05	2.00				
E2	1.40	1.60	1.50				
е	_		0.65				
L	0.25	0.35	0.30				
L1	0.82	0.92	0.87				
K1			0.305				
K2			0.225				
Z			0.20				
All	Dimens	ions in r	nm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)



Dimensions	Value (in mm)		
С	0.650		
Х	0.400		
X1	0.285		
X2	1.050		
Y	0.500		
Y1	0.920		
Y2	1.600		
Y3	2.300		



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