

MCXL System on Module

The MCXL SoM leverages the functionality of the Cyclone 10 LP family on a compact embedded module. Intel Cyclone 10 LP FPGAs are ideal for cost-sensitive applications that require increasing lower static power as the need for scalable processing acceleration increases system interface requirements defence grade security to embedded systems.



Intelligence for Industry

ARIES Embedded GmbH
Schöngesinger Str. 84
D-82256 Fürstentfeldbruck
Germany

Tel: +49(0)8141.36 367-0
Fax: +49(0)8141.36 367-67

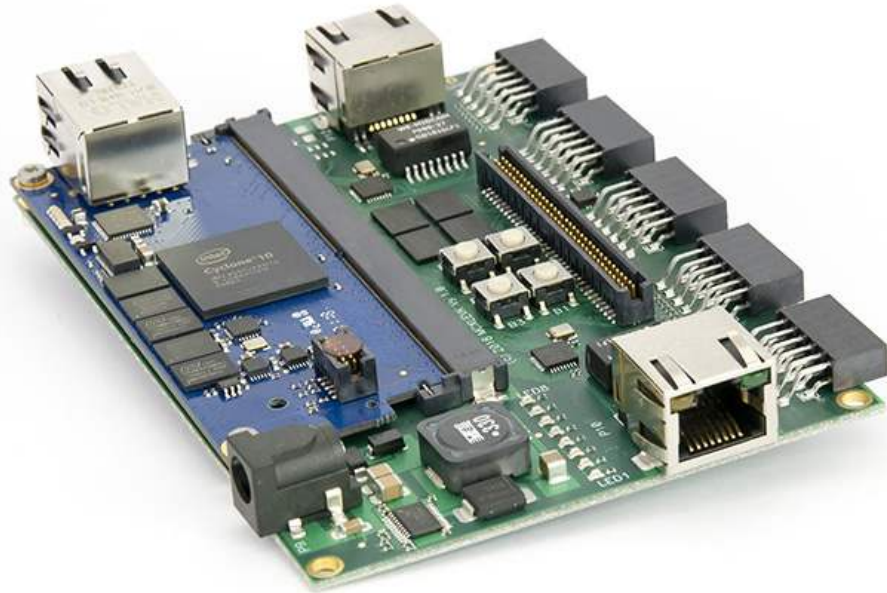
www.aries-embedded.com
info@aries-embedded.de

- MCXL Cyclone 10LP
 - 10CL016YU484C7N
 - 10CLO40YU484C7N
 - 10CLO55YU484C7N
- 246Mbit QSPI Flash
- 512Mbit SDRAM, 32bit
- or 2x128MBit HyperRAM and 2x512MBit HyperFlash
- 221 FPGA Pins:
 - 6 Pins IO-Bank 1 (3.3V)
 - 18 Pins IO-Bank 3 (3.3V)
 - 39 Pins IO-Bank 4
 - 39 Pins IO-Bank 5
 - 37 Pins IO-Bank 6
 - 41 Pins IO-Bank 7
 - 41 Pins IO-Bank 8
- JTAG pin header
- on board 2.5V, and 1.2V power supply
- Clock distribution
- size : 37 x 90 mm
- 4 User LEDs
- Gigabit Ethernet Phy

MCXLEVK Evaluation Kit

MCXLEVK represents the flexible Evaluation Platform for working with the MCXL Cyclone 10LP SoM.

The system helps developers to have a smooth start with the MCXL SoMs, it can be used for designing IP, developing software as well as implementing prototypes.



Intelligence for Industry

ARIES Embedded GmbH
Schöngesinger Str. 84
D-82256 Fürstfeldbruck
Germany

Tel: +49(0)8141.36 367-0
Fax: +49(0)8141.36 367-67

www.aries-embedded.com
info@aries-embedded.de

- MCXLEVK Cyclone 10LP
 - MCXL-S055BC-I
 - MCXL-H055BBB-I
- SPI EEPROM
- 8 User LEDs
- 4 User Buttons
- micro JTAG Header
- 5 PModes
- size : 118 x 83 mm
- 2x RJ45 Ethernet Phy
- Power Supply