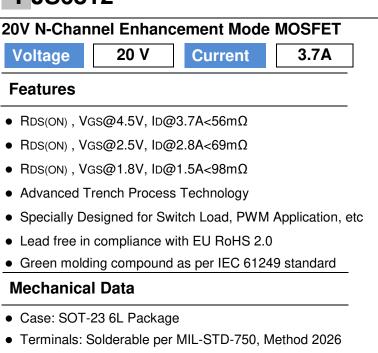
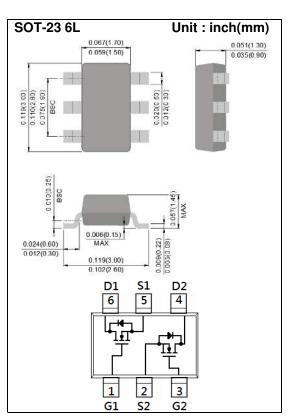
ΡΛΝ	JIT
	SEMI
	CONDUCTOR



- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: SE2



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage	V _{GS}	<u>+</u> 12	V	
Continuous Drain Current		lь	3.7	А
Pulsed Drain Current		Ідм	14.8	А
Power Dissipation	T _a =25°C	PD	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{0JA}	100	°C/W



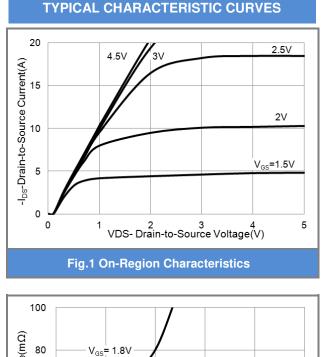
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		•				
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V, I _D =250uA	20	-	_	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.4	0.67	1.2	V
Drain-Source On-State Resistance	RDS(on)	V _{GS} =4.5V, I _D =3.7A	-	41	56	mΩ
		V_{GS} =2.5V, I _D =2.8A	-	51	69	
		V _{GS} =1.8V, I _D =1.5A	-	69	98	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}=20V, V_{GS}=0V$	-	-0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V _{DS} =10V, I _D =3.7A, V _{GS} =4.5V ^(Note 1,2)	-	4.57	-	nC
Gate-Source Charge	Q_{gs}		-	0.77	-	
Gate-Drain Charge	Q_{gd}		-	0.98	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,	-	350	-	
Output Capacitance	Coss		-	40	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	29.3	-	
Switching						
Turn-On Delay Time	td _(on)		-	3.4	-	
Turn-On Rise Time	tr	V _{DD} =10V, I _D =3.7A, V _{GS} =4.5V,	-	47	-	ns
Turn-Off Delay Time	td _(off)		-	18	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	1.5	А
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.75	1.2	v

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





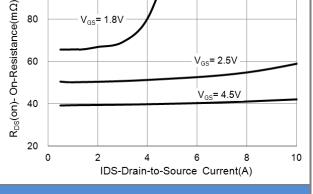
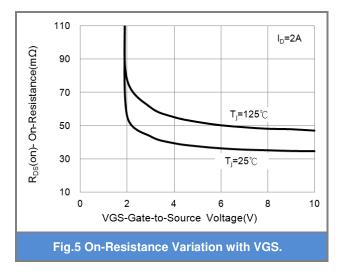
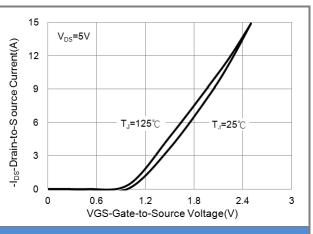


Fig.3 On-Resistance vs. Drain Current







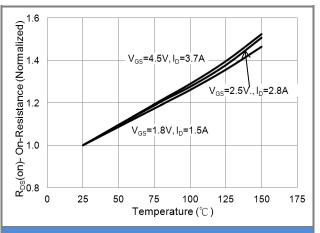
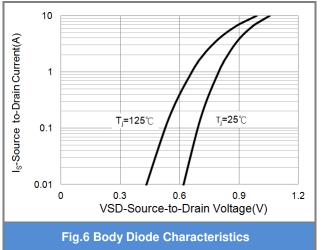


Fig.4 On-Resistance vs. Junction temperature





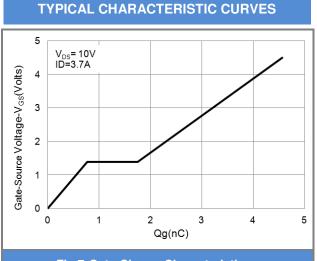
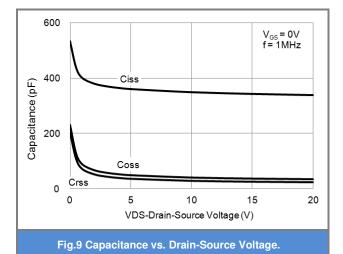


Fig.7 Gate-Charge Characteristics



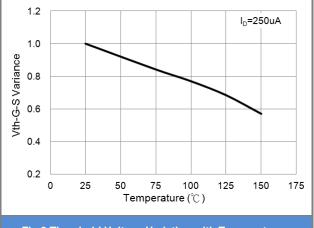


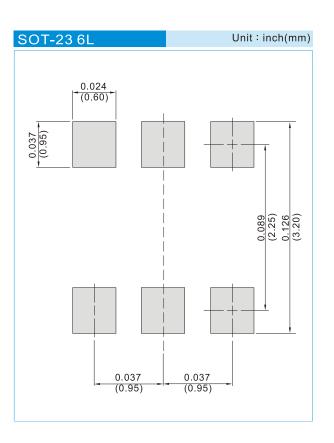
Fig.8 Threshold Voltage Variation with Temperature



PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6812_S1_00001	SOT-23 6L	3K pcs / 7" reel	SE2	Halogen free RoHS compliant
PJS6812_S2_00001	SOT-23 6L	10K pcs / 13" reel	SE2	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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