

## **DATA SHEET**

# **SMP1352 Series: Large Signal Switching, Plastic Packaged PIN Diodes**

# **Applications**

• Large signal switches in base stations and handsets

### **Features**

• Packages rated MSL1, 260 °C per JEDEC J-STD-020



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Skyworks Green<sup>TM</sup> products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green*<sup>TM</sup>, document number SQ04-0074.



# **Description**

The SMP1352 series of plastic packaged, surface mountable low capacitance (0.3 pF) silicon PIN diodes is designed for large signal switch applications from 10 MHz to more than 10 GHz. These diodes have a reverse voltage rating of 200 V and are designed for use in low-distortion switches that are required to hold off large RF voltages.

The nominal 50  $\mu$ m I-region width, combined with the typical 1.5  $\mu$ s carrier lifetime, results in a PIN diode with low forward resistance and low distortion characteristics.

Table 1 describes the various packages and marking of the SMP1352 series.

**Table 1. SMP1352 Series Packaging and Marking** 

	<b>□</b>		<b>—</b>			
Single	Single	Series Pair	Single			
SC-79 Green™	SOD-323 Green™	SOT-23 Green™	SOD-882 Green™			
◆ SMP1352-079LF Marking: Cathode and CG	SMP1352-011LF Marking: RR	SMP1352-005LF Marking: RR2	SMP1352-040LF Marking: S			
L <sub>S</sub> = 0.7 nH	Ls = 1.5 nH	Ls = 1.5 nH	L <sub>S</sub> = 0.45 nH			



The Pb-free symbol or "LF" in the part number denotes a lead-free, RoHS-compliant package unless otherwise noted as Green<sup>TM</sup>. Tin/lead (Sn/Pb) packaging is not recommended for new designs.

### **Table 2. SMP1352 Series Absolute Maximum Ratings (Note 1)**

Parameter	Symbol	Minimum	Maximum	Units
Reverse voltage	<b>V</b> R		200	V
Power dissipation @ 25 °C lead temperature	PD		250	mW
Storage temperature	Тѕтс	-65	+150	°C
Operating temperature	Та	-65	+150	°C
Electrostatic discharge:	ESD			
Human Body Model (HBM), Class 1C			1000	V

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**CAUTION**: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SMP1352 Series Electrical Specifications (Note 1) ( $T_A = +25$  °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Reverse current	l <sub>R</sub>	V <sub>R</sub> = 200 V			10	μΑ
Capacitance	Ст	F = 1 MHz, V = 20 V			0.35	pF
Resistance	Rs	F = 100 MHz				
		I = 1 mA I = 10 mA I = 100 mA		11 2 1	15 2.80 1.35	Ω Ω Ω
Forward voltage	VF	I <sub>F</sub> = 10 mA		0.8		V
Carrier lifetime	TI	IF = 10 mA		1		μs
I region width				50		μm

Note 1: Performance is guaranteed only under the conditions listed in this table.

# **Electrical and Mechanical Specifications**

The absolute maximum ratings of the SMP1352 series are provided in Table 2. Electrical specifications are provided in Table 3. Resistance versus temperature measurements are provided in Table 4.

Typical performance characteristics of the SMP1352 series are illustrated in Figures 1 to 4. Package dimensions are shown in Figures 5 to 11 (odd numbers), and tape and reel dimensions are provided in Figures 6 to 12 (even numbers).

# **Package and Handling Information**

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed.

Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMP1352 series is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

Table 4. Resistance vs Temperature @ 100 MHz

lF (mA)	Rs @ –55 °C (Ω)	Rs @ -15 °C (Ω)	Rs @ +25 °C (Ω)	Rs @ +65 °C (Ω)	Rs @ +100 °C (Ω)	
0.02	260	276	302	263	240	
0.10	60.9	64.0	70.6	71.0	70.1	
0.30	22.4	23.6	26.0	27.8	28.2	
1.0	7.9	8.5	9.2	10.3	10.7	
10	1.5	1.7	1.9	2.2	2.3	
20	1.1	1.2	1.3	1.6	1.7	
100	0.55	0.69	0.78	0.98	1.03	

# **Typical Performance Characteristics**

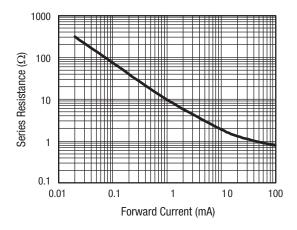


Figure 1. Series Resistance vs Current @ 100 MHz

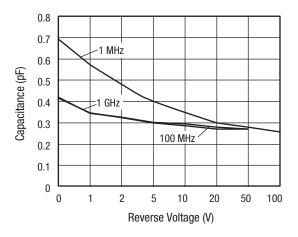


Figure 3. Capacitance vs Reverse Voltage

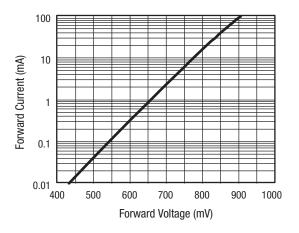


Figure 2. DC Characteristics

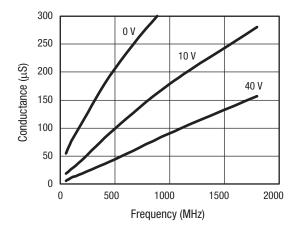
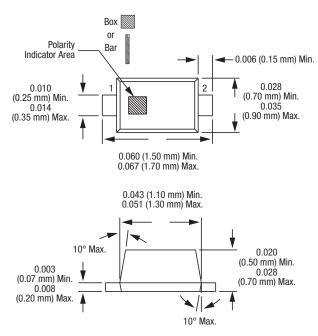


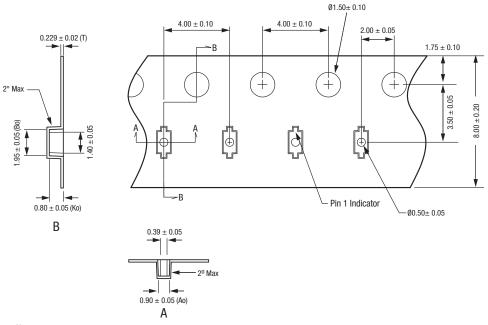
Figure 4. Conductance vs Frequency and Reverse Voltage



Dimensions are in inches (millimeters shown in parentheses)

S1652

**Figure 5. SC-79 Package Dimension Drawing** 



- Notes.

  1. Carrier tape: black conductive polycarbonate or polystyrene.

  2. Cover tape material: transparent conductive PSA.

  3. Cover tape size: 5.4 mm width.

- S. Cover lape size. 3.4 min whith.
   ESD-surface resistivity is ≤ 1 x 10<sup>8</sup> Ohms/square per EIA, JEDEC TNR Specification.
   All measurements are in millimeters.

S2929

**Figure 6. SC-79 Tape and Reel Dimensions** 

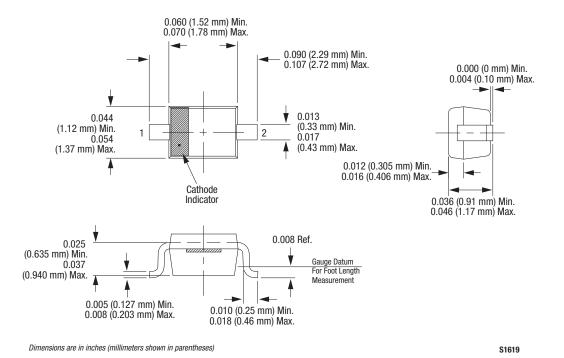
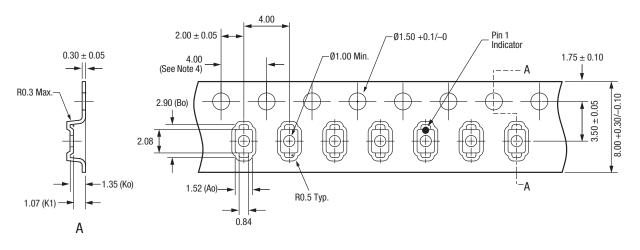


Figure 7. SOD-323 Package Dimension Drawing



#### Notes:

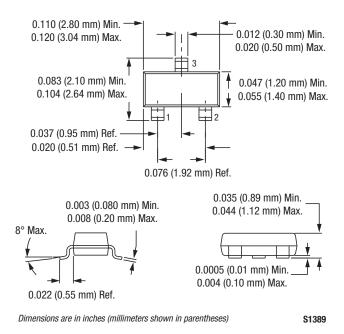
- Carrier tape: black conductive polystyrene. Cover tape: transparent conductive PSA. Cover tape size: 5.4 mm width.

- 10 sprocket hole pitch cumulative tolerance: ±0.20 mm.

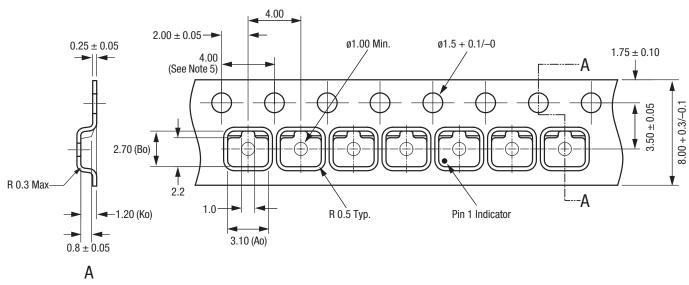
All measurements are in millimeters.

S2910

**Figure 8. SOD-323 Tape and Reel Dimensions** 



**Figure 9. SOT-23 Package Dimension Drawing** 



#### Notes.

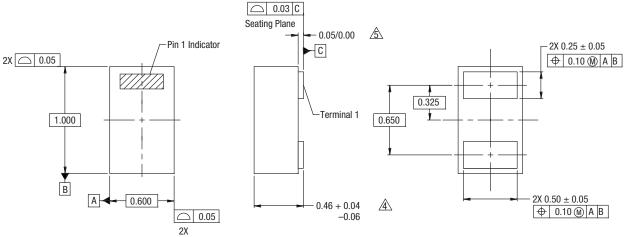
- Carrier tape: black conductive polycarbonate.
- Cover tape material: transparent conductive PSA.
  Cover tape size: 5.40 mm width.
  Tolerance ±0.10 mm.

- 5. Ten sprocket hole pitch cumulative tolerance: ±0.2 mm.
  6. All measurements are in millimeters.
  7. Alternative carrier tape dimensions are:

Ao = 3.3Bo = 2.9Ko = 1.22

S1684b

Figure 10. SOT-23 Tape and Reel Dimensions



### NOTES:

- 1. All measurements are in millimeters.
- 2. Dimensions and tolerances according to ASME Y14.5M-1994.
- These packages are used principally for discrete devices.
   This dimension includes stand-off height and package body thickness, but does not include attached features, e.g., external heatsink or chip capacitors. An integral heatslug is not considered an attached feature.
- 5. This dimension is primarily terminal plating, but does not include small metal protrusion.

Y1410

Figure 11. SOD-882 Package Dimension Drawing

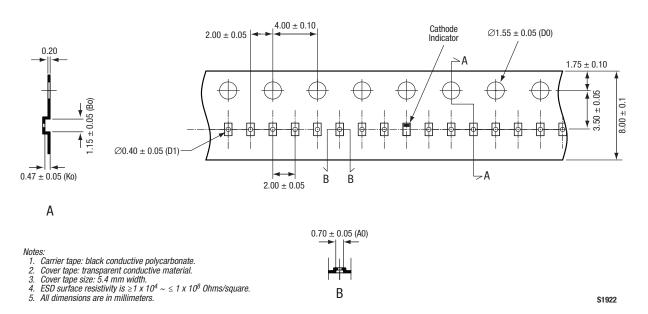


Figure 12. SOD-882 Tape and Reel Dimensions

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