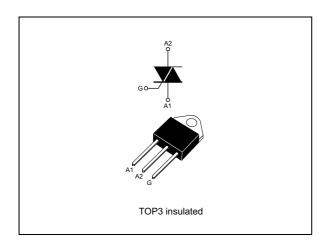


40 A high voltage Triacs

Datasheet - production data



Features

On-state current (I_{T(RMS)}): 40 A

Max. blocking voltage (V_{DRM}/V_{RRM}): 1200 V

Gate current (I_{GT}): 200 mA

Commutation at 10 V/µs: up to 142 A/ms

Noise immunity: 500 V/µs

Insulated package:

- 2,500 V rms (UL recognized: E81734)

Description

The TPDVxx40 series use a high performance alternistor technology. Featuring very high commutation levels and high surge current capability, this family is well adapted to power control on inductive load (motor, transformer...).

Table 1. Device summary

Parameter	Blocking voltage V _{DRM} /V _{RRM}	On-state current I _{T(RMS)}	Gate current I _{GT}	
TPDV640RG	600 V			
TPDV840RG	800 V	40 A 200 n		
TPDV1240RG	1200 V			

Characteristics TPDVxx40

1 Characteristics

Table 2. Absolute ratings (limiting values)

Symbol	Parameter			Value	Unit
I _{T(RMS)}	On-state rms current (180° conduction a	ngle)	T _c = 75 °C	40	Α
		t _p = 2.5 ms		590	А
I _{TSM}	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	T _j = 25 °C	370	
		t _p = 10 ms	1	350	
I ² t	I ² t value for fusing	t _p = 10 ms	T _j = 25 °C	610	A ² S
dI/dt	Critical rate of rise of on-state current $I_G = 500 \text{ mA}$; $dI_G/dt = 1 \text{ A/}\mu\text{s}$	Repetitive F =	Repetitive F = 50 Hz		A/µs
di/dt		Non repetitive	Non repetitive		
	Repetitive peak off-state voltage	TPDV640	T _j = 125 °C	600	V
V_{DRM} V_{RRM}		TPDV840		800	
* KKM		TPDV1240		1200	
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			-40 to +150 -40 to +125	°C
T _L	Maximum lead temperature for soldering during 10 s at 2 mm from case			260	°C
V _{INS(RMS)} ⁽¹⁾	Insulation rms voltage			2500	V

^{1.} A1, A2, gate terminals to case for 1 minute

Table 3. Electrical Characteristics (T_j = 25 °C, unless otherwise specified)

Symbol	Test condition		Quadrant		Value	Unit
I _{GT}	V 40 V DO D 00 O		1 - 11 - 111	Max.	200	mA
V _{GT}	$V_D = 12 \text{ V DC}, R_L = 33 \Omega$		1-11-111	Max.	1.5	V
V_{GD}	$V_D = V_{DRM} R_L = 3.3 \text{ k}\Omega$	T _j = 125 °C	I - II - III	Min.	0.2	V
t _{gt}	$V_D = V_{DRM} I_G = 500 \text{ mA d}I_G/\text{d}t$	= 3A/µs	1 - 11 - 111	Тур.	2.5	μs
I _H ⁽¹⁾	I _T = 500 mA Gate open		Тур.	50	mA	
ı.	I _L		1 - 111	Тур.	100	- mA
".			II		200	
dV/dt	Linear slope up to : $V_D = 67\% V_{DRM}$ Gate open $T_j = 125 ^{\circ}\text{C}$			Min.	500	V/µs
V _{TM} ⁽¹⁾	$I_{TM} = 56 \text{ A}$ $t_p = 380 \mu\text{s}$		Max.	1.8	V	
I _{DRM}	I_{DRM} I_{RRM} $V_{DRM} = V_{RRM}$ $T_j = 25$ $T_j = 12$			Max.	20	μA
I _{RRM}				IVIAX.	8	mA
(dl/dt)c (1)	$(dV/dt)c = 200 V/\mu s$ $T_i = 125 °C$			Min.	35	A/ms
(diracjo v	(dV/dt)c = 10 V/μs	1, - 120 0		IVIII I.	142	Allis

^{1.} For either polarity of electrode ${\rm A_2}$ voltage with reference to electrode ${\rm A_1}$.



TPDVxx40 Characteristics

Table 4. Gate characteristics (maximum values)

Symbol	Parameter	Value	Unit	
P _{G(AV)}	Average gate power dissipation	1	W	
P _{GM}	Peak gate power dissipation $t_p = 20 \mu s$		40	W
I _{GM}	Peak gate current $t_p = 20 \mu s$		8	А
V _{GM}	Peak positive gate voltage $t_p = 20 \mu s$		16	V

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-a)}	Junction to ambient	50	°C/W
R _{th(j-c)} DC	Junction to case for DC	1.2	°C/W
R _{th(j-c)} AC	Junction to case for 360 °conduction angle (F = 50 Hz)	0.9	°C/W

Figure 1. Max. rms power dissipation versus on-state rms current (F = 50 Hz) (curves limited by (dl/dt)c)

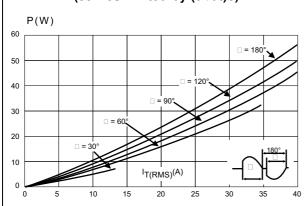


Figure 2. Max. rms power dissipation and max. allowable temperatures (T_{amb} and T_{case}) for various R_{th}

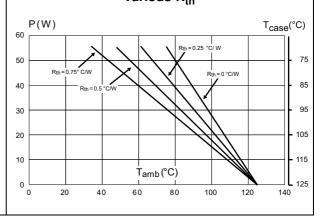


Figure 3. On-state rms current versus case temperature

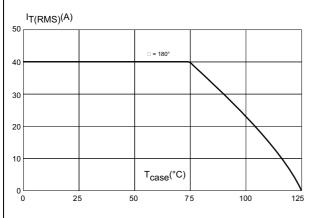
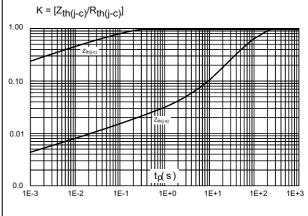
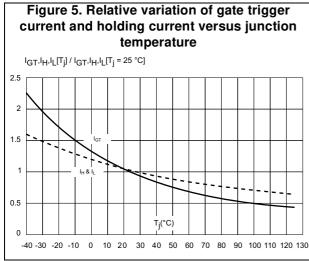
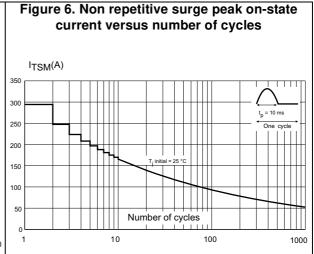


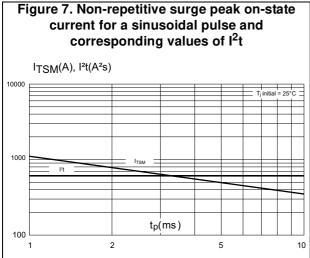
Figure 4. Relative variation of thermal impedance versus pulse duration



Characteristics TPDVxx40







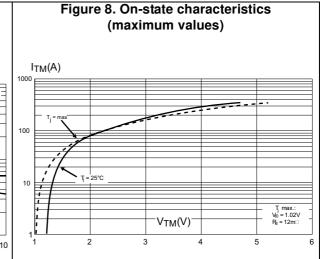
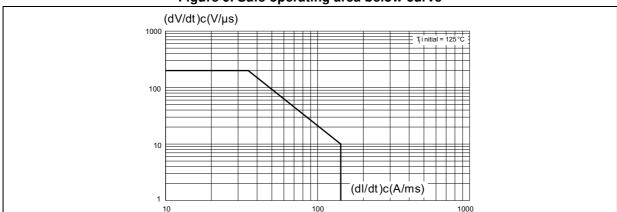


Figure 9. Safe operating area below curve



TPDVxx40 Package information

2 Package information

- Epoxy meets UL94, V0
- Cooling method:C (by conduction)
- Recommended torque value: 0.9 to 1.2 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

2.1 TOP3 insulated package information

Figure 10. TOP3 insulated package outline

Package information TPDVxx40

Table 6. TOP3 insulated package mechanical data

	Dimensions					
Ref.		Millimeters			Inches ⁽¹⁾	
	Тур.	Min.	Max.	Тур.	Min.	Max.
Α		4.4	4.6		0.173	0.181
В		1.45	1.55		0.057	0.061
С		14.35	15.60		0.565	0.614
D		0.5	0.7		0.020	0.028
Е		2.7	2.9		0.106	0.114
F		15.8	16.5		0.622	0.650
G		20.4	21.1		0.815	0.831
Н		15.1	15.5		0.594	0.610
J		5.4	5.65		0.213	0.222
K		3.4	3.65		0.134	0.144
ØL		4.08	4.17		0.161	0.164
Р		1.20	1.40		0.047	0.055
R	4.60			0.181		

^{1.} Values in inches are converted from mm and rounded to 4 decimal digits.

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3 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty.	delivery mode
TPDV640RG	TPDV640				
TPDV840RG	TPDV840	TOP3 insulated	4.5 g	30	Tube
TPDV1240RG	TPDV1240				

4 Revision history

Table 8. Document revision history

Date	Revision	Changes
30-Mar-2011	1	Initial release.
10-Jun-2015	2	Updated <i>Table 3</i> . Updated <i>Figure 9</i> . Format updated to current standard.

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