



#### 60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C (Note 9)
60V	2.8mΩ @ V <sub>GS</sub> = 10V	100A

#### **Features**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application
- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish: RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Switching
- Synchronous Rectification
- **DC-DC Converters**

### **Mechanical Data**

- Case: PowerDI 85060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)

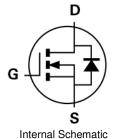
#### PowerDI5060-8 (Type K)



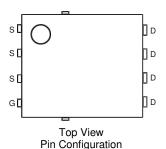




**Bottom View** 







### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMTH62M8SPS-13	PowerDI5060-8 (Type K)	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.</p>
  4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



⊃¦¦ = Manufacturer's Marking TH62M8SS = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)



# **Maximum Ratings** $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	60	V	
Gate-Source Voltage	$V_{GSS}$	±20	V	
Continuous Drain Current V 10V (Notes 6 8 0)	$T_{C} = +25^{\circ}C$	1	100	А
Continuous Drain Current, V <sub>GS</sub> = 10V (Notes 6 & 9)	$T_{C} = +100^{\circ}C$	ID	100	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		$I_{DM}$	400	Α
Continuous Body Diode Forward Current (Note 6)	$T_{C} = +25^{\circ}C$	I <sub>S</sub>	100	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	400	Α	
Avalanche Current, L = 0.2mH	I <sub>AS</sub>	45.5	Α	
Avalanche Energy, L = 0.2mH	E <sub>AS</sub>	207	mJ	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>0JA</sub>	47	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	125	W
Thermal Resistance, Junction to Case (Note 6)	R <sub>eJC</sub>	1.2	°C/W
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +175	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	2	2.53	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	2.2	2.8	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	$V_{SD}$	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	4556	_		$V_{DS} = 30V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Output Capacitance	Coss	_	1383	_	pF		
Reverse Transfer Capacitance	Crss	_	105.2	_			
Gate Resistance	$R_{G}$	_	0.66	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	$Q_g$	_	95.4	_		$V_{DD} = 30V, I_D = 90A,$ $V_{GS} = 10V$	
Gate-Source Charge	Qgs	_	21.6	_	nC		
Gate-Drain Charge	$Q_{qd}$	_	20.4	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	13.2	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 90A, R_{G} = 3.5\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	11.7	_	20		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	31	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	12	_			
Reverse Recovery Time	t <sub>RR</sub>	_	50.5	_	ns	L	
Reverse Recovery Charge	Q <sub>RR</sub>	_	80.8		nC		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate. Notes:

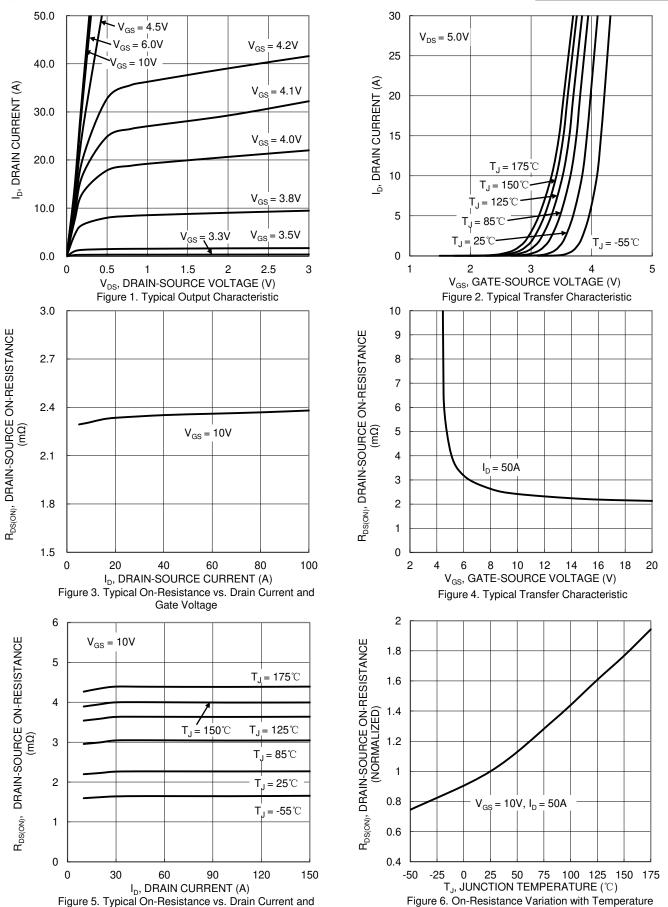
6. Thermal resistance from junction to soldering point (on the exposed drain pad).

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.
9. Limited by package.







Temperature





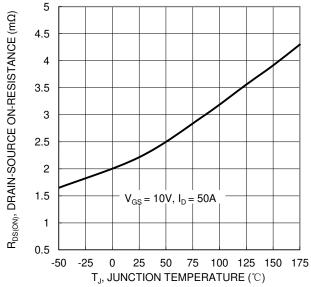


Figure 7. On-Resistance Variation with Temperature

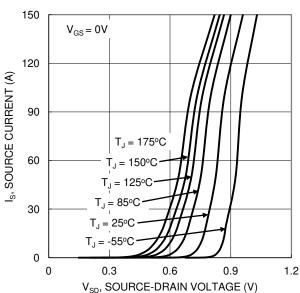


Figure 9. Diode Forward Voltage vs. Current

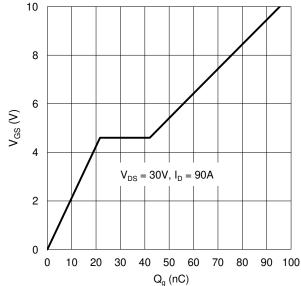


Figure 11. Gate Charge

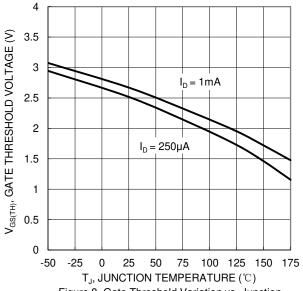
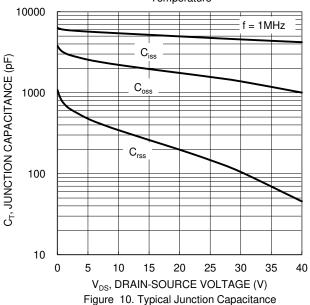


Figure 8. Gate Threshold Variation vs. Junction Temperature



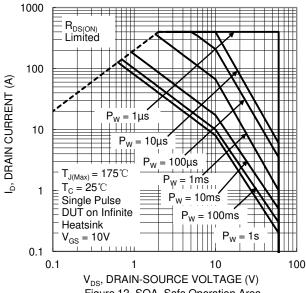


Figure 12. SOA, Safe Operation Area

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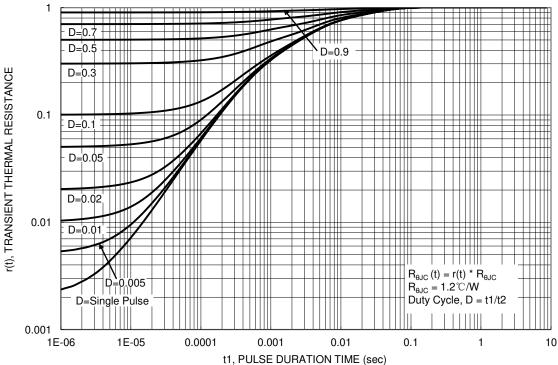


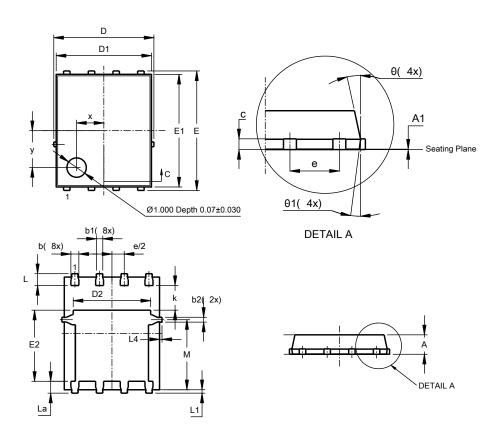
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 

#### PowerDI5060-8 (Type K)

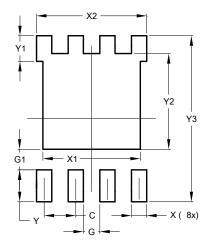


PowerDI5060-8 (Type K)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0	0.05	0.02		
b	0.33	0.51	0.41		
b1	0.300	0.366	0.333		
b2	0.20	0.35	0.25		
С	0.23	0.33	0.277		
D	5	.15 BS0	2		
D1	4.85	4.95	4.90		
D2	-	-	3.98		
Е	6	.15 BS0	C		
E1	5.75	5.85	5.80		
E2	3.56	3.725	3.66		
е	1	.27BSC			
k	-	-	1.27		
L	0.51	0.71	0.61		
La	0.51	0.675	0.61		
L1	0.05	0.20	0.175		
L4	-	-	0.125		
М	3.50	3.71	3.605		
X	-	-	1.400		
<u>γ</u>	-	-	1.900		
	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8 (Type K)



Dimensions	Value			
Dilliensions	(in mm)			
С	1.270			
G	0.660			
G1	0.820			
X	0.610			
X1	3.910			
X2	4.420			
Y	1.270			
Y1	1.020			
Y2	3.810			
Y3	6.610			



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