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April 1st, 2010 Renesas Electronics Corporation

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HD74HC4066

Quad Analog Switches/Quad Multiplexers

REJ03D0651-0200 (Previous ADE-205-538) Rev.2.00 Mar 30, 2006

Description

This switch has low "on" resistance and low "off" leakage. It is a bidirectional switch, thus any analog input may be used as an output and vice-versa. Also the HD74HC4066 switch contains linearization circuitry which lowers the "on" resistance and increases switch linearity. The HD74HC4066 device allows control of up to $12\ V$ (peak) analog signals with digital control signals of the same range. Each switch has its own control input which disables each switch when low.

Features

• High Speed Operation

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Quiescent Supply Current: I_{CC} (static) = 1 \propto A max (Ta = 25 $^{\circ}$ C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC4066P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74HC4066FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC4066RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)
HD74HC4066TELL	TSSOP-14 pin	PTSP0014JA-B (TTP-14DV)	Т	ELL (2,000 pcs/reel)

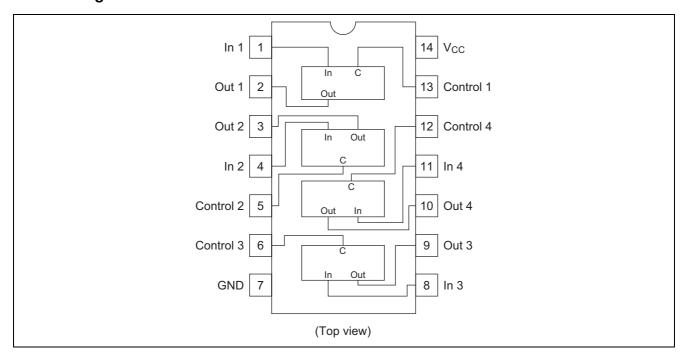
Note: Please consult the sales office for the above package availability.

Function Table

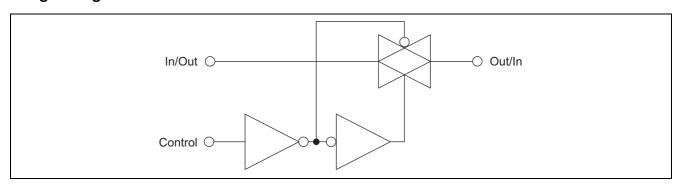
Control	Switch			
L	OFF			
Н	ON			

$$\begin{split} &GND \leq Vin \leq V_{CC} \\ &GND \leq Vout \leq V_{CC} \end{split}$$

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item		Symbol	Rating	Unit	
Supply voltage		V _{CC}	-0.5 to +7.0	V	
Control input voltage		V _C	-0.5 to $V_{CC} + 0.5$	V	
Switch I/O voltage		$V_{IN/OUT}$	- 0.5 to V _{CC} + 0.5	V	
Supply current	(V _{CC})	I _{CC}	+50	mA	
	(GND)	I_{GND}	– 50	mA	
Switch I/O current (per pin)		I _{IN/OUT}	±25	mA	
Control input diode current		lık	±20	mA	
Switch I/O diode current		I _{IOK}	±20	mA	
Power dissipation		P _T	500	mW	
Storage temperature range		Tstg	-65 to +150	°C	

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit	
Supply voltage	V _{CC}	2	_	6	V	
Control input voltage	V _C	0	_	V _{CC}	V	
Switch I/O voltage	V _{IN/OUT}	0	_	V _{CC}	V	
Operating temperature	Topr	-40	_	+85	°C	
Input rise/fall time	$V_{CC} = 2.0 \text{ V}$	t _r , t _f	0	_	1000	ns
	$V_{CC} = 4.5 \text{ V}$		0	_	500	ns
	$V_{CC} = 6.0 \text{ V}$		0	_	400	ns

Electrical Characteristics

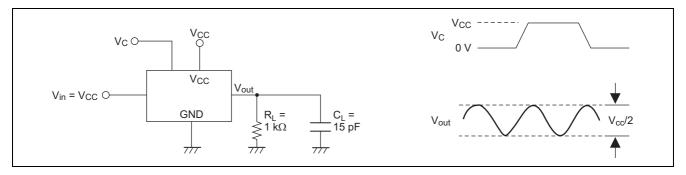
			7	a = 25°0	С	Ta = -40	to+85°C		
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Control input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	٧	
		4.5	3.15	_	_	3.15	_		
		6.0	4.2	_	_	4.2	_		
	V _{IL}	2.0	_	_	0.5	_	0.5	V	
		4.5	_	_	1.35	_	1.35		
		6.0	_	_	1.8	_	1.8		
"ON" resistance	Ron	2.0	_	2000	5000	_	6250	Ω	$V_C = V_{IH}$
		4.5	_	100	200	_	250		Vin = 0 to V _{CC}
		6.0	_	60	170	_	210		lin/out = 1 mA
∆ON resistance	ΔR_{ON}	2.0	_	50	_	_	_	Ω	V _C = V _{IH} , lin/out = 1 mA
between any two		4.5	_	3	_	_	_		between any two
channels		6.0	_	2	_	_	_		channels
OFF channel	I _{S (OFF)}	6.0	_	_	±0.1	_	±1.0	∝A	$V_C = V_{IL}$
leakage current									$V_{IN} = V_{CC}$, $Vout = GND$ or,
(switch off)									Vin = GND,
									Vout = V _{CC}
OFF channel	I _{S (ON)}	6.0	_	_	±0.1	_	±1.0	∞A	$V_C = V_{IH}$
leakage current									$Vin = V_{CC}$ or GND
(switch on)									
Control input current	lin	6.0		_	±0.1	_	±1.0	∞A	Vin = V _{CC} or GND
Quiescent supply	I _{CC}	6.0	_	_	1.0	_	10.0	∞A	Vin = V _{CC} or GND
current									

Switching Characteristics ($C_L = 50 \ pF$, Input $t_r = t_f = 6 \ ns$, $V_{EE} = GND$)

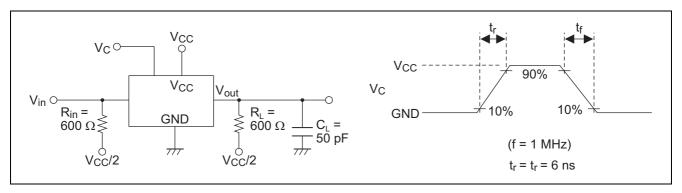
			Т	a = 25°	С	Ta = -40 to +85°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0		25	60	_	75	ns	$R_L = 10 \text{ k}\Omega$
time		4.5	_	6	12	_	15		Switch input to
		6.0	_	5	10	_	13		switch output
	t _{PHL}	2.0	_	25	60	_	75	ns	
		4.5	_	6	12	_	15		
		6.0	_	5	10	_	13		
Propagation delay	t _{PLH}	2.0	_	_	50	_	65	ns	$R_L = 10 \text{ k}\Omega$
time	t _{PHT}	4.5	_	4	10	_	13		
		6.0	_	_	9	_	11		
Output enable	t _{zH}	2.0	_	_	115	_	145	ns	$R_L = 1 \text{ k}\Omega$
time		4.5	_	10	23	_	29		
		6.0		_	20	_	25		
Output disable	t_{LZ}	2.0		_	115	_	145	ns	$R_L = 1 \text{ k}\Omega$
time	t _{HZ}	4.5		14	23	_	29		
		6.0		_	20	_	25		
Sine wave distortion		4.5		0.05	_	_	_	%	$R_L = 10 \text{ k}\Omega, C_L = 50 \text{ pF},$
									$f_{IN} = 1 \text{ kHz}$
Band width (-3 dB)		4.5	_	30	_	_	_	MHz	$R_L = 600 \Omega, C_L = 50 pF,$ 20 $log_{10} Vout/Vin = -3dB$
Feed through attenuation		4.5	_	-50	_	_	_	dB	$R_L = 600 \Omega$, $C_L = 50 pF$, $f_{IN} = 1 MHz$
Cross talk between		2.0		25	_	_	_	mA	$R_L = 600 \Omega$, $C_L = 50 pF$,
control input to		4.5	_	60	_	_	_		f _{IN} = 1 MHz
signal I/O		6.0	_	75	_	_	_		
Cross talk between any two switches		4.5	_	- 50	_	_	_	dB	$R_L = 600 \Omega$, $C_L = 50 pF$, $f_{IN} = 1 MHz$
Maximum control		2.0		20	_	_	_	MHz	$R_L = 1 \text{ k}\Omega$, $C_L = 15 \text{ pF}$,
frequency		4.5		30	_	_	_		Vout = 1/2 (V _{CC})
		6.0		30	_	_	_		
Control input capacitance	Cin		_	5	10	_	10	pF	
Switch I/O capacitance	Cin/out		_	6	_	_	_	pF	
Feed through capacitance	Cin/out		_	0.5	_	_	_	pF	
Power dissipation capacitance	C _{PD}		_	13	_	_	_	pF	

Test Circuit

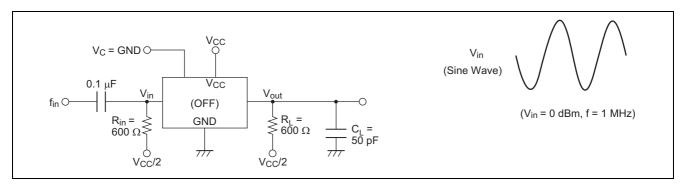
Maximum Control Frequency



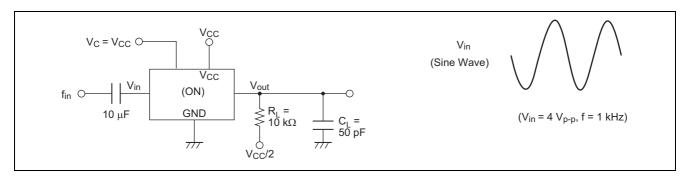
Cross talk (Control Input to Switch Output)



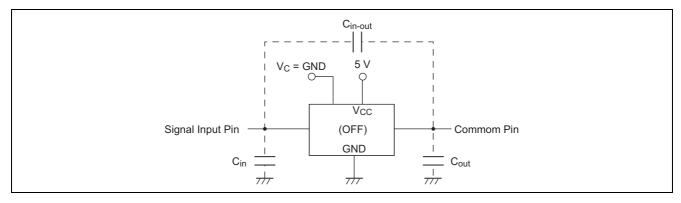
Feed through Attenuation



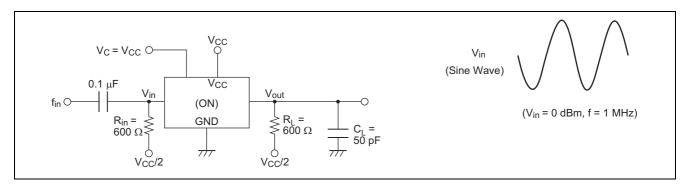
Sine Wave Distortion



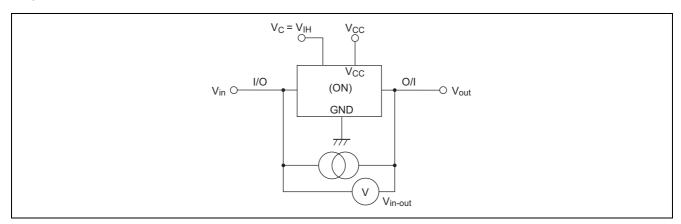
Cin, Cout, Cin-out (Input, Output, and Feed through Capacitance)



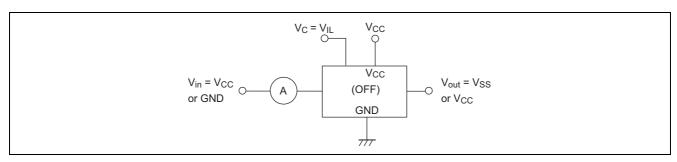
Switch Frequency Response Band Width (-3dB)



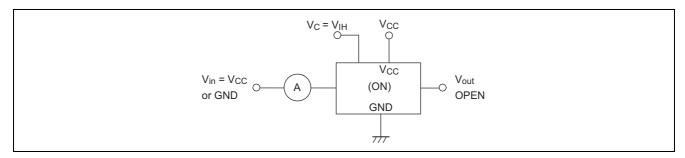
R_{on}: ON Resistance



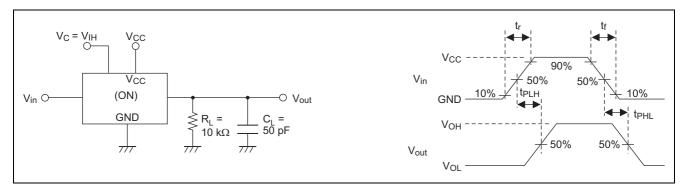
Is (OFF): OFF Channel Leakage Current (Switch OFF)



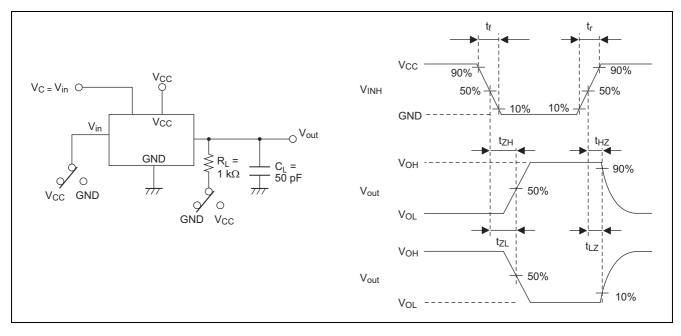
Is (ON): OFF Channel Leakage Current (Switch ON)



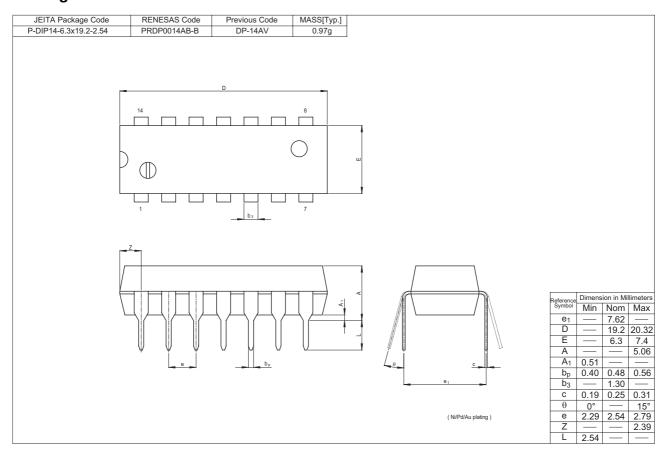
t_{PLH}, t_{PHL}: Propagation Delay Time (Switch Input to Switch Output)

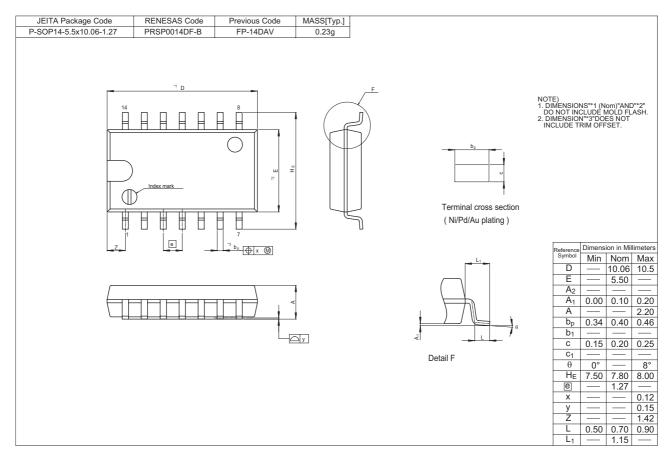


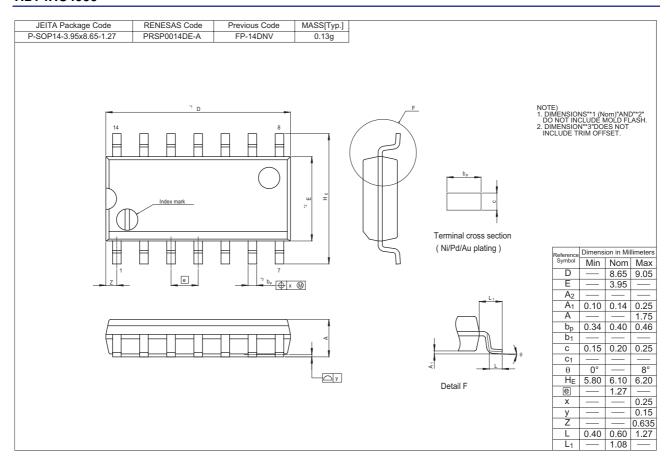
 $t_{ZH},\,t_{ZL}/t_{HZ},\,t_{LZ}$: Output Enable and Disable Time

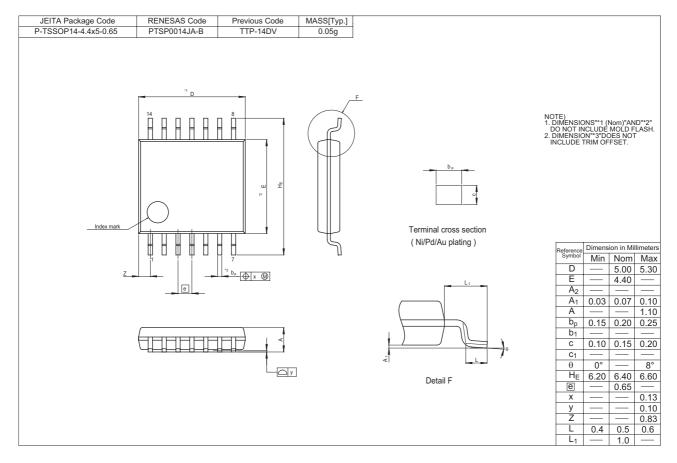


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Renesas Technology Malaysia Sdn. Bhd
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