

AN-2252 LM3561 Evaluation Kit

1 Introduction

The LM3561 is a 2-MHz fixed-frequency current-mode synchronous boost converter. The device is designed to operate as a single 600-mA constant current driver for high-current white LEDs. The high-side current source allows for grounded cathode LED operation while the 250-mV regulated headroom voltage ensures that the LED current is well regulated and efficiency remains high.

The LM3561EVM is designed to fully evaluate the LM3561 Boost Converter + White LED Camera Flash LED driver. The board comes populated with a single Flash LED (LXCL-EYW4 from Lumileds), and an indicator LED (LEDI). For each input/output of the LM3561 there is a dedicated header pin for testing purposes. The LM3561EVM is included with a USB2ANY Module which interfaces the LM3561EVM with the downloadable Graphical User Interface (LM3561.exe GUI) available at (www.ti.com).

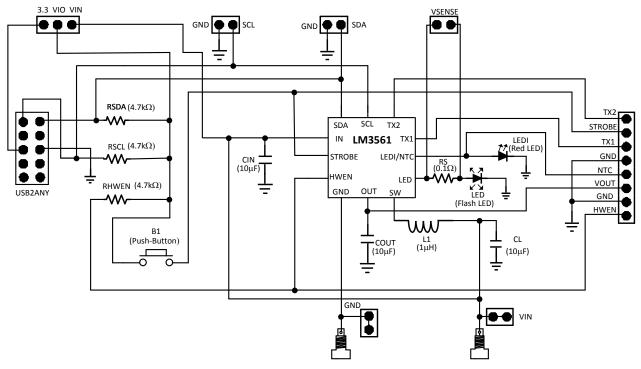


Figure 1. LM3561 Evaluation Board Schematic

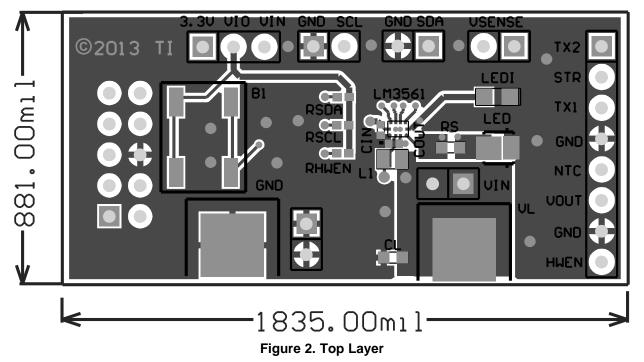
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2 LM3561 Evaluation Board Bill of Materials

Component Symbol	Value	Manufacturer	Part #	Size
LM3561	LED Driver	ТІ	LM3561	(0.4 mm × 1.215 mm × 1.615 mm)
L1	1 μ H, I _{DC_MAX} = 2.15A, R _L = 80mΩ	токо	MDT2012-CR1R0N	(2 mm x 1.25 mm x 0.9 mm)
CIN	10 µF, 6.3V	TDK	C1005X5R0J106M	0402 (1mm x 0.5 mm x 0.5 mm)
COUT	10 µF, 6.3V	TDK	C1005X5R0J106M	0402
CL	10 µF, 6.3V	TDK	C1608X5R1A106M	0603 (1.6 mm x 0.8 mm x 0.8 mm)
RHWEN	4.7 kΩ	Vishay-Dale	CRCW04024K70JNED	0603
RSDA	4.7 kΩ	Vishay-Dale	CRCW04024K70JNED	0603
RSCL	4.7 kΩ	Vishay-Dale	CRCW04024K70JNED	0603
RS	0.1Ω	Panasonic	ERJ-L03KF10CV	0603
LED	Flash LED	Lumileds	LXCL-EYW4	
LEDI	Red Indicator LED	Lite-On	LTST-C170KRKT	

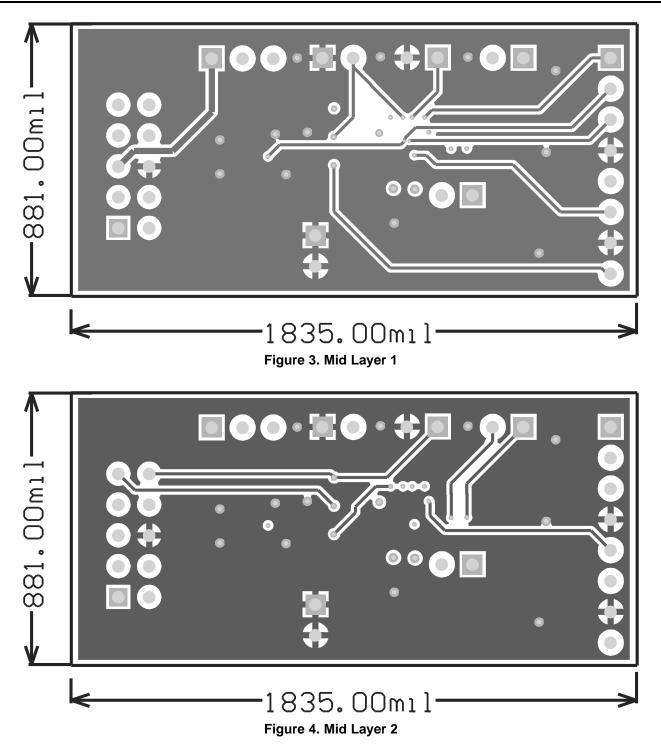
3 LM3561 Evaluation Board Layout

Figure 2, Figure 3, Figure 4, and Figure 5 show the board layout for the LM3561 Evaluation Board. This represents an ultra compact design which can deliver a 600 mA flash LED current with VIN down to 2.7V.

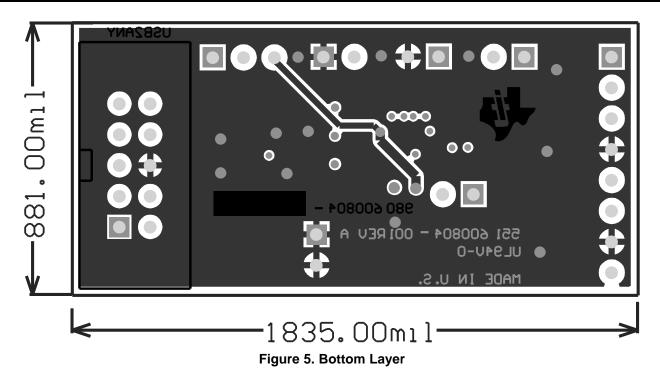




LM3561 Evaluation Board Layout







4 LM3561EVM Set-Up

To operate the LM3561EVM the following steps must be taken:

- 1. Download the LM3561 Graphical User Interface (GUI) from <u>www.ti.com</u>. The downloaded .zip folder consists of 2 files: the LM3561.exe and the USB2ANY.dll. The .zip file must be copied and the files extracted to a folder on a PC. Both files must reside in the same folder.
- 2. Ensure there is a jumper connected from VIO to VIN or from VIO to 3.3V. This is the jumper at the top left of the board. This will connect the I2C pull-ups, the HWEN pull-up, and the STROBE button (B1) to a logic voltage.
- Connect a (2.7V to 5.5V) power supply across the red and black banana plugs. Red (+) and Black (GND).
- 4. Connect the USB2ANY, USB Interface module with the supplied 10-pin cable to the LM3561 evaluation board.
- 5. Connect the USB2ANY, USB Interface module to the PC with the LM3561.exe installed.
- 6. Launch the LM3561.exe

5 LM3561.exe Graphical User Interface (GUI) Instructions

The GUI for the LM3561 is designed to fully demonstrate all the features of the LM3561 Flash LED Driver. The GUI provides the interface between the PC and the Texas Instruments USB2ANY, USB Interface Module. A picture of the interface program (LM3561.exe) is shown in Figure 6. The GUI is broken up into different sections that correspond to the available registers within the LM3561. Operation of the GUI is fairly basic. When any button is pushed/un-pushed or pull-down menu is selected, the appropriate I2C command will be automatically written to the LM3561. The following section lists the register descriptions for the LM3561.



6 LM3561 GUI

J TEXAS INSTRUMENTS	Simple I2C Interface	Configuration Register 1 (0xE0)
·	Address Data Real	Tarch Polarity Polarity Enable Polarity Enable
		Configuration Register 2 (0xF0)
nable Register (0x10) (bit 2) (bit 1) (bit 0)	Flash Brightness Register (0xB0) LED1 Flash Current (Bits 3 - 0)	VIN Monitor Shutdown AET Enable NTC TX2 Shutdown Shutdown
STR EN1 EN0	525mA v Flash	GPIO Register (0x20) (Bit6) GPIO2 Data (Bit GPIO2 Control GPIO1 Data (Bit GPIO1 Control
Flash Duration Register (0xC0)	Torch Brightness Register (0xA0)	INT Enable 5) (Bits 4-3) 2) (Bits 1-0)
Current Limit (Bits Flash Duration 5-5) (Bits 4-0)	Torch Current (Bits 2 - 0)	GPIO Read 0 GPIO2 Off 💌 0 GPIO1 Off 💌
1.5A 💌 512ms 💌	56.25mA ▼	Flags Register (0xD0)
VIN Monitor Register (0x80) VIN_MON_TH	Indicator Brightness Register (0x12) Current Level (Bits 2 - 0)	Hugs register (xxx) Flash Read VIN Mon NTC (BitTX2 TX1 LED Fault TSD Timeout Flags (Bit 7) 5) (Bit 4) (Bit 3) (Bit 2) (Bit 1) (Bit 0)
(Bits 2 - 1) 2.9∨ ▼ 0)	2.25mA 💌	

Figure 6. LM3561.exe GUI

7 LM3561 Register Descriptions

Bits[7:3]	Bit 2	Bits [1:0]
Not Used	(Strobe Level or Edge, STR bit)	Enable Bits
N/A	 0 = STROBE Input set for Level. Flash current turns on when STROBE input is high and turns off when STROBE either goes low or the Timeout Duration expires (default) 1 = STROBE Input set for edge triggered. Flash current turns on when STROBE sees a rising edge. Flash pulse turns off when timeout duration expires 	00 = Shutdown (default) 01 = Indicator Mode 10 = Torch Mode 11 = Elesh Mede (bits reset at timeout)

Table 2. Indicator Brightness Register Bit Settings	Table 2.	Indicator	Brightness	Register	Bit Settings
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Bits [7:3]	Bits [2:0] Indicate Current Settings	
Not Used		
N/A	000 = 2.25 mA (default) 001 = 4.5 mA 010 = 6.75 mA 011 = 9 mA 100 = 11.25 mA 101 = 13.5 mA 110 = 15.75 mA 111 = 18 mA	



LM3561 Register Descriptions

		Table		giotor Bit Co	ungo		
Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Not Used	TX2/GPIO2/INT Interrupt Output	TX2/GPIO2 data	TX2/GPIO2 data direction	TX2/GPIO2 Control	TX1/GPIO1 data	TX1/GPIO1 data direction	TX1/GPIO1 Control
N/A	0 = INT mode is disabled (default) 1 = When TX2/GPIO2 is configured as a GPIO output TX2/GPIO2/INT is set for INT mode and will pull low when either the LED Thermal Fault Flag is set or the VIN Monitor Flag is set	This bit is the read or write data for TX2/GPIO2 in GPIO mode (default is 0)	0 = TX2/GPIO2 is a GPIO Input (default) 1 = TX2/GPIO2 is a GPIO Output	0 = TX2/GPIO2 is a flash interrupt input (default) 1 = TX2/GPIO2 is configured as a GPIO	This bit is the read or write data for TX1/GPIO1 in GPIO mode (default is 0)	0 = TX1/GPIO1 is a GPIO input (default) 1 = TX1/GPIO1 is a GPIO output	0 = TX1/GPIO1 is configured as flash interrupt input(default) 1 = TX1/GPIO1 is configured as a GPIO

Table 3. GPIO Register Bit Settings

Table 4. VIN Monitor Register Bit Settings

Bits [7:3]	Bits [2:1]	Bit 0
Not Used	VIN Monitor Threshold Settings	VIN Monitor Enable
N/A	$\begin{array}{l} 00 = 2.9 \text{V threshold } (\text{V}_{\text{IN}} \text{ falling}) \ \textbf{Default} \\ 01 = 3.0 \text{V threshold } (\text{V}_{\text{IN}} \text{ falling}) \\ 10 = 3.1 \text{V threshold } (\text{V}_{\text{IN}} \text{ falling}) \\ 11 = 3.2 \text{V threshold } (\text{V}_{\text{IN}} \text{ falling}) \end{array}$	0 = VIN Monitor Comparator is disabled (default) 1 = VIN Monitor Comparator is enabled.

Table 5. GPIO Register Bit Settings

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Not Used	TX2/GPIO2/IN T Interrupt Output	TX2/GPIO2 data	TX2/GPIO2 data direction	TX2/GPIO2 Control	TX1/GPIO1 data	TX1/GPIO1 data direction	TX1/GPIO1 Control
N/A	0 = INT mode is disabled (default) 1 = When TX2/GPIO2 is configured as a GPIO output TX2/GPIO2/IN T is set for INT mode and will pull low when either the LED Thermal Fault Flag is set or the VIN Monitor Flag is set	This bit is the read or write data for TX2/GPIO2 in GPIO mode (default is 0)	0 = TX2/GPIO2 is a GPIO Input (default) 1 = TX2/GPIO2 is a GPIO Output	0 = TX2/GPIO2 is a flash interrupt input (default) 1 = TX2/GPIO2 is configured as a GPIO	This bit is the read or write data for TX1/GPIO1 in GPIO mode (default is 0)	0 = TX1/GPIO1 is a GPIO input (default) 1 = TX1/GPIO1 is a GPIO output	0 = TX1/GPIO1 is configured as flash interrupt input(default) 1 = TX1/GPIO1 is configured as a GPIO

LM3561 Register Descriptions

Bits [7:4]	Bits [3:0]		
Not Used	Flash Current Settings		
N/A	$\begin{array}{c} 0000 = 36 \text{ mA} \\ 0001 = 73.6 \text{ mA} \\ 0010 = 111.2 \text{ mA} \\ 0011 = 148.8 \text{ mA} \\ 0100 = 186.4 \text{ mA} \\ 0100 = 224 \text{ mA} \\ 0101 = 224 \text{ mA} \\ 0110 = 261.6 \text{ mA} \\ 0111 = 299.2 \text{ mA} \\ 1000 = 336.8 \text{ mA} \\ 1001 = 374.4 \text{ mA} \\ 1001 = 374.4 \text{ mA} \\ 1011 = 449.6 \text{ mA} \\ 1101 = 449.6 \text{ mA} \\ 1100 = 487.2 \text{ mA} \\ 1101 = 524.8 \text{ mA} (default) \\ 1110 = 562.4 \text{ mA} \\ 1111 = 600 \text{ mA} \end{array}$		

Table 6. Flash Brightness Register Bit Settings

Bit [7:6]	Bit 5	Bits [4:0]
Not Used	Current Limit Select	Flash Timeout Duration Settings
N/A	0 = 1A Peak Current Limit 1 = 1.5A Peak Current Limit (default)	$\begin{array}{l} 00000 = 32 \text{ ms timeout} \\ 00001 = 64 \text{ ms timeout} \\ 00010 = 96 \text{ ms timeout} \\ 00010 = 128 \text{ ms timeout} \\ 00101 = 128 \text{ ms timeout} \\ 00101 = 192 \text{ ms timeout} \\ 00101 = 224 \text{ ms timeout} \\ 00110 = 224 \text{ ms timeout} \\ 00111 = 256 \text{ ms timeout} \\ 01000 = 288 \text{ ms timeout} \\ 01001 = 320 \text{ ms timeout} \\ 01001 = 320 \text{ ms timeout} \\ 01011 = 344 \text{ ms timeout} \\ 01011 = 344 \text{ ms timeout} \\ 01110 = 448 \text{ ms timeout} \\ 01110 = 448 \text{ ms timeout} \\ 01111 = 512 \text{ ms timeout} \\ 10000 = 544 \text{ ms timeout} \\ 10001 = 576 \text{ ms timeout} \\ 10011 = 640 \text{ ms timeout} \\ 10101 = 736 \text{ ms timeout} \\ 10111 = 768 \text{ ms timeout} \\ 10111 = 768 \text{ ms timeout} \\ 10101 = 844 \text{ ms timeout} \\ 10111 = 768 \text{ ms timeout} \\ 11100 = 800 \text{ ms timeout} \\ 11001 = 844 \text{ ms timeout} \\ 11001 = 844 \text{ ms timeout} \\ 11001 = 736 \text{ ms timeout} \\ 11001 = 1024 \text{ ms timeout} \\ 11011 = 1024 \text{ ms timeout} \\ 11111 = 1024 \text{ ms timeout} \\ 1$



LM3561 Register Descriptions

Table 0. Trags Register bit Settings							
Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
VIN Monitor Flag	Not Used	LED Thermal Fault (NTC)	TX2 Interrupt	TX1 Interrupt	Led Fault (LEDF)	Thermal Shutdown (TSD)	Flash Timeout (TO)
0 = No Fault at VIN (default) 1 = Input Voltage Monitor is enabled and VIN has fallen below (V _{IN_TH})	N/A	$\begin{array}{l} 0 =& LEDI/NTC \\ \text{pin is above} \\ V_{TRIP} (default) \\ 1 =& LEDI/NTC \\ \text{has fallen below} \\ V_{TRIP} (NTC \text{ mode} \\ \text{only}) \end{array}$	0=TX2 has not changed state (default) 1=TX2 has changed state (TX2 mode only)	0=TX1/TORCH has not changed state (default) 1=TX1/TORCH pin has changed state (TX1 mode only)	0 = Proper LED Operation (default) 1 = LED Failed (Open or Short)	0 = Die Temperature below Thermal Shutdown Limit (default) 1 = Die Temperature has crossed the Thermal Shutdown Threshold of +150°C	0 = Flash timeout did not expire (default) 1 = Flash timeout Expired

Table 8. Flags Register Bit Settings

Table 9. Configuration Register 1 Bit Settings

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Hardware Torch Mode Enable	TX2 Polarity	TX1 Polarity	NTC Mode Enable	STROBE Polarity	STROBE Input Enable	Not Used	Not Used
0 = TX1/TORCH is a TX1 flash interrupt input (default) 1 = TX1/TORCH pin is a hardware TORCH enable. This bit is reset to 0 after a flash event.	0 = TX2 is set for active low polarity 1 = TX2 is set for active high polarity (default)	0 = TX1 is set for active low polarity 1 = TX1 is set for active high polarity (default)	0 = LEDI/NTC is a Indicator Current Source Output (default) 1 = LEDI/NTC is a Comparator Input for LED Temperature Sensing	0 = STROBE set for active low polarity 1 = STROBE set for active high polarity (default)	0 = STROBE Input Disabled (default) 1 = STROBE Input Enabled	N/A	N/A

Table 10. Configuration Register 2 Bit Settings

Bits [7:4]	Bit 3	Bit 2	Bit 1	Bit 0
Not Used	V _{IN} Monitor Shutdown	AET mode	NTC Shutdown	TX2 Shutdown
N/A	$ \begin{array}{l} 0 = \text{If IN drops below } V_{\text{IN_TH}} \\ \text{and the VIN Monitor feature} \\ \text{is enabled, the LEDs are} \\ \text{forced into Torch mode} \\ (\text{default}) \\ 1 = \text{If IN drops below } V_{\text{IN_TH}} \\ \text{and the VIN Monitor feature} \\ \text{is enabled, the LEDs turn} \\ \text{off} \end{array} $	0 = Normal operation for TX1/TORCH high before STROBE (TX1 mode only) default 1 = Alternate External Torch Mode. TX1/TORCH high before STROBE forces Torch mode with no timeout (TX1 mode only)	0 = LEDI/NTC pin going below V_{TRIP} forces the LEDs into Torch mode (NTC mode only) default 1 = LEDI/NTC pin going below V_{TRIP} forces the LEDs into shutdown (NTC mode only)	0 = TX2 interrupt event forces the flash LED into Torch mode (TX2 mode only) default 1 = TX2 interrupt event forces the flash LED into shutdown (TX2 mode only)

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- 3 Regulatory Notices:
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 - 3.1.1 Notice applicable to EVMs not FCC-Approved:

This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

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CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 Japan

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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