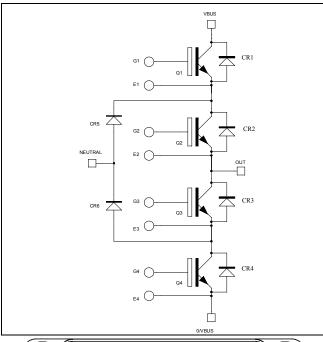
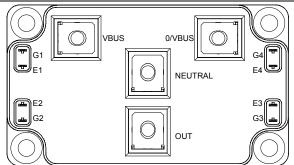


## Three level inverter Trench + Field Stop IGBT Power Module







### Application

- Solar converter
- Uninterruptible Power Supplies

#### **Features**

- Trench + Field Stop IGBT Technology
  - Low voltage drop
  - Low tail current
  - Switching frequency up to 20 kHz
  - Soft recovery parallel diodes
  - Low diode VF
  - Low leakage current
  - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
- High level of integration

#### Benefits

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile
- RoHS Compliant

### Q1 to Q4 Absolute maximum ratings (per IGBT)

Symbol	Parameter		Max ratings	Unit
$V_{CES}$	Collector - Emitter Breakdown Voltage		1700	V
Ţ	Continuous Collector Current	$T_C = 25^{\circ}C$	150	
$I_{C}$	Continuous Collector Current	$T_C = 80$ °C	100	A
$I_{CM}$	Pulsed Collector Current	$T_C = 25^{\circ}C$	200	
$V_{GE}$	Gate – Emitter Voltage		±20	V
$P_{D}$	Maximum Power Dissipation	$T_C = 25$ °C	560	W
RBSOA	Reverse Bias Safe Operating Area	$T_j = 125$ °C	200A @ 1600V	

These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



## All ratings @ $T_j = 25$ °C unless otherwise specified

## Q1 to Q4 Electrical Characteristics (per IGBT)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 1700V$				350	μΑ
V	Collector Emitter Saturation Voltage	$V_{GE} = 15V$ $T_j = 25^{\circ}C$		2.0	2.4	V	
$V_{CE(sat)}$	Conector Emitter Saturation Voltage	$I_{\rm C} = 100 A$	$T_{j} = 125^{\circ}C$		2.4		V
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 2mA$		5.0	5.8	6.5	V
$I_{GES}$	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE}$	= 0V			500	nA

### Q1 to Q4 Dynamic Characteristics (per IGBT)

_	Characteristic	Test Conditions		Min	Тур	Max	Unit
Cies	Input Capacitance	$V_{GE} = 0V$			9		
Coes	Output Capacitance	$V_{CE} = 25V$			0.36		nF
$C_{res}$	Reverse Transfer Capacitance	f = 1MHz		0.3			
$Q_{G}$	Gate charge	$V_{GE} = \pm 15V, I_{C} = 100$ $V_{CE} = 900V$	V <sub>GE</sub> =±15V, I <sub>C</sub> =100A V <sub>CE</sub> =900V				μС
$T_{d(on)}$	Turn-on Delay Time	Inductive Switch		370			
$T_{\rm r}$	Rise Time	$V_{GE} = 15V$			40		ns
$T_{d(off)}$	Turn-off Delay Time	$V_{Bus} = 900V$ $I_{C} = 100A$			650		
$T_{\mathrm{f}}$	Fall Time	$R_G = 4.7 \Omega$		180			
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) $V_{GE} = 15V$ $V_{Bus} = 900V$ $I_{C} = 100A$ $R_{G} = 4.7 \Omega$			400		ns
$T_{\rm r}$	Rise Time				50		
T <sub>d(off)</sub>	Turn-off Delay Time				800		
$T_{\mathrm{f}}$	Fall Time				300		
Eon	Turn-on Switching Energy	$V_{GE} = 15V$ $V_{Bus} = 900V$	$T_j = 125$ °C		32		I
$E_{\text{off}}$	Turn-off Switching Energy	$I_C = 100A$ $R_G = 4.7 \Omega$	$T_j = 125$ °C		31		mJ
$I_{sc}$	Short Circuit data	$V_{GE} \le 15V$ ; $V_{Bus}$ $t_p \le 10 \mu s$ ; $T_i = 1$			400		A
$R_{thJC}$	Junction to Case Thermal Resistance					0.22	°C/W



## CR1 to CR4 diode ratings and characteristics (per diode)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			1700			V
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> =1700V	$T_j = 25^{\circ}C$			350	μA
Kivi	5	R	$T_j = 125$ °C			600	
$I_{\mathrm{F}}$	DC Forward Current		$Tc = 80^{\circ}C$		100		A
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_{\rm F} = 100A$	$T_i = 25^{\circ}C$		1.8	2.2	V
<b>v</b> <sub>F</sub>	Diode Forward Voltage	$I_{\rm F} - 100A$	$T_{i} = 125^{\circ}C$		1.9		V
t <sub>rr</sub>	Reverse Recovery Time		$T_j = 25^{\circ}C$		385		ns
ι <sub>rr</sub>	Reverse Recovery Time		$T_j = 125$ °C		490		115
$Q_{rr}$	Reverse Recovery Charge	$I_{\rm F} = 100 A$	$T_j = 25$ °C		28		μC
Vп	Reverse Recovery Charge	$V_R = 900V$ di/dt = 1600A/µs	$T_j = 125$ °C		46		μ
$\mathrm{E}_{\mathrm{rr}}$	Daviana Dagayany Enganay		$T_j = 25$ °C		12		I
	Reverse Recovery Energy	Reverse Recovery Emergy	$T_{j} = 125^{\circ}C$		24		mJ
$R_{thJC}$	Junction to Case Thermal Resistance					0.39	°C/W

### CR5 & CR6 diode ratings and characteristics (per diode)

Symbol	Characteristic	Test Conditions	Test Conditions		Тур	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			1700			V
T	Marine Branch Ladau Commit	$V_{R}=1700V$	$T_j = 25^{\circ}C$			350	4
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> -1700 V	$T_{j} = 125^{\circ}C$			600	μA
$I_F$	DC Forward Current		$Tc = 80^{\circ}C$		150		A
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_{\rm F} = 150A$	$T_i = 25^{\circ}C$		1.8	2.2	V
<b>v</b> <sub>F</sub>	Diode Forward Voltage	$I_{\rm F} = 130A$	$T_i = 125$ °C		1.9		V
+	Reverse Recovery Time		$T_j = 25^{\circ}C$		385		ns
t <sub>rr</sub>	Reverse Recovery Time		$T_{i} = 125^{\circ}C$		490		115
	Reverse Recovery Charge	$I_F = 150A$ $V_R = 900V$	$T_j = 25$ °C		38		ııC
$Q_{rr}$	Reverse Recovery Charge	$V_R = 900 V$ di/dt = 1600A/µs	$T_j = 125$ °C		62		μC
Е	$T_j = 25^{\circ}C$	$T_j = 25$ °C		17.5		mJ	
$E_{rr}$			$T_{j} = 125^{\circ}C$		35		1113
$R_{thJC}$	Junction to Case Thermal Resistance					0.26	°C/W

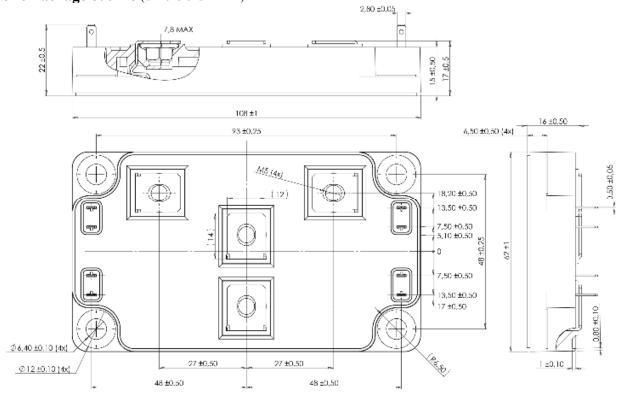
### Thermal and package characteristics

Symbol	Characteristic			Min	Typ	Max	Unit
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz			4000			V
$T_{J}$	Operating junction temperature range			-40		150	
T <sub>STG</sub>	Storage Temperature Range	orage Temperature Range				125	°C
$T_{\rm C}$	Operating Case Temperature		-40		100		
Torque	Mounting torque	To heatsink	M6	3	5	N.m	
Torque	Mounting torque	For terminals	M5	2		3.5	IN.III
Wt	Package Weight					300	g

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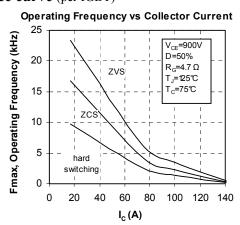


### SP6 Package outline (dimensions in mm)

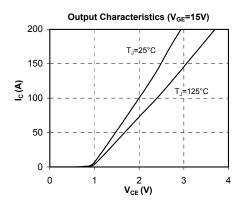


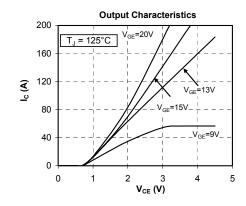
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

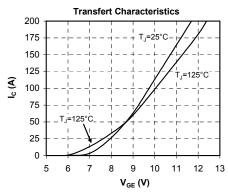
### Q1 to Q4 Typical performance curve (per IGBT)

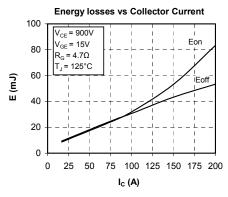


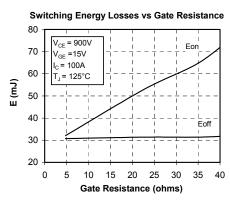


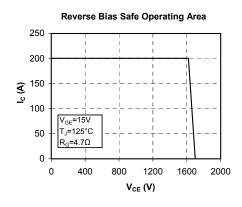


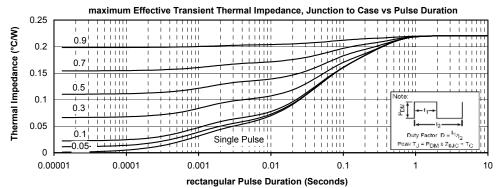






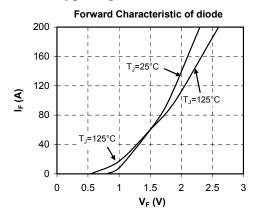




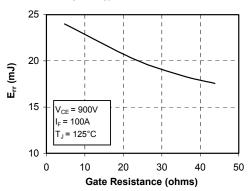


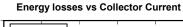


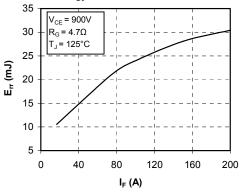
### CR1 to CR4 Typical performance curve (per diode)



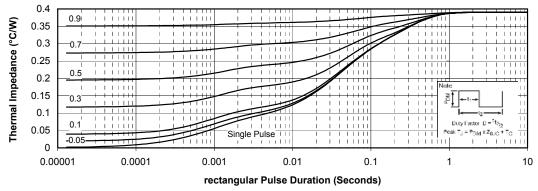
#### Switching Energy Losses vs Gate Resistance





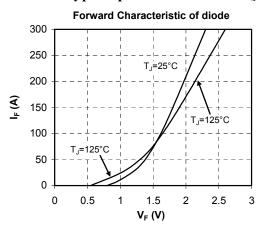


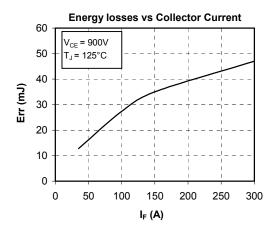
#### maximum Effective Transient Thermal Impedance, Junction to Pulse Duration

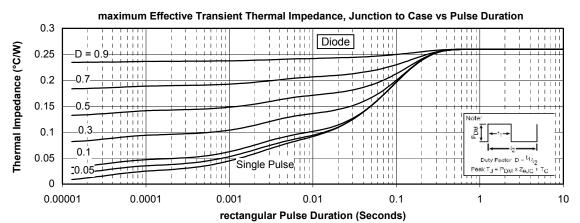




### CR5 & CR6 Typical performance curve (per diode)









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