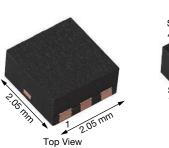
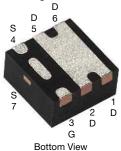
www.vishay.com

Vishay Siliconix

Automotive P-Channel 20 V (D-S) 175 °C MOSFET





Marking Code: QGXXXX

PRODUCT SUMMARY				
V _{DS} (V)	-20			
$R_{DS(on)}$ (Ω) at V_GS = -4.5 V	0.113			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -2.5 V$	0.200			
I _D (A)	-2.68			
Configuration	Single			
Package	PowerPAK SC-70			

PowerPAK[®] SC-70-6L Single

FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified ^d
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



(1, 2, 5, 6) D (3) G (3) G (3) G (3) G (3) G (4) G (4)

ABSOLUTE MAXIMUM RATING	iS (T _C = 25 °C, unles:	s otherwise notec)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-20	M	
Gate-source voltage		V _{GS}	± 8	V	
Continuous drain current	T _C = 25 °C	- I _D	-2.68		
	T _C = 125 °C		-1.55		
Continuous source current (diode conduction) ^a		I _S	3.75	А	
Pulsed drain current ^b		I _{DM}	10		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-7		
Single pulse avalanche energy	L = 0.1 mm	E _{AS}	2.45	mJ	
Maximum power dissipation ^b	T _C = 25 °C	- P _D	13.6	W	
	T _C = 125 °C		4.5	vv	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-ambient	PCB mount ^c	R _{thJA}	90	°C/W		
lunction-to-case (drain)		R _{thJF}	11	0/10		

Notes

- a. Package limited
- b. Pulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%$
- c. When mounted on 1" square PCB (FR4 material)

d. Parametric verification ongoing

1



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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static		1					-	
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA		-20	-	-	v	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$		-1.0	-1.5		
		$V_{DS} = 0 V$, $V_{GS} = \pm 3 V$		-	-	± 100	nA	
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 8 V$		-	± 5		
Zero gate voltage drain current		$V_{GS} = 0 V$	V _{DS} = -20 V	-	-	-1	1	
	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = -20 V, T _J = 125 °C	-	-	-50	μA	
		$V_{GS} = 0 V$	V _{DS} = -20 V, T _J = 175 °C	-	-	-150		
On-state drain current ^a	I _{D(on)}	V _{GS} = -4.5 V	$V_{DS} \ge 5 V$	-8	-	-	Α	
Drain-source on-state resistance ^a		V _{GS} = -4.5 V	I _D = -2 A	- 0.093 0.113	0.113			
	D D	V _{GS} = -4.5 V	I _D = -2 A, T _J = 125 °C	-	-	0.161	Ω	
	R _{DS(on)}	V _{GS} = -4.5 V	l _D = -2 A, T _J = 175 °C	-	-	0.184		
		V _{GS} = -2.5 V	I _D = -2 A	-	0.165	0.200		
Forward transconductance b	g _{fs}	V _{DS} = -10 V, I _D = -2 A		-	4.7	-	S	
Dynamic ^b								
Input capacitance	C _{iss}			-	298	375		
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{GS} = 0 V V _{DS} = -10 V, f = 1 MHz		104	130	pF	
Reverse transfer capacitance	C _{rss}			-	56	70	1	
Total gate charge ^c	Qg		V _{DS} = -10 V, I _D = -2.4 A	-	4.2	5.3		
Gate-source charge c	Q _{gs}	V _{GS} = -4.5 V		-	0.75	-	nC	
Gate-drain charge ^c	Q _{gd}			-	1.2	-		
Gate resistance	Rg	f = 1 MHz		5.1	8.6	14	Ω	
Turn-on delay time ^c	t _{d(on)}			-	9	11.1		
Rise time ^c	t _r	V_{DD} = -10 V, R _L = 5.3 Ω I _D \cong -1.9 A, V _{GEN} = -4.5 V, R _g = 1 Ω		-	17	21.4	- ns	
Turn-off delay time ^c	t _{d(off)}			-	19	24		
Fall time ^c	t _f			-	8	10		
Source-Drain Diode Ratings and Cha	acteristics							
Pulsed current ^a	I _{SM}			-	-	-12.7	Α	
Forward voltage	V _{SD}	$I_{F} = -2 \text{ A}, V_{GS} = 0$		_	-0.8	-1.2	V	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

c. Independent of operating temperature

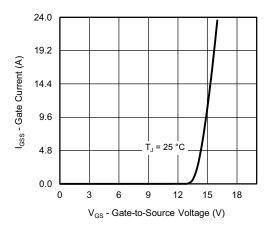
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2

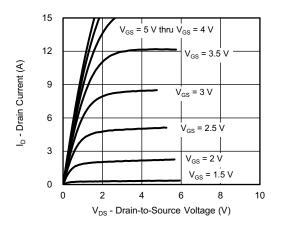


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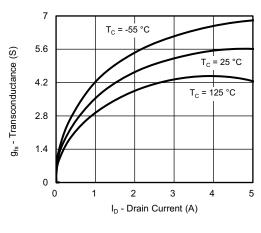
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



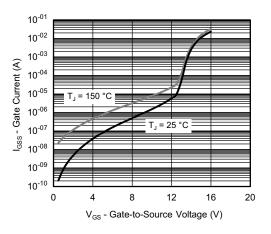
Gate Current vs. Gate-Source Voltage



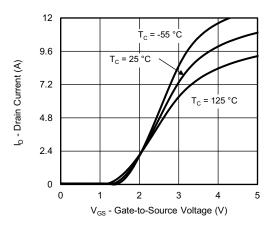
Output Characteristics



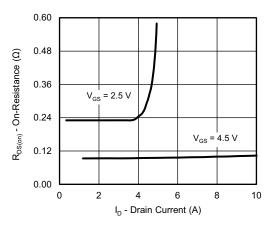
Transconductance



Gate Current vs. Gate-Source Voltage



Transfer Characteristics



On-Resistance vs. Drain Current

S18-0063-Rev. B, 29-Jan-18

3

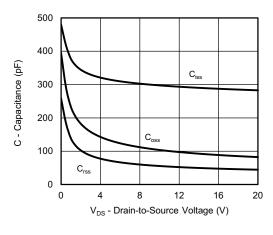
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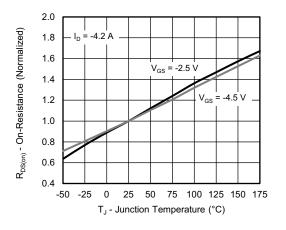


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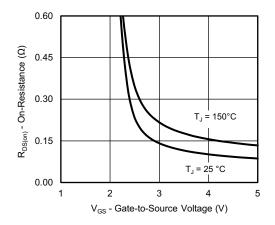
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



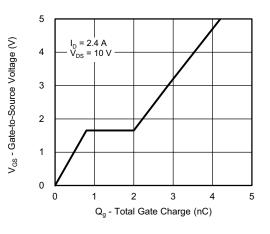
Capacitance



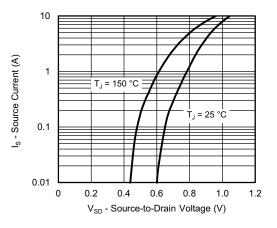
On-Resistance vs. Junction Temperature



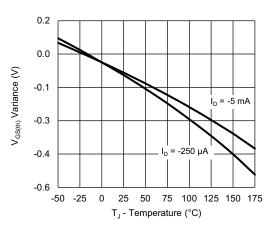
On-Resistance vs. Gate-to-Source Voltage



Gate Charge



Source-Drain Diode Forward Voltage



Threshold Voltage

4

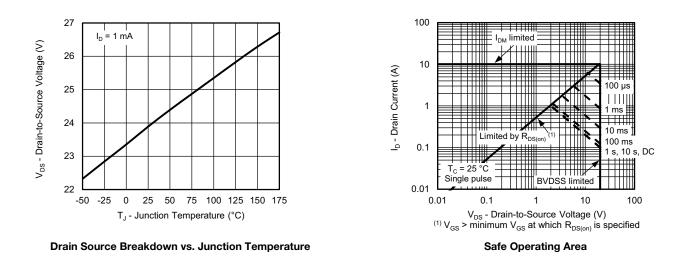
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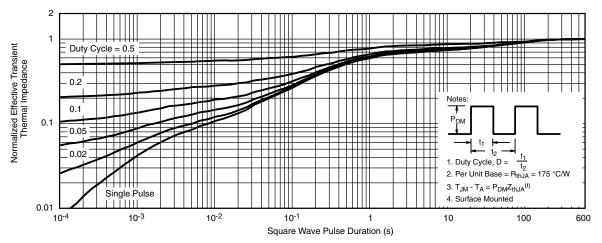


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TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



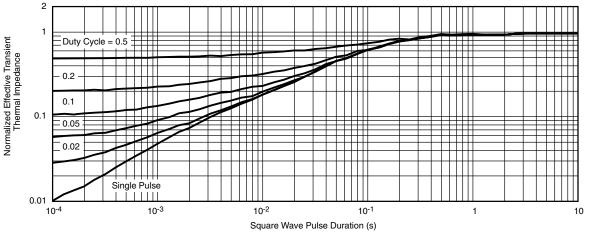
Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

• The characteristics shown in the two graphs

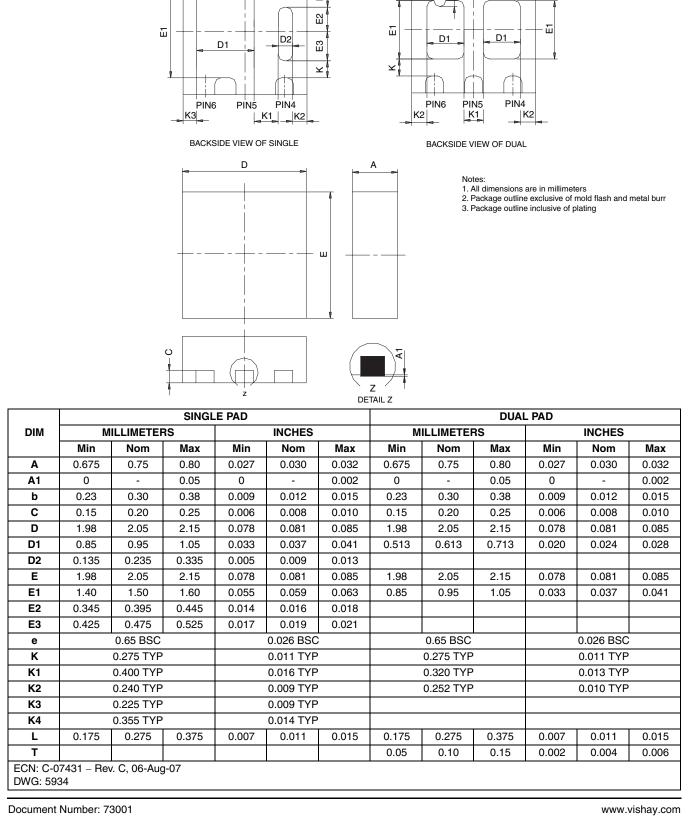
S18-0063-Rev. B, 29-Jan-18

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?75957.

6



PowerPAK[®] SC70-6L

b PIN2 PIN1 PIN3 _ ₹

Package Information Vishay Siliconix

_ ₿

b

PIN3

PIN2

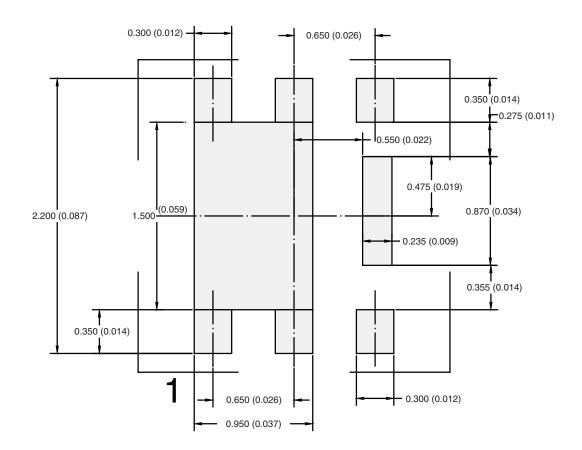
PIN1

¥

VISHA



RECOMMENDED PAD LAYOUT FOR PowerPAK[®] SC70-6L Single



Dimensions in mm/(Inches)

Return to Index



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